



**SPECIFICATION  
FOR  
LCM+CTP Module  
KD050FWFPA011**

MODULE:	KD050FWFPA011
CUSTOMER:	

REV	DESCRIPTION	DATE
1.0	FIRST ISSUE	2016.07.21

STARTEK	INITIAL	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

CUSTOMER	INITIAL	DATE
APPROVED BY		

ISO9001:2008  
ISO16949  
2009



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**\* Description**

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This model is composed of a Transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 5.0'TFT-LCD contains 480x854 pixels, and can display up to 65K/262K/16.7M colors.

**\* Features**

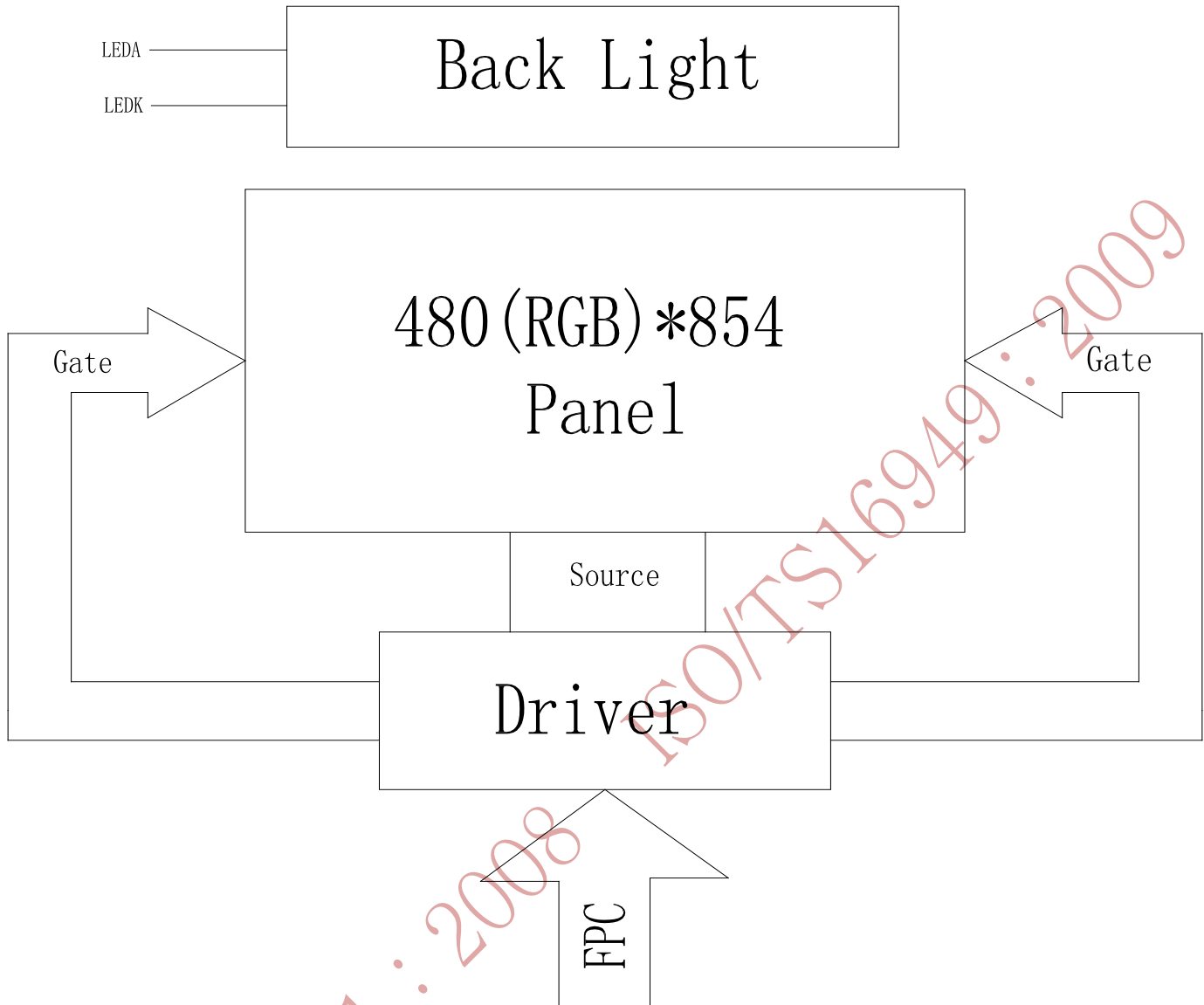
- Low Input Voltage: 3.3V(TYP)
- Display Colors of TFT LCD: 65K/262K/16.7M colors
- Interface: 3-SPI+16/18/24-bits RGB interface.

General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	61.632(H)*109.6536(V) (5.0inch)	mm	-
Driver element	TFT active matrix	-	-
Display colors	65K/262K/16.7M	colors	-
Number of pixels	480(RGB)*854	dots	-
TFT Pixel arrangement	RGB vertical stripe	-	-
Pixel pitch	0.1284(H)*0.1284(V)	mm	-
Viewing angle	ALL	o'clock	-
Controller IC	ILI9806E	-	-
Display mode	Transmissive/Normally Black	-	-
Operating temperature	-20~+70	°C	-
Storage temperature	-30~+80	°C	-

**\* Mechanical Information**

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)		67.56		mm	-
	Vertical(V)		122.35		mm	-
	Depth(D)		2.6		mm	-
Weight			--		g	-

### 1. Block Diagram



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	常备库存 Standing Stock	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range



### 3. Input terminal Pin Assignment

NO.	SYMBOL	DISCRIPTION	I/O
1	XR(NC)	Touch panel Right Glass Terminal	A/D
2	YD(NC)	Touch panel Bottom Film Terminal	A/D
3	XL(NC)	Touch panel LIFT Glass Terminal	A/D
4	YU(NC)	Touch panel Top Film Terminal	A/D
5	GND	Ground.	P
6	GND	Ground.	P
7	VCI	Supply voltage (3.3V).	P
8	IOVCC	I/O power supply voltage.	P
9	SDO	SPI interface output pin.-The data is output on the falling edge of the SCL signal.-If not used, let this pin open.	O
10	SDI	Data lane in 1 data lane serial interface. The data is latched on the rising edge of the SCL signal.	I
11	SCL	This pin is used to select "Data or Command" in the parallel interface. When D/CX = '1', data is selected. When D/CX = '0', command is selected. This pin is used serial interface clock in 3-wire 9-bit / 4-wire 8-bit serial data interface. fix this pin at VCI or GND when not in use.	I
12	CS	Chip select input pin ("Low" enable). fix this pin at VCI or GND when not in use.	I
13	RESET	Reset pin. Setting either pin low initializes the LSI. Must be reset after power is supplied.	I
14-37	DB23-DB0	24-bit parallel bi-directional data bus for MCU system and RGB interface mode .Fix to GND level when not in use	I/O
38	DE	Data enable signal for RGB interface peration. fix this pin at VCI or GND when not in use.	I
39	DOTCLK	Dot clock signal for RGB interface operation. Fix this pin at VCI or GND when not in use.	I
40	HSYNC	Line synchronizing signal for RGB interface operation. fix this pin at VCI or GND when not in use.	I



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41	VSYNC	Frame synchronizing signal for RGB interface operation. fix this pin at VCI or GND when not in use.	I
42	NC		
43	LEDK	Cathode pin of backlight.	P
44	NC		
45	LEDA	Anode pin of backlight.	P

ISO9001 : 2008  
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## 4. LCD Optical Characteristics

### 4.1 Optical specification

Item	Symbol	Condition	Min.	Typ.	Max.	Unit.	Note	
Contrast Ratio	CR	$\Theta=0$ Normal viewing angle	640	800	--		(1)(2)	
Response time	Rising		$T_R$	--	16	21	msec	(1)(3)
	Falling		$T_F$	--	19	24		
Color gamut	S(%)			--	70	--	%	C-light
Color Filter Chromaticity	White		$W_X$	--	0.305	--	-	(1)(4) CF glass
			$W_Y$	--	0.340	--		
	Red		$R_X$	--	--	--		
			$R_Y$	--	--	--		
	Green		$G_X$	--	--	--		
			$G_Y$	--	--	--		
	Blue	$B_X$	--	--	--			
		$B_Y$	--	--	--			
Viewing angle	Hor.	$\Theta_L$	--	80	--	-	(1)(4) Measuring with Polarizer, Reference Only	
		$\Theta_R$	--	80	--			
	Ver.	$\Theta_U$	--	80	--			
		$\Theta_D$	--	80	--			
Option View Direction			Free					

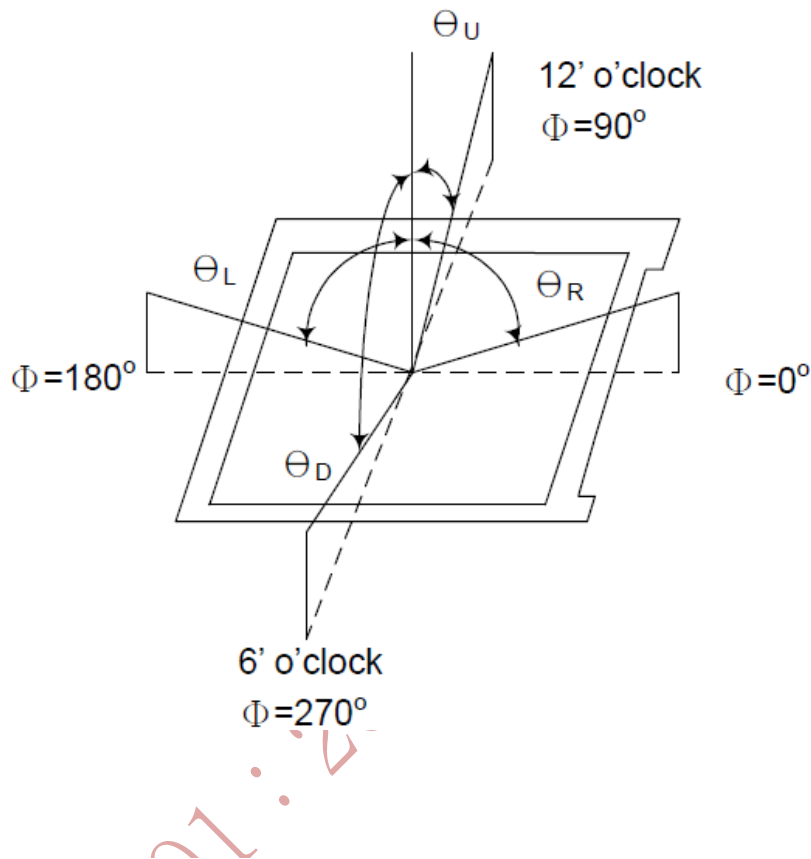
### 4.2 Measuring Condition

- Measuring surrounding: dark room
- Ambient temperature:  $25 \pm 2^\circ\text{C}$
- 15min. warm-up time.

### 4.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

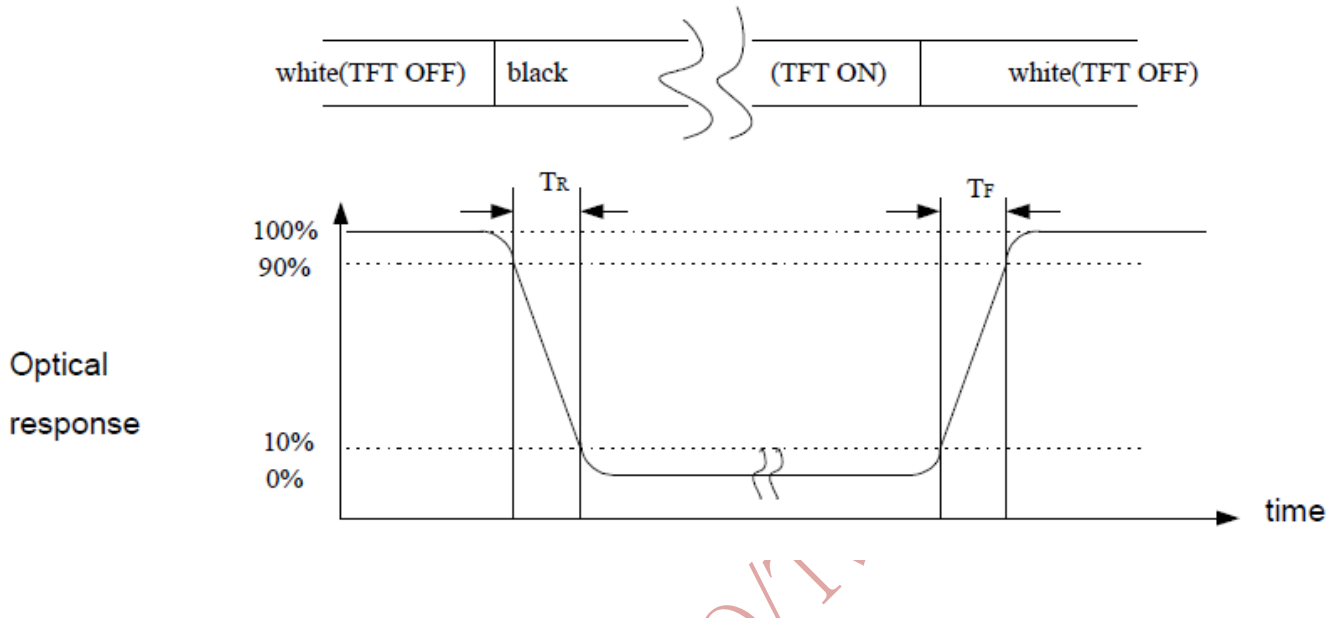
Note (1) Definition of Viewing Angle:



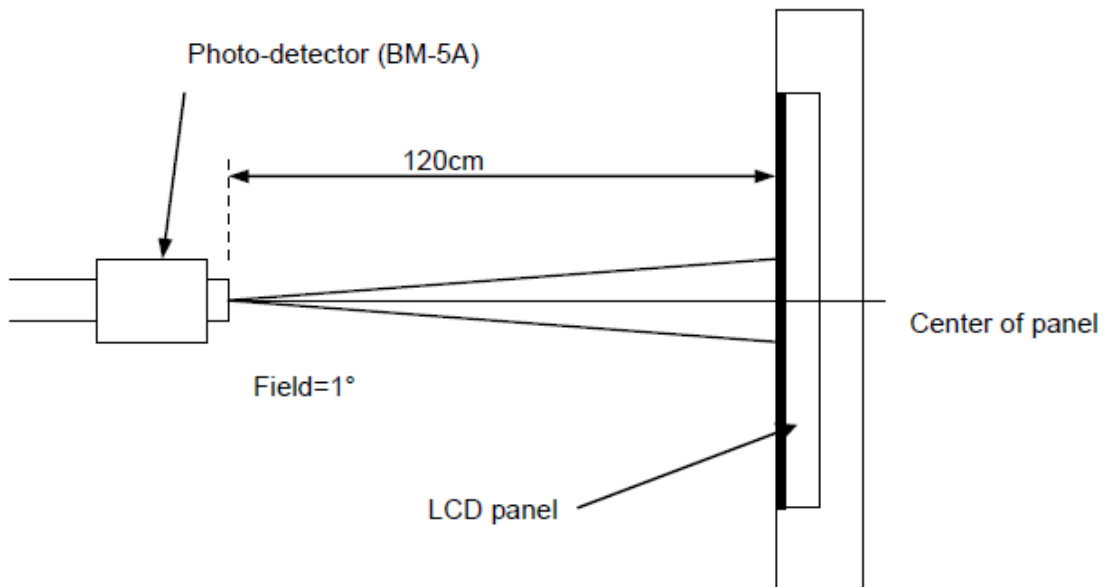
Note (2) Definition of Contrast Ratio (CR) :  
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3) Definition of Response Time : Sum of  $T_R$  and  $T_F$



Note (4) Definition of optical measurement setup



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常备库存  
Standing Stock

长期供货  
Long Time supply

支持小量  
NO MOQ

品种齐全  
In Full Range

## 5. TFT Electrical Characteristics

### 5.1 Absolute Maximum Rating (Ta=25 VSS=0V)

Characteristics	Symbol	Min.	Max.	Unit
Digital Supply Voltage	VDD	-0.3	4.6	V
Digital interface supply Voltage	VDDIO	-0.3	4.6	V
Operating temperature	T <sub>OP</sub>	-20	+70	°C
Storage temperature	T <sub>ST</sub>	-30	+80	°C

NOTE: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

### 5.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	VDD	2.5	2.8	3.6	V	--
Digital interface supply Voltage	VDDIO	1.65	1.8	3.3	V	--
Normal mode Current consumption	IDD	--	30	--	mA	--
Level input voltage	V <sub>IH</sub>	0.7V <sub>DDIO</sub>	--	V <sub>DDIO</sub>	V	--
	V <sub>IL</sub>	-0.3	--	0.3V <sub>DDIO</sub>	V	--
Level output voltage	V <sub>OH</sub>	0.8*V <sub>DDIO</sub>	--	V <sub>DDIO</sub>	V	--
	V <sub>OL</sub>	GND	--	0.2V <sub>DDIO</sub>	V	--

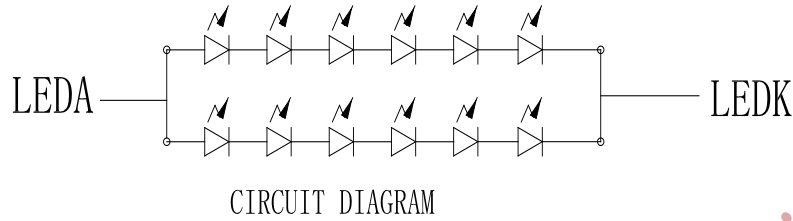
### 5.3 LED Backlight Characteristics

The back-light system is edge-lighting type with 12 chips White LED

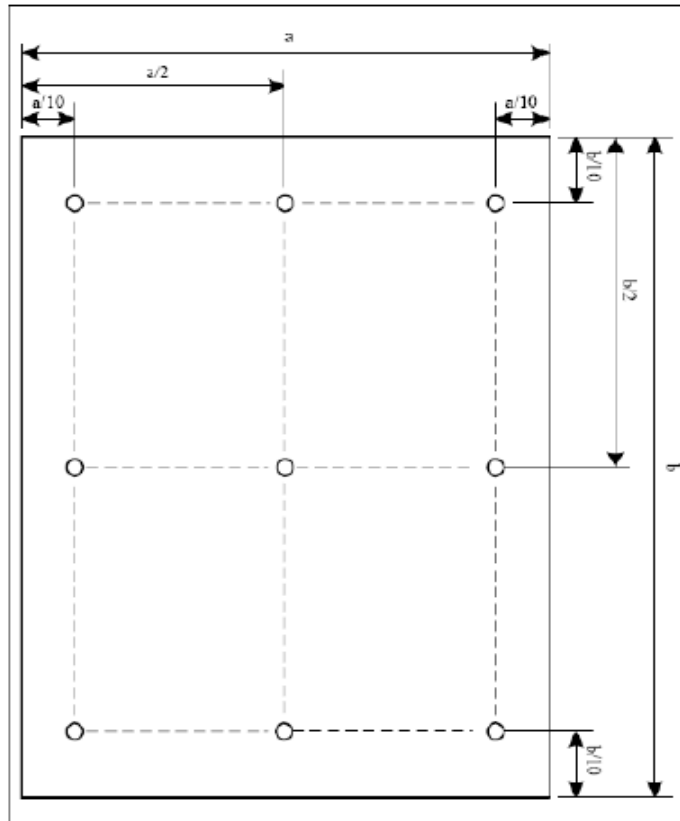
Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I <sub>F</sub>	30	40	--	mA	--
Forward Voltage	V <sub>F</sub>	--	19.2	--	V	--
LCM Luminance	L <sub>V</sub>	--	520	--	cd/m <sup>2</sup>	Note3
LED life time	Hr	50000			Hour	Note1,2
Uniformity	AV <sub>g</sub>	80	--	--	%	Note3

Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition:  $T_a=25\pm3\text{ }^\circ\text{C}$ , typical IL value indicated in the above table until the brightness becomes less than 50%.

Note (2) The "LED life time" is defined as the module brightness decrease to 50% original brightness at  $T_a=25^\circ\text{C}$  and  $I_L=40\text{mA}$ . The LED lifetime could be decreased if operating  $I_L$  is larger than 40mA. The constant current driving method is suggested.



NOTE 3: Luminance Uniformity of these 9 points is defined as below:



$$\text{Uniformity} = \frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$$

$$\text{Luminance} = \frac{\text{Total Luminance of 9 points}}{9}$$

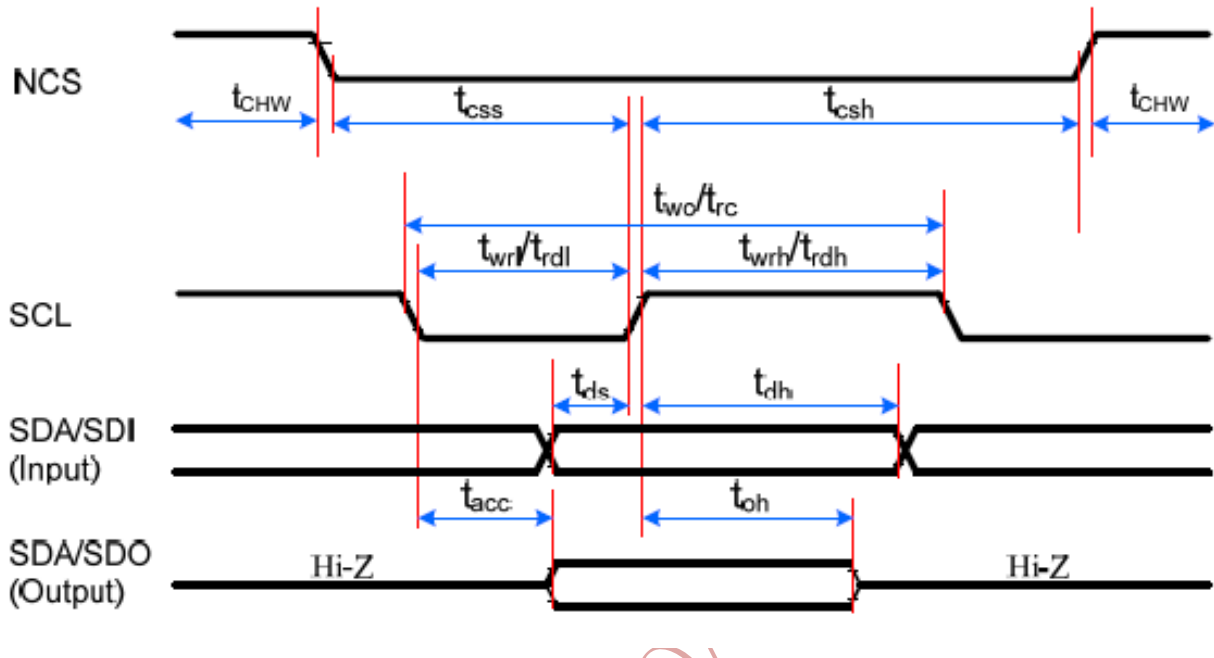
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## 6. TFT AC Characteristic

### 6.1 Display Serial Interface Timing Characteristics (3-line SPI system)

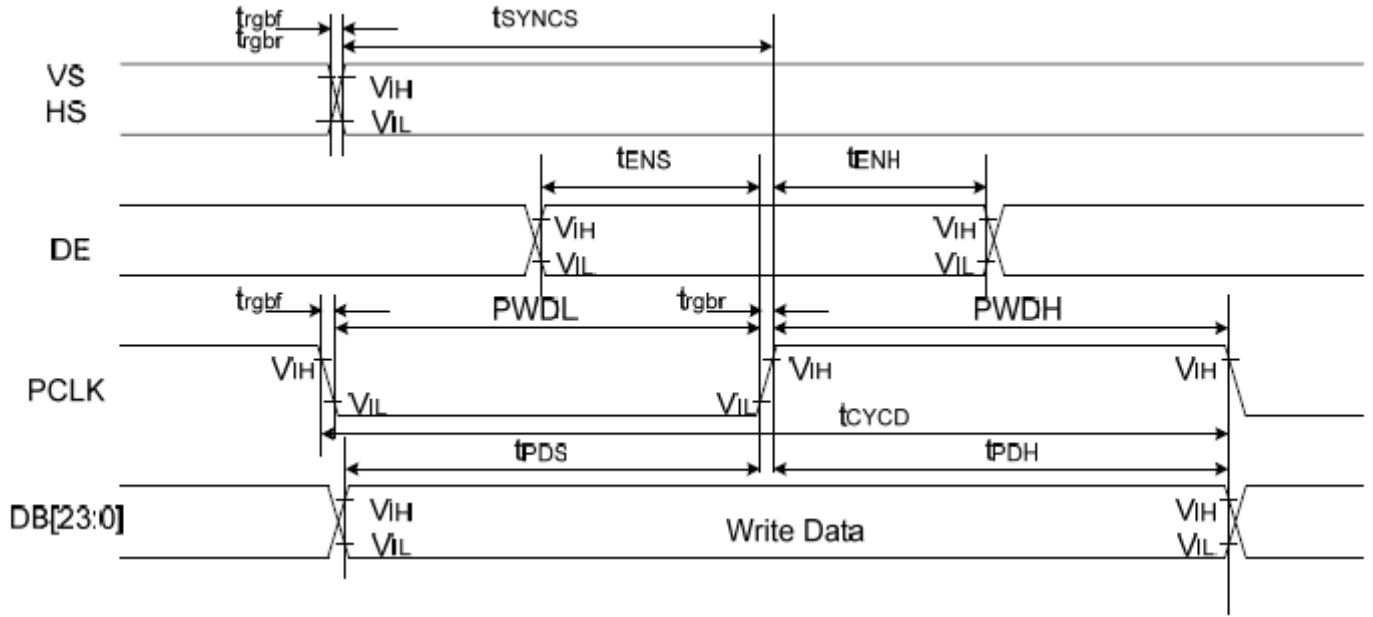


Signal	Symbol	Parameter	min	max	Unit	Description
CSX	t <sub>CSS</sub>	Chip select time (Write)	15	-	ns	
	t <sub>CSH</sub>	Chip select hold time (Read)	15	-	ns	
	t <sub>CHW</sub>	CS "H" pulse width	40	-	ns	
SCL	t <sub>WC</sub>	Serial clock cycle (Write)	30	-	ns	
	t <sub>WRH</sub>	SCL "H" pulse width (Write)	10	-	ns	
	t <sub>WRL</sub>	SCL "L" pulse width (Write)	10	-	ns	
	t <sub>TRC</sub>	Serial clock cycle (Read)	150	-	ns	
	t <sub>TRDH</sub>	SCL "H" pulse width (Read)	60	-	ns	
	t <sub>TRDL</sub>	SCL "L" pulse width (Read)	60	-	ns	
SDA/SDO (Output)	t <sub>ACC</sub>	Access time (Read)	10	100	ns	For maximum CL=30pF
	t <sub>OH</sub>	Output disable time (Read)	15	100	ns	For minimum CL=8pF
SDA/SDI (Input)	t <sub>DS</sub>	Data setup time (Write)	10	-	ns	
	t <sub>DH</sub>	Data hold time (Write)	10	-	ns	

Note:

1. Ta = -30 to 70 °C, IOVCC=1.65V to 3.6V, VCI=2.5V to 3.6V, T=10+/-0.5ns.
2. Does not include signal rise and fall times.

## 6.2 Parallel 24/18/16-bit RGB Interface Timing Characteristics

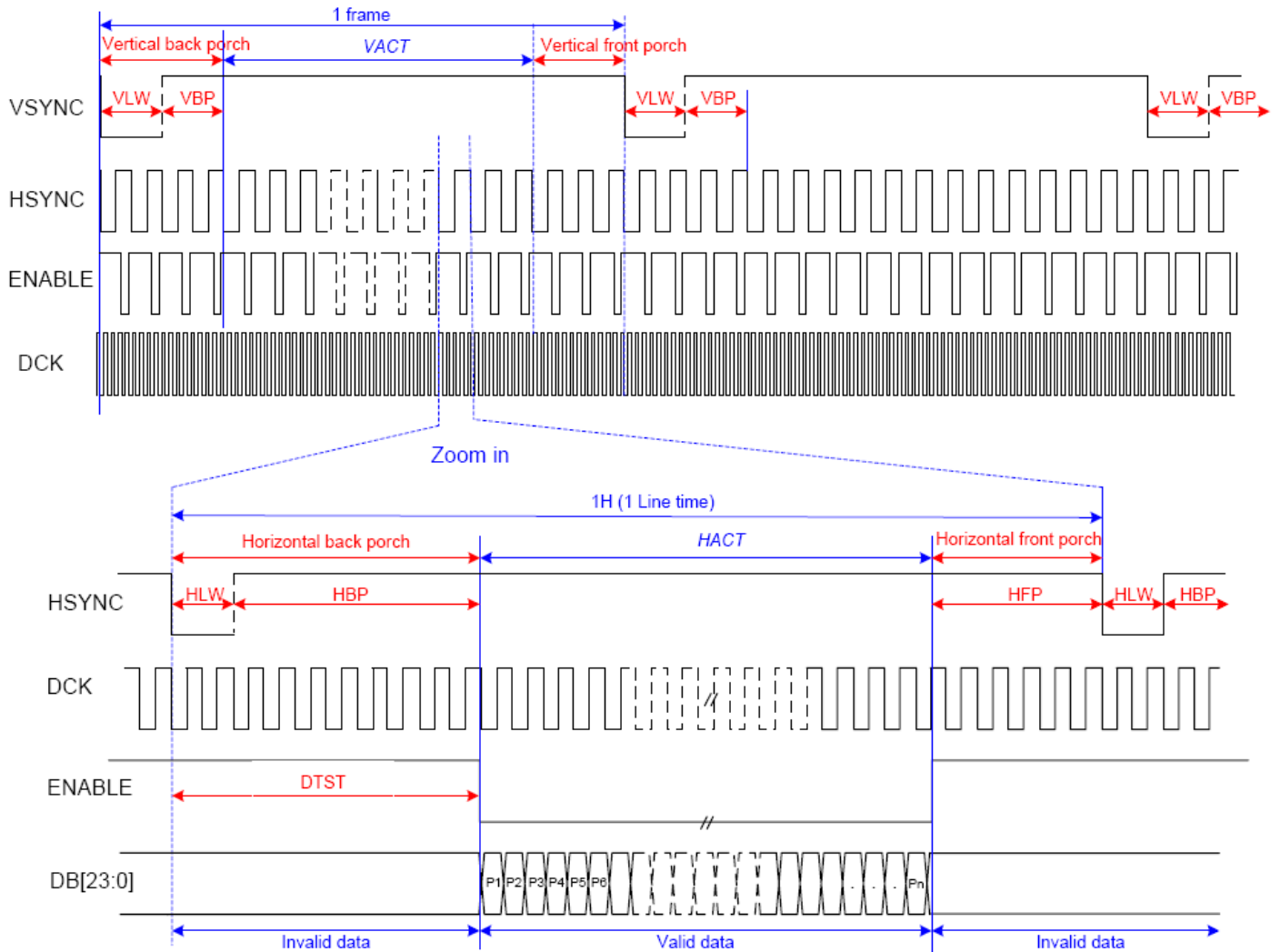


Signal	Symbol	Parameter	min	max	Unit	Description
VS/ HS	$t_{SYNCS}$	VS/HS setup time	5	-	ns	24/18/16-bit bus RGB interface mode
	$t_{SYNCH}$	VS/HS hold time	5	-	ns	
DE	$t_{ENS}$	DE setup time	5	-	ns	
	$t_{ENH}$	DE hold time	5	-	ns	
DB[23:0]	$t_{POS}$	Data setup time	5	-	ns	
	$t_{PDH}$	Data hold time	5	-	ns	
PCLK	$PWDH$	PCLK high-level period	13	-	ns	
	$PWDL$	PCLK low-level period	13	-	ns	
	$t_{CYCD}$	PCLK cycle time	28	-	ns	
	$t_{rgbr}, t_{rgbf}$	PCLK,HS,VS rise/fall time	-	15	ns	

Note:  $T_a = -30$  to  $70$  °C,  $IOVCC=1.65V$  to  $3.6V$ ,  $VCI=2.5V$  to  $3.6V$ ,  $DGND=0V$

ISO9001

### 6.3 DPI Interface Timing



VLW : VSYNC Low pulse Width  
 HLW : HSYNC Low pulse Width  
 DTST : Data Transfer Startup Time  
 Pn : pixel 1, pixel 2..., pixel n.

Parameter	Symbols	Condition	Min.	Typ.	Max.	Units
Frame Rate	FR		54		66	fps
Horizontal Low Pulse width	HLW		1		-	DOTCLK
Horizontal Back Porch	HBP		2		126	DOTCLK
Horizontal Address	HACT			480		DOTCLK
Horizontal Front Porch	HFP		2		-	DOTCLK
Vertical Low Pulse width	VLW		1		126	Line
Vertical Back Porch	VBP		1		126	Line
Vertical Address	VACT				864	Line
Vertical Front Porch	VFP		1		255	Line
Data Clock	DCLK		16.6		41.7	MHz



## 6.4 Reset input timing

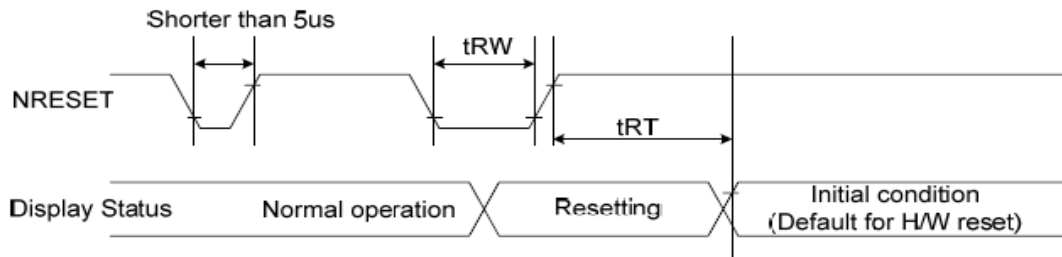


Figure 102 Reset Timing

Table 41 Reset Timing

Signal	Symbol	Parameter	Min	Max	Unit
RESX	tRW	Reset pulse duration	10		us
	tRT	Reset cancel		5(note 1,5) 120 (note 1,6,7)	ms

Note:

1. The reset cancel includes also required time for loading ID bytes, VCOM setting and other settings from OTP to registers. This loading is done every time when there is H/W reset cancel time (tRT) within 5 ms after a rising edge of RESX.
2. Spike due to an electrostatic discharge on RESX line does not cause irregular system reset according to the Table 43.

Table 42 Reset Descript

RESX Pulse	Action
Shorter than 5us	Reset Rejected
Longer than 9us	Reset
Between 5us and 9us	Reset starts

3. During the Resetting period, the display will be blanked (The display is entering blanking sequence, which maximum time is 120 ms, when Reset Starts in Sleep Out mode. The display remains the blank state in Sleep In mode.) and then return to Default condition for Hardware Reset.
4. Spike Rejection also applies during a valid reset pulse as shown below:

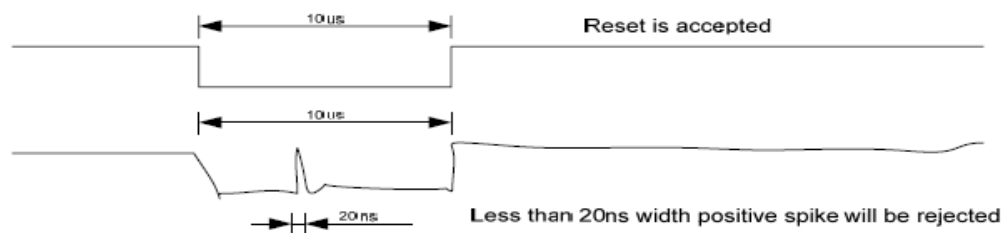


Figure 103 Positive Noise Pulse during Reset Low

5. When Reset applied during Sleep In Mode.
6. When Reset applied during Sleep Out Mode.
7. It is necessary to wait 5msec after releasing RESX before sending commands. Also Sleep Out command cannot be sent for 120msec.

## 7 LCD Module Out-Going Quality Level

### 7.1 VISUAL & FUNCTION INSPECTION STANDARD

#### 7.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

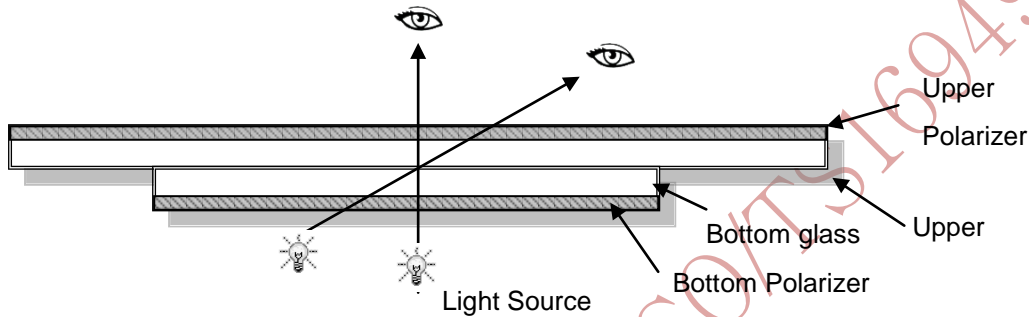
Temperature : 25±5°C

Humidity : 65%±10%RH

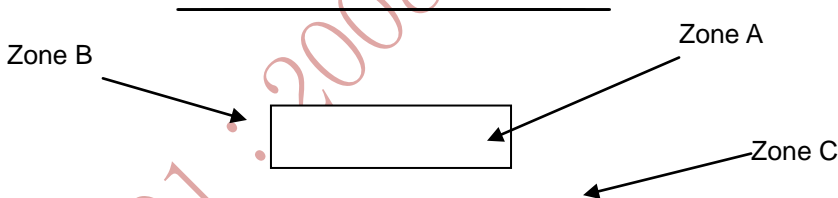
Viewing Angle : Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance:30-50cm



#### 7.1.2 Definition



Zone A : Effective Viewing Area(Character or Digit can be seen)

Zone B : Viewing Area except Zone A

Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .)

Note:

As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer.

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### 7.1.3 Sampling Plan

According to GB/T 2828-2003 ; , normal inspection, Class II

AQL:

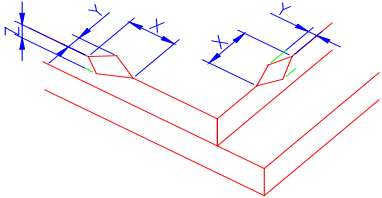
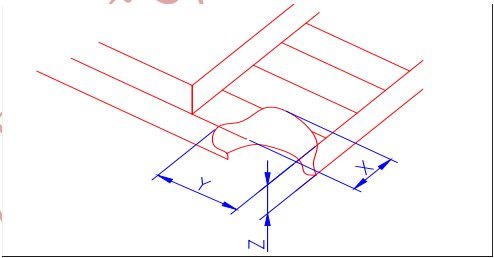

Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , TP: Touch Panel , LCM: Liquid Crystal Module

No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. 4) TP no function	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Soldering appearance	Good soldering , Peeling off is not allowed.	
6	LCD/Polarizer/TP	Black/White spot/line, scratch, crack, etc.	

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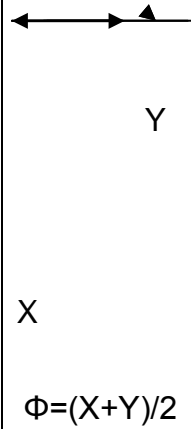
7.1.4 Criteria (Visual)

Number	Items	Criteria(mm)						
1.0 LCD Crack/Broken  NOTE: X: Length Y: Width Z: Height L: Length of ITO, T: Height of LCD	(1) The edge of LCD broken	 <table border="1" data-bbox="868 667 1441 819"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>≤3.0mm</td> <td>&lt;Inner border line of the seal</td> <td>≤T</td> </tr> </tbody> </table>	X	Y	Z	≤3.0mm	<Inner border line of the seal	≤T
X	Y	Z						
≤3.0mm	<Inner border line of the seal	≤T						
	(2)LCD corner broken	 <table border="1" data-bbox="932 1155 1377 1256"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>≤3.0mm</td> <td>≤L</td> <td>≤T</td> </tr> </tbody> </table>	X	Y	Z	≤3.0mm	≤L	≤T
X	Y	Z						
≤3.0mm	≤L	≤T						
	(3) LCD crack	 <p>Crack Not allowed</p>						

ISO 9001:2008

ISO 17025



Number	Items	Criteria (mm)																											
2.0	Spot defect  $\Phi = (X+Y)/2$	① light dot (LCD/TP/Polarizer black/white spot , light dot, pinhole, dent, stain) <table border="1" data-bbox="446 358 1308 716"> <thead> <tr> <th rowspan="2">Zone Size (mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.10</math></td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.10 &lt; \Phi \leq 0.20</math></td> <td colspan="3">3( distance <math>\geq 10\text{mm}</math>)</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.25</math></td> <td colspan="3">2</td> </tr> <tr> <td><math>\Phi &gt; 0.25</math></td> <td colspan="3">0</td> </tr> </tbody> </table>	Zone Size (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.10$	Ignore			$0.10 < \Phi \leq 0.20$	3( distance $\geq 10\text{mm}$ )			$0.20 < \Phi \leq 0.25$	2			$\Phi > 0.25$	0						
		Zone Size (mm)		Acceptable Qty																									
			A	B	C																								
		$\Phi \leq 0.10$	Ignore																										
		$0.10 < \Phi \leq 0.20$	3( distance $\geq 10\text{mm}$ )																										
		$0.20 < \Phi \leq 0.25$	2																										
		$\Phi > 0.25$	0																										
		② Dim spot (LCD/TP/Polarizer dim dot, light leakage, dark spot) <table border="1" data-bbox="446 761 1308 1120"> <thead> <tr> <th rowspan="2">Zone Size (mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.1</math></td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.10 &lt; \Phi \leq 0.20</math></td> <td colspan="3">3( distance <math>\geq 10\text{mm}</math>)</td> </tr> <tr> <td><math>0.20 &lt; \Phi \leq 0.30</math></td> <td colspan="3">2</td> </tr> <tr> <td><math>\Phi &gt; 0.30</math></td> <td colspan="3">0</td> </tr> </tbody> </table>	Zone Size (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.1$	Ignore			$0.10 < \Phi \leq 0.20$	3( distance $\geq 10\text{mm}$ )			$0.20 < \Phi \leq 0.30$	2			$\Phi > 0.30$	0						
		Zone Size (mm)		Acceptable Qty																									
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$\Phi > 0.30$	0																												
③ Polarizer accidented spot <table border="1" data-bbox="446 1164 1308 1456"> <thead> <tr> <th rowspan="2">Zone Size (mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.2</math></td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.3 &lt; \Phi \leq 0.5</math></td> <td colspan="3">2( distance <math>\geq 10\text{mm}</math>)</td> </tr> <tr> <td><math>\Phi &gt; 0.5</math></td> <td colspan="3">0</td> </tr> </tbody> </table>	Zone Size (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.2$	Ignore			$0.3 < \Phi \leq 0.5$	2( distance $\geq 10\text{mm}$ )			$\Phi > 0.5$	0												
Zone Size (mm)		Acceptable Qty																											
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$0.3 < \Phi \leq 0.5$	2( distance $\geq 10\text{mm}$ )																												
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Line defect (LCD/TP /Polarizer black/white line, scratch, stain)	<table border="1" data-bbox="446 1523 1308 1881"> <thead> <tr> <th rowspan="2">Width(mm)</th> <th rowspan="2">Length(mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> </thead> <tbody> <tr> <td><math>\Phi \leq 0.03</math></td> <td>Ignore</td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.03 &lt; W \leq 0.05</math></td> <td><math>L \leq 3.0</math></td> <td colspan="3"><math>N \leq 2</math></td> </tr> <tr> <td><math>0.05 &lt; W \leq 0.08</math></td> <td><math>L \leq 2.0</math></td> <td colspan="3"><math>N \leq 2</math></td> </tr> <tr> <td><math>0.08 &lt; W</math></td> <td colspan="4">Define as spot defect</td> </tr> </tbody> </table>	Width(mm)	Length(mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.03$	Ignore	Ignore			$0.03 < W \leq 0.05$	$L \leq 3.0$	$N \leq 2$			$0.05 < W \leq 0.08$	$L \leq 2.0$	$N \leq 2$			$0.08 < W$	Define as spot defect			
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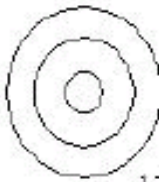


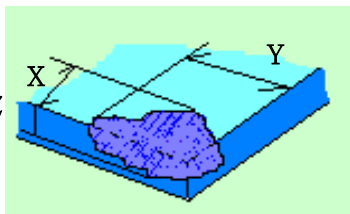
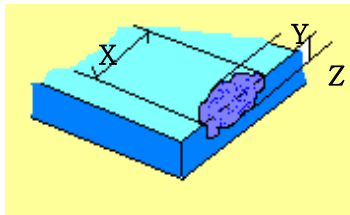


3.0	Polarizer Bubble	<table border="1"> <tr> <th rowspan="2">Zone Size (mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> <tr> <td><math>\Phi \leq 0.2</math></td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.2 &lt; \Phi \leq 0.4</math></td> <td colspan="3">3 (distance <math>\geq 10\mu\text{m}</math>)</td> </tr> <tr> <td><math>0.4 &lt; \Phi \leq 0.6</math></td> <td colspan="3">2</td> </tr> <tr> <td><math>0.6 &lt; \Phi</math></td> <td colspan="3">0</td> </tr> </table>			Zone Size (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.2$	Ignore			$0.2 < \Phi \leq 0.4$	3 (distance $\geq 10\mu\text{m}$ )			$0.4 < \Phi \leq 0.6$	2			$0.6 < \Phi$	0		
		Zone Size (mm)	Acceptable Qty																								
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$0.4 < \Phi \leq 0.6$	2																										
$0.6 < \Phi$	0																										
4.0	SMT	According to IPC-A-610C class II standard . Function defect and missing part are major defect ,the others are minor defect.																									

		TP bubble/ accidented spot	<table border="1"> <tr> <th rowspan="2">Size <math>\Phi</math>(mm)</th> <th colspan="3">Acceptable Qty</th> </tr> <tr> <th>A</th> <th>B</th> <th>C</th> </tr> <tr> <td><math>\Phi \leq 0.1</math></td> <td colspan="3">Ignore</td> </tr> <tr> <td><math>0.1 &lt; \Phi \leq 0.25</math></td> <td colspan="3">3 (distance <math>\geq</math></td> </tr> <tr> <td><math>0.25 &lt; \Phi \leq 0.3</math></td> <td colspan="3">2</td> </tr> <tr> <td><math>0.3 &lt; \Phi</math></td> <td colspan="3">0</td> </tr> </table>			Size $\Phi$ (mm)	Acceptable Qty			A	B	C	$\Phi \leq 0.1$	Ignore			$0.1 < \Phi \leq 0.25$	3 (distance $\geq$			$0.25 < \Phi \leq 0.3$	2			$0.3 < \Phi$	0		
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$0.25 < \Phi \leq 0.3$	2																											
$0.3 < \Phi$	0																											
		Assembly deflection	beyond the edge of backlight $\leq 0.15\text{mm}$																									

ISO9001:2008

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5.0	TP Related	Newton Ring	<p>Newton Ring area &gt; 1/3 TP area NG</p> <p>Newton Ring area ≤ 1/3 TP area OK</p>			 <p>1 规律性</p>  <p>2 非规律性</p>  <p>似牛顿环</p>					
			<p>TP corner broken</p> <p>X : length</p> <p>Y : width</p> <p>Z : height</p>	<table border="1"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>X ≤ 3.0mm</td> <td>Y ≤ 3.0mm</td> <td>Z &lt; LCD thickness</td> </tr> </table> <p>* Circuitry broken is not allowed.</p>	X	Y	Z	X ≤ 3.0mm	Y ≤ 3.0mm	Z < LCD thickness	
			X	Y	Z						
X ≤ 3.0mm	Y ≤ 3.0mm	Z < LCD thickness									
<p>TP edge broken</p> <p>X : length</p> <p>Y : width</p> <p>Z : height</p>	<table border="1"> <tr> <td>X</td> <td>Y</td> <td>Z</td> </tr> <tr> <td>X ≤ 6.0mm</td> <td>Y ≤ 2.0mm</td> <td>Z &lt; LCD thickness</td> </tr> </table> <p>* Circuitry broken is not allowed.</p>	X	Y	Z	X ≤ 6.0mm	Y ≤ 2.0mm	Z < LCD thickness				
X	Y	Z									
X ≤ 6.0mm	Y ≤ 2.0mm	Z < LCD thickness									

Criteria ( functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed
5	TP no function	Not allowed



## 8. Reliability Test Result

### 8.1 Condition

Item	Condition	Sample Size	Test Result	Note
Low Temperature Operating Life test	-20°C, 96HR	3ea	pass	-
Thermal Humidity Operating Life test	70°C90%RH, 96HR	3ea	pass	-
Temperature Cycle ON/OFF test	-20°C ↔ 70°C, ON/OFF, 20CYC	3ea	pass	(1)
High Temperature Storage test	80°C, 96HR	3ea	pass	-
Low Temperature Storage test	- 30°C, 96HR	3ea	pass	-
ESD test	150pF, 330Ω, ±6KV(Contact)/± 8KV(Air), 5 points/panel, 10 times/point	3ea	pass	
Thermal Shock Resistance	The sample should be allowed to stand the following 5 cycles of operation: TSTL for 30 minutes -> normal temperature for 5 minutes -> TSTH for 30 minutes -> normal temperature for 5 minutes, as one cycle, then taking it out and drying it at normal temperature, and allowing it stand for 24 hours	3ea	pass	
Box Drop Test	1 Corner 3 Edges 6 faces, 66cm(MEDIUM BOX)	1box	pass	-

Note (1) ON Time over 10 seconds, OFF Time under 10 seconds

Part. No	KD050FWFPA011	REV	V1.0	Page 24 of 26
	常备库存 Standing Stock	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range



## 9. Cautions and Handling Precautions

### 9.1 Handling and Operating the Module

- (1) When the module is assembled, it should be attached to the system firmly.  
Do not warp or twist the module during assembly work.
- (2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.
- (3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.
- (4) Do not allow drops of water or chemicals to remain on the display surface.  
If you have the droplets for a long time, staining and discoloration may occur.
- (5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.  
Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.
- (8) Protect the module from static; it may cause damage to the CMOS ICs.
- (9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (10) Do not disassemble the module.
- (11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (12) Pins of I/F connector shall not be touched directly with bare hands.
- (13) Do not connect, disconnect the module in the "Power ON" condition.
- (14) Power supply should always be turned on/off by the item 6.1 Power On Sequence & 6.2 Power Off Sequence

### 9.2 Storage and Transportation.

- (1) Do not leave the panel in high temperature, and high humidity for a long time.  
It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%
- (2) Do not store the TFT-LCD module in direct sunlight.
- (3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.
- (4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.  
In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.
- (5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.

Part. No	KD050FWFPA011	REV	V1.0	Page 25 of 26
	常备库存 Standing Stock	长期供货 Long Time supply	支持少量 NO MOQ	品种齐全 In Full Range

**10. Packing**

----TBD-----

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Part. No	KD050FWFPA011	REV	V1.0	Page 26 of 26
	常备库存 Standing Stock	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range