

PRODUCT 产品名称	: OLED MODULE 有机发光显示模块
MODEL NO. 模块型号	: OEL9M1053-Y-E
SUPPLIER 生产商	: TRULY SEMICONDUCTORS LTD. 信利半导体有限公司
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CERT. No. QAC0946535
(ISO9001)



CERT. No. HKG002005
(ISO14001)

OLED SPECIFICATION

OLED 产品说明书

OEL9M1053-Y-E

Version: 1.1

This module uses ROHS material
模块使用环保材料

This specification maybe changed without any notice in order to improve performance or quality etc.
出于提高性能或质量等目的，本规格书有可能在不作任何通知的情况下进行修改。
Please contact TRULY Semiconductors LTD. OLED R&D department for updates specification and product status before design for this product or release the order.
在进行产品设计或下达订单前，请与信利半导体有限公司 OLED 开发部索取最新规格书。

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REVISION RECORD
修订记录

REV NO. 版本号	REV DATE 修改记录	CONTENTS 内容	REMARKS 备注
1.0	2018-4-27	First release.	Written by Ivan.
1.1	2018-5-18	Update the Inspection Criteria.	Written by Ivan.

WRITTEN BY 编写	CHECKED BY 审核	APPROVED BY 审批
Hua Shineng	Chen Yongquan	Li Liumin

■ PHYSICAL DATA (物理参数)

NO.编号	Items 项目	Specification 规格	Unit 单位
1	Diagonal Size 尺寸	2.70	Inch
2	Resolution 分辨率	128 x 64	Dots
3	Active Area 有效显示区域	61.41(W) x 30.69(H)	mm ²
4	Outline Dimension 外围尺寸	73.00(W) x 40.24(H)	mm ²
5	Pixel Pitch 像素间距	0.48 (W) x 0.48 (H)	mm ²
6	Pixel Size 像素尺寸	0.45 (W) x 0.45 (H)	mm ²
7	Driver IC 驱动 IC	SSD1327Z	-
8	Display Color 显示色彩	Yellow	-
9	Gray Scale 灰阶	4	Bit
10	Interface 接口	4-SPI/I2C/parallel	-
11	IC Package Type IC 封装类型	COG	-
12	Module Connecting Type 模块连接方式	ZIF	-
13	Thickness 厚度	2.00 ±0.15	mm
14	Weight 重量	TBD±10%	g
15	Duty 占空比	1/64	-

■ ABSOLUTE MAXIMUM RATINGS (极限参数)

Unless otherwise specified, VSS = 0V

(Ta=25°C)

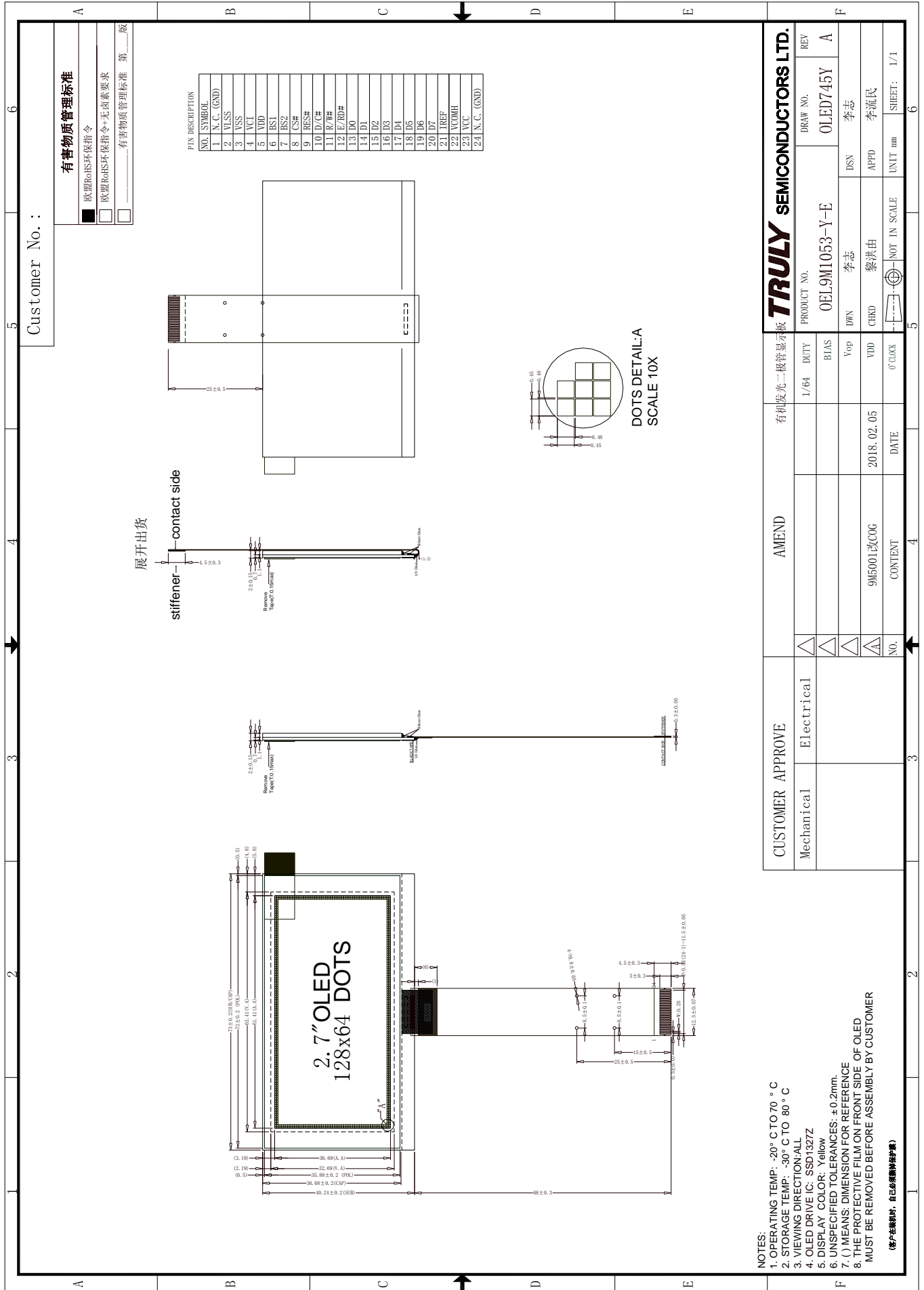
除另行规定外, VSS=0V

Items 项目	Min. 最小值	Max. 最大值	Unit 单位	Remark 备注
Supply Voltage(VDD) 逻辑电压 (VDD)	-0.3	+4.0	V	IC maximum rating IC 极限参数
Supply Voltage(VCC) 驱动电压 (VCC)	-0.3	+18.0	V	IC maximum rating IC 极限参数
Operating Temperature(T _{OP}) 操作温度 (T _{OP})	-20	70	°C	-
Storage Temperature(T _{ST}) 存储温度 (T _{SP})	-30	80	°C	Note 2

NOTE:

1. Permanent device damage may occur if ABSOLUTE MAXIMUM RATINGS are exceeded. Functional operation should be restricted to the conditions as detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect reliability.
如果超过极限参数可能会导致器件永久性损坏。实际工作条件应仅限于在本数据表格中详述的操作部分。在极限参数条件下长时间工作会影响其可靠性。
2. The defined temperature ranges do not include the polarizer. The maximum withstood temperature of the polarizer should be 80 °C.
定义的温度范围不包括偏光片。偏光片的最大值耐温应为 80°C。
3. Humidity: Temperature should be 29°C max. and no condensation of water.
湿度:最高温度应不超过 29°C, 且不可凝结水珠。

EXTERNAL DIMENSIONS (外形尺寸)



■ **ELECTRICAL CHARACTERISTICS (电气特性)**

◆ **DC Characteristics (直流特性)**

Unless otherwise specified, Voltage referenced to VSS;

除另行规定外，参考电平为 VSS;

VCI=3.0V, VCC = 15.0V, IREF = 10uA,

(Ta = 25 ± 3°C, 60 ± 10%RH)

Items 项目		Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位
Supply Voltage 供电电压	Logic Supply Voltage IC 逻辑电压	V _{DD}	2.6	3.0	3.3	V
	Operating (for OLED panel) 操作电压 (供 OLED 面板)	V _{CC}	14.50	15.0	15.50	V
Input Voltage 输入电压	High Voltage 高电平	V _{IH}	0.8 x V _{CI}	-	V _{CI}	V
	Low Voltage 低电平	V _{IL}	V _{SS}	-	0.2 x V _{CI}	V
Output Voltage 输出电压	High Voltage 高电平	V _{OH}	0.8 x V _{CI}	-	V _{CI}	V
	Low Voltage 低电平	V _{OL}	V _{SS}	-	0.2 x V _{CI}	V

◆ **AC Characteristics (交流特性)**

Conditions:

Voltage referenced to V_{SS} 参考电压为 V_{SS}

V_{DD}=2.4 to 2.6V

T_A = 25°C

AC Characteristics

Symbol	Parameter	Test Condition	Min	Typ	Max	Unit
F _{OSC} ⁽¹⁾	Oscillation Frequency of Display Timing Generator 显示时钟发生器的震荡频率	V _{CI} = 2.8V	535	595	655	kHz
F _{FRM}	Frame Frequency 帧频	128x128 Graphic Display Mode, Display ON, Internal Oscillator Enabled 128x128 图像显示模式, 显示开, 使能内部振荡器	-	F _{OSC} x 1/(DxKx39) ⁽²⁾	-	Hz
RES#	Reset low pulse width 复位脉冲持续时间		2000	-	-	us

Note:

(1) F_{OSC} stands for the frequency value of the internal oscillator and the value is measured when command B3h A[7:4] is in default value.

F_{OSC} 表示内部振荡器的频率值, 该值在 B3h 设定为缺省值时测得。

(2) D: divide ratio

D: 分频比

K: number of display clocks per row period

K: 每行显示周期的时钟数

Please refer to (Set Display Clock Divide Ratio/Oscillator Frequency, B3h) for detail description
详细的描述请参考 (设置时钟分频比/震荡频率, B3h)

1. Serial Interface Timing Characteristics (4-wire SPI)串行接口时序特性 (4-SPI)

Serial Interface Timing Characteristics (4-wire SPI)

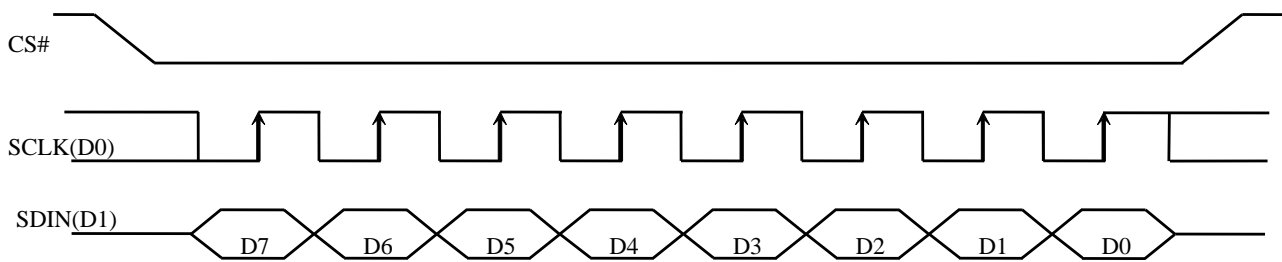
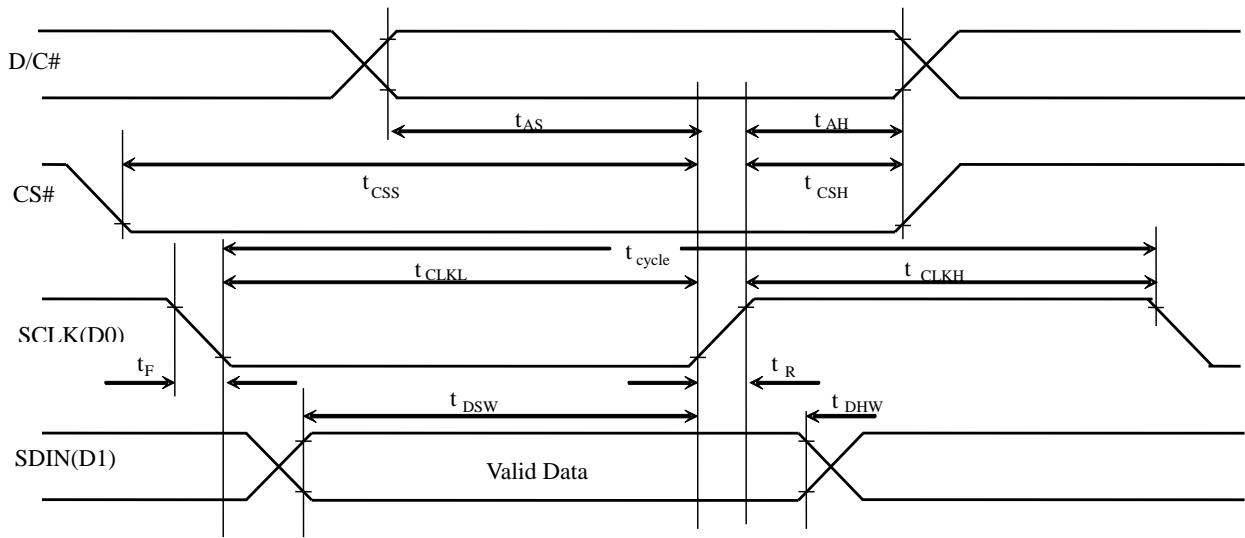
(V_{DD} - V_{SS} = 1.65V~2.1V, T_A = 25 °C)

Symbol	Parameter	Min	Typ	Max	Unit
t _{cycle}	Clock Cycle Time 时钟周期	220	-	-	ns
t _{AS}	Address Setup Time 地址建立时间	15	-	-	ns
t _{AH}	Address Hold Time 地址保持时间	15	-	-	ns
t _{CS}	Chip Select Setup Time 片选建立时间	20	-	-	ns
t _{CSH}	Chip Select Hold Time 片选保持时间	10	-	-	ns
t _{DSW}	Write Data Setup Time 写数据建立时间	15	-	-	ns
t _{DHW}	Write Data Hold Time 写数据保持时间	30	-	-	ns
t _{CLKL}	Clock Low Time 时钟低电平时间	25	-	-	ns
t _{CLKH}	Clock High Time 时钟高电平时间	20	-	-	ns
t _R	Rise Time 上升时间	-	-	15	ns
t _F	Fall Time 下降时间	-	-	15	ns

(V_{DD} - V_{SS} = 2.1V~3.5V, T_A = 25 °C)

Symbol	Parameter	Min	Typ	Max	Unit
t _{cycle}	Clock Cycle Time 时钟周期	160	-	-	ns
t _{AS}	Address Setup Time 地址建立时间	15	-	-	ns
t _{AH}	Address Hold Time 地址保持时间	15	-	-	ns
t _{CS}	Chip Select Setup Time 片选建立时间	20	-	-	ns
t _{CSH}	Chip Select Hold Time 片选保持时间	10	-	-	ns
t _{DSW}	Write Data Setup Time 写数据建立时间	15	-	-	ns
t _{DHW}	Write Data Hold Time 写数据保持时间	15	-	-	ns
t _{CLKL}	Clock Low Time 时钟低电平时间	20	-	-	ns
t _{CLKH}	Clock High Time 时钟高电平时间	20	-	-	ns
t _R	Rise Time 上升时间	-	-	15	ns
t _F	Fall Time 下降时间	-	-	15	ns

Serial interface characteristics (4-wire SPI)



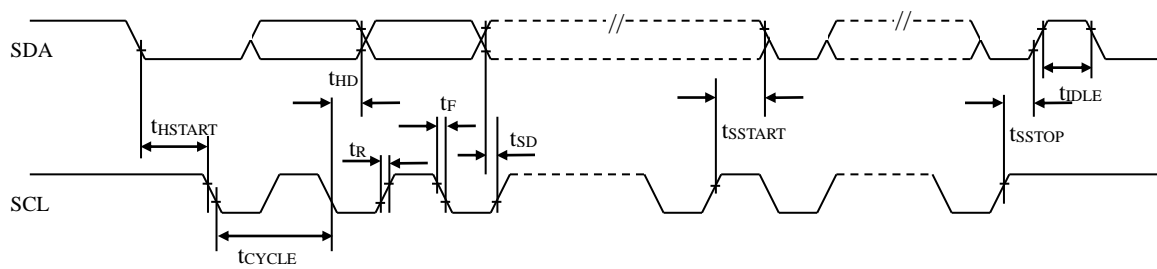
2. I²C Interface Timing Characteristics I²C 接口时序特性 (4-SPI)

I²C Interface Timing Characteristics

(VDD - VSS = 1.65V~3.3V, TA = 25 °C)

Symbol	Parameter	Min	Typ	Max	Unit
t _{cycle}	Clock Cycle Time 时钟周期	2.5	-	-	us
t _{HSTART}	Start condition Hold Time 开始条件保持时间	0.6	-	-	us
t _{HD}	Data Hold Time (for “SDA _{OUT} ” pin) 数据保持时间 (SDA _{OUT} 引脚)	0	-	-	ns
	Data Hold Time (for “SDA _{IN} ” pin) 数据保持时间 (SDA _{IN} 引脚)	300	-	-	ns
t _{SD}	Data Setup Time 数据建立时间	100	-	-	ns
t _{SSTART}	Start condition Setup Time (Only relevant for a repeated Start condition) 开始条件建立时间 (仅与重复开始条件有关)	0.6	-	-	us
t _{SSTOP}	Stop condition Setup Time 停止条件建立时间	0.6	-	-	us
t _R	Rise Time for data and clock pin 数据和时钟上升时间	-	-	300	ns
t _F	Fall Time for data and clock pin 数据和时钟下降时间	-	-	300	ns
t _{IDLE}	Idle Time before a new transmission can start 新传输开始之前的空闲时间	1.3	-	-	us

I²C interface Timing characteristics



3. 8080-Series MCU Parallel Interface Timing Characteristics 8080 并行接口时序特性

8080-Series MCU Parallel Interface Timing Characteristics

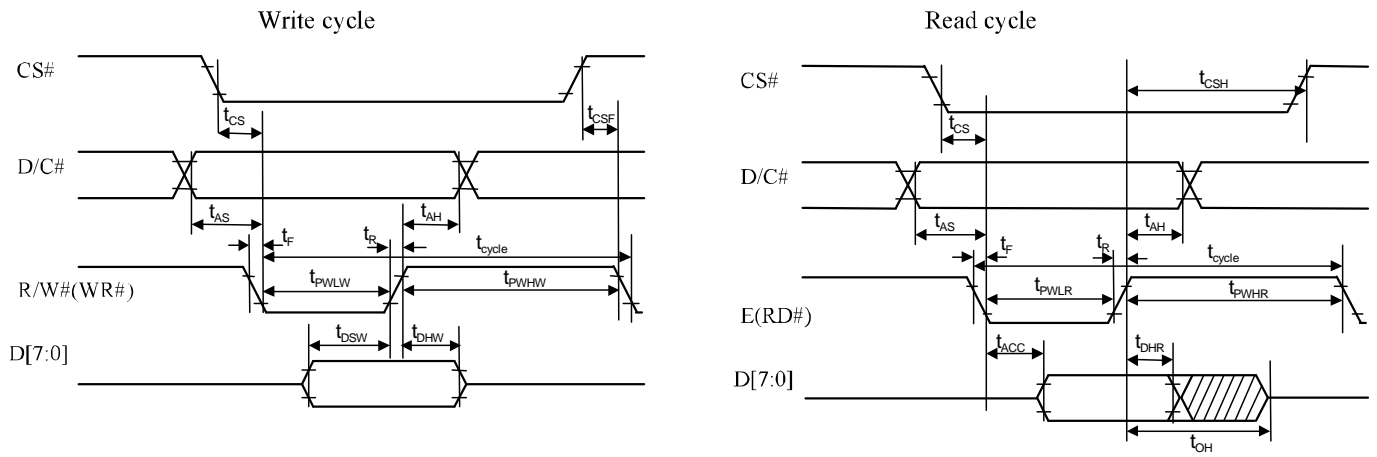
(V_{DD} - V_{SS} = 1.65V~2.1V, T_A = 25 °C)

Symbol	Parameter	Min	Typ	Max	Unit
t _{cycle}	Clock Cycle Time 时钟周期	300	-	-	ns
t _{AS}	Address Setup Time 地址建立时间	30	-	-	ns
t _{AH}	Address Hold Time 地址保持时间	0	-	-	ns
t _{DSW}	Write Data Setup Time 写数据建立时间	20	-	-	ns
t _{DHW}	Write Data Hold Time 写数据保持时间	42	-	-	ns
t _{DHR}	Read Data Hold Time 读数据保持时间	20	-	-	ns
t _{OH}	Output Disable Time 输出失效时间	-	-	70	ns
t _{ACC}	Access Time 可输入时间	-	-	140	ns
t _{PWLR}	Read Low Time 读数据低电平时间	150	-	-	ns
t _{PWLW}	Write Low Time 写数据低电平时间	60	-	-	ns
t _{PWHR}	Read High Time 读数据高电平时间	60	-	-	-
t _{PWHW}	Write High Time 写数据高电平时间	60	-	-	ns
t _r	Rise Time 上升时间	-	-	15	ns
t _f	Fall Time 下降时间	-	-	15	ns
t _{CS}	Chip select setup time 片选建立时间	0	-	-	-
t _{CSH}	Chip select hold time to read signal 读信号片选保持时间	0	-	-	-
t _{CSF}	Chip select hold time 片选保持时间	20	-	-	-

(V_{DD} - V_{SS} = 2.1V~3.5V, T_A = 25 °C)

Symbol	Parameter	Min	Typ	Max	Unit
t _{cycle}	Clock Cycle Time 时钟周期	300	-	-	ns
t _{AS}	Address Setup Time 地址建立时间	18	-	-	ns
t _{AH}	Address Hold Time 地址保持时间	0	-	-	ns
t _{DSW}	Write Data Setup Time 写数据建立时间	14	-	-	ns
t _{DHW}	Write Data Hold Time 写数据保持时间	20	-	-	ns
t _{DHR}	Read Data Hold Time 读数据保持时间	20	-	-	ns
t _{OH}	Output Disable Time 输出失效时间	-	-	70	ns
t _{ACC}	Access Time 可输入时间	-	-	140	ns
t _{PWLR}	Read Low Time 读数据低电平时间	150	-	-	ns
t _{PWLW}	Write Low Time 写数据低电平时间	60	-	-	ns
t _{PWHR}	Read High Time 读数据高电平时间	60	-	-	-
t _{PWHW}	Write High Time 写数据高电平时间	60	-	-	ns
t _r	Rise Time 上升时间	-	-	15	ns
t _f	Fall Time 下降时间	-	-	15	ns
t _{CS}	Chip select setup time 片选建立时间	0	-	-	-
t _{CSH}	Chip select hold time to read signal 读信号片选保持时间	0	-	-	-
t _{CSF}	Chip select hold time 片选保持时间	20	-	-	-

8080-Series MCU Parallel Interface Timing Characteristics



■ TIMING OF POWER SUPPLY (电源时序)

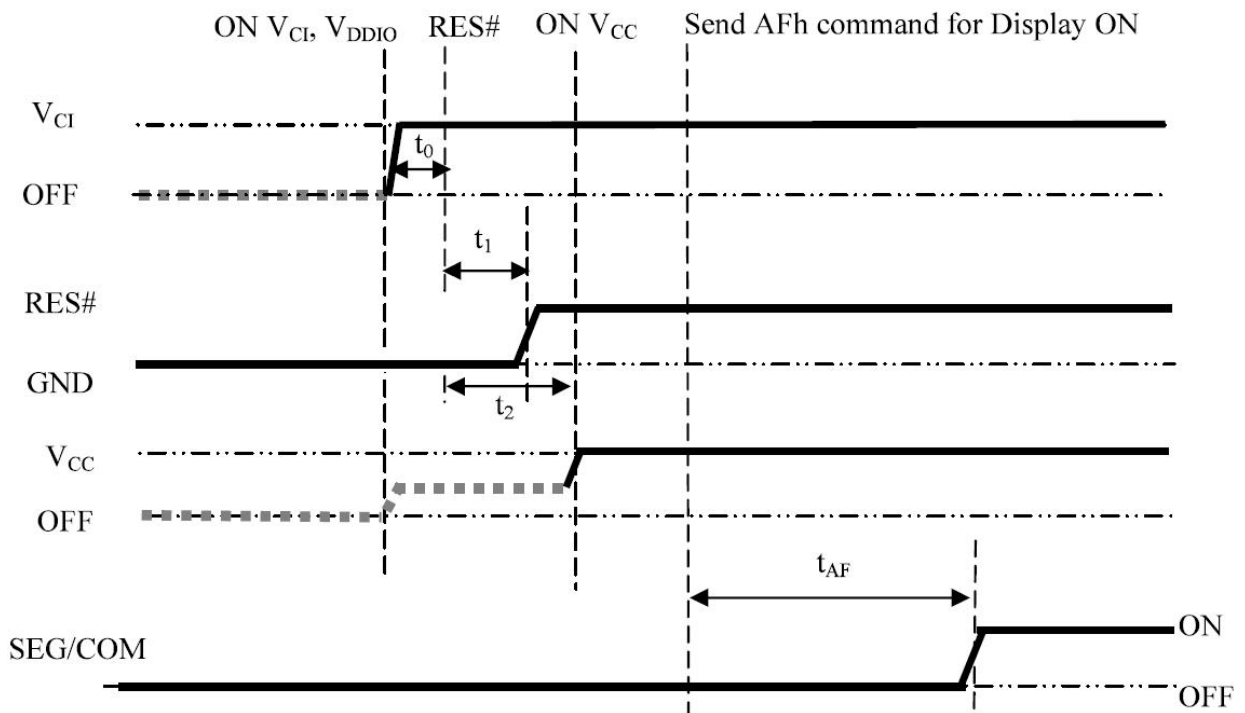
The following figures illustrate the recommended power ON and power OFF sequence of SSD1327.
 以下图示表示 SSD1327 推荐的上电和下电时序。

Power ON sequence:

上电时序

1. Power ON V_{CI}
 打开 V_{CI}
2. After V_{CI} become stable, set RES# pin LOW (logic low) for at least 3us (t₁)⁽⁴⁾ and then HIGH (logic high)
 在 V_{CI} 稳定后, RES# 引脚置低 (逻辑低) 至少 3us(t₁)⁽⁴⁾ 再置高 (逻辑高)
3. After RES# HIGH (logic high), wait for at least 3us (t₂). Then power ON V_{CC}.⁽¹⁾
 在 RES# 置高 (逻辑高) 后, 至少等待 3us(t₂)。然后打开 V_{CC}。
4. After V_{CC} become stable, send command AFh for display ON. SEG/COM will be ON after 100ms(t_{AF}).
 在 V_{CC} 稳定后, 发送 AFh 命令打开显示, SEG/COM 会在 100ms(t_{AF})后打开。

Power ON Sequence

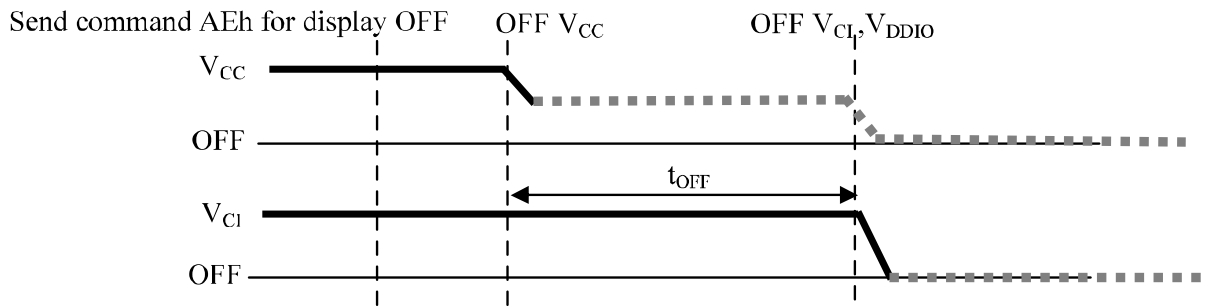


Power OFF sequence:

下电时序

1. Send command AEh for display OFF.
发送 AEh 命令关闭显示。
2. Power OFF V_{CC} .^{(1), (2)}
关闭 V_{CC} 。⁽¹⁾⁽²⁾
3. Power OFF V_{CI} after t_{OFF} .⁽⁴⁾ (where Minimum $t_{OFF}=0ms$, typical $t_{OFF}=100ms$)
在 t_{OFF} ⁽⁴⁾后关闭 V_{CI} 。（ t_{OFF} 的最小值为 0ms，典型值为 100ms）

Power OFF Sequence



Note:

- (1) V_{CC} should be kept float (i.e. disable) when it is OFF.
在 V_{CC} 关闭时应保持悬空（即失能）。
- (2) Power Pins (V_{CI}, V_{CC}) can never be pulled to ground under any circumstance.
电源引脚（ V_{CI}, V_{CC} ）在任何情况下都不能接地。
- (3) The register values are reset after t_1 .
寄存器的值在 t_1 后重置。
- (4) V_{CI} should not be Power OFF before V_{CC} Power OFF.
 V_{CI} 不应该在 V_{CC} 电源关闭之前关闭。

■ ELECTRO-OPTICAL CHARACTERISTICS (光电参数)

All data in below based the condition (Ta = 22±3℃, 60±10%RH).

以下参数均基于 Ta = 22±3℃,60±10%RH 的条件。

Items 项目	Symbol 符号	Min. 最小值	Typ. 典型值	Max. 最大值	Unit 单位	Remark 备注	
Operating Luminance 工作亮度	L	60	75	-	cd /m ²	All pixels ON	
Power Consumption 功耗	P	-	250	310	mW	30% pixels ON	
Frame Frequency 帧频	Fr	-	100	-	Hz	-	
Color Coordinate 色坐标	Yellow	CIE x	0.42	0.46	0.50	CIE1931	Darkroom
		CIE y	0.49	0.53	0.57		
Response Time 响应时间	Rise	Tr	-	10	-	us	-
	Decay	Td	-	10	-	us	-
Contrast Ratio* 对比度	Cr	10000:1	-	-	-	Darkroom	
Viewing Angle 可视角	Δθ	160	-	-	Degree	-	
Operating Life Time* 工作寿命	Top	50000	-	-	Hours	L=75cd/m2	

Note(注意事项) :

1. 75cd/m2 is based on VDD=3.0V,VCC=15V,Contrast command setting: 0XDF.
75cd/m2 显示条件为 VDD=3.0V,VCC=15V,对比度设置: 0XDF.

2. Contrast ratio is defined as follows(对比度的定义如下):

$$\text{Contrast ratio} = \frac{\text{Photo - detector output with OLED being "white"} \quad \text{OLED 显示全屏亮时的亮度}}{\text{Photo - detector output with OLED being "black"} \quad \text{OLED 显示全屏黑时的亮度}}$$

对比度

3. Life Time is defined when the Luminance has decayed to less than 50% of the initial Luminance specification. (Odd and even chess board alternately displayed).(The initial value should be closed to the typical value after adjusting.)
寿命的定义为当亮度衰减到初始亮度 50%时所消耗的时间。(奇数和偶数棋盘交替显示)。(初始亮度值应调试到接近典型值的大小)。

■ INTERFACE PIN CONNECTIONS (引脚接口)

No.编号	Symbol 符号	Description 描述
1	NC(GND)	NC or connected to GND. NC 或者接地。
2	VLSS	Analog system ground pin. It must be connected to external ground. 逻辑地，需外接地。
3	VSS	Ground pin. It must be connected to external ground. 接地引脚，必须与外部接地连接。
4	VCI	Low voltage power supply and power supply for interface logic level. It should match with the MCU interface voltage level and must be connected to external source.VCI must always set to be equivalent to or higher than VDD. 逻辑低电平，需与外部逻辑电平相匹配，并且 VCI 要略高于 VDD 电压。
5	VDD	Power supply pin for core logic operation.VDD can be supplied externally (within the range of 2.4V to 2.6V) or regulated internally from VCI. A capacitor should be connected between VDD and VSS under all circumstances. 核心操作电源，VDD 可外部提供也可内部由 VCI 转化，且需接电容到底。
6	BS1	MCU bus interface selection pins. Select appropriate logic setting as described in the following table. 接口选择脚。
7	BS2	
8	CS#	This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW). 此引脚是芯片选择输入引脚，连接到微控制器。 只有当 CS #拉低（低电平有效）时使能 OLED 与微控制器的通信。
9	RES#	This pin is reset signal input. When the pin is pulled LOW, initialization of the chip is executed. Keep this pin pull HIGH during normal operation. 此引脚是复位信号输入引脚。 当引脚被拉低时，执行芯片的初始化。 在正常操作时保持这个引脚高电平。
10	D/C#	This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the data at SDIN will be interpreted as data.When the pin is pulled LOW, the data at SDIN will be transferred to a command register. 此引脚是连接到微控制器的数据/命令控制引脚。 当引脚拉高，SDIN 的数据会被理解为数据。 当引脚拉低，SDIN 的数据会被传输到命令寄存器。
11	R/W#	This pin is read / write control input pin connecting to the MCU interface. When 6800 interface mode is selected, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and

		<p>the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS. 当 6800 通信方式时, 该 Pin 引脚作为读写功能脚; 当 8080 通信方式时, 该 Pin 引脚作为写功能脚; 当作为 SPI 或 IIC 接口通信方式时, 该 pin 引脚接地。</p>
12	E/RD#	<p>This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS. 当 6800 通信方式时, 该 Pin 引脚作为使能脚; 当 8080 通信方式时, 该 Pin 引脚作为读功能; 当作为 SPI 或 IIC 接口通信方式时, 该 pin 引脚接地。</p>
13~20	D0 ~ D7	<p>These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN and D2 should be kept NC. When I2C mode is selected, D2, D1 should be tied together and serve as SDAout, SDAin in application and D0 is the serial clock input, SCL. 当并行通信方式时, 这些 Pin 脚作为数据传输位; 当作为 SPI 接口通信方式时, D3~D7 接地处理, D2 脚 NC, D0 作为时钟信号, D1 为数据输入信号。 当作为 IIC 接口通信方式时, D3~D7 接地处理, D0 作为时钟信号, D1 和 D2 接在一起为数据传输信号。</p>
21	IREF	<p>This pin is the segment output current reference pin. IREF is supplied externally. A resistor should be connected between this pin and VSS to maintain the current around 10uA. 此引脚是 SEG 输出电流基准引脚。 IREF 由外部提供, 此引脚和 VSS 之间应接入一个电阻使电流保持在 10uA。</p>
22	VCOMH	<p>COM signal deselected voltage level. A capacitor should be connected between this pin and VSS. COM 信号取消选择的电平, 该引脚应连接电容到地。</p>
23	VCC	<p>Power supply for panel driving voltage. 面板驱动电压电源。</p>
24	NC(GND)	<p>NC or connected to GND. NC 或者接地。</p>

■ COMMAND TABLE (指令表)

SSD1327Z Command Table

(D/C#=0, R/W#(WR#) = 0, E(RD#=1) unless specific setting is stated)

1. Fundamental Command Table											
D/C#	Hex	D7	D6	D5	D4	D3	D2	D1	D0	Command	Description
0 0 0	15 A[5:0] B[5:0]	0 * *	0 * *	0 A ₅ A ₅	1 A ₄ A ₄	0 A ₃ A ₃	1 A ₂ A ₂	0 A ₁ A ₁	1 A ₀ A ₀	Set Column Address	Setup Column start and end address A[5:0]: Start Address, range:00h~3Fh, (RESET = 00h) B[5:0]: End Address, range:00h~3Fh, (RESET = 3Fh)
0 0 0	75 A[6:0] B[6:0]	0 * *	0 A ₆ A ₆	0 A ₅ A ₅	1 A ₄ A ₄	0 A ₃ A ₃	1 A ₂ A ₂	0 A ₁ A ₁	1 A ₀ A ₀	Set Row Address	Setup Row start and end address A[6:0]: Start Address, range:00h~7Fh, (RESET = 00h) B[6:0]: End Address, range:00h~7Fh, (RESET = 7Fh)
0 0	81 A[7:0]	1 A ₇	0 A ₆	0 A ₅	0 A ₄	0 A ₃	0 A ₂	0 A ₁	1 A ₀	Set Contrast Control	Double byte command to select 1 out of 256 contrast steps. Contrast increases as the value increases. (RESET = 7Fh)
0	84 ~ 86	1	0	0	0	0	1	X ₁	X ₀	Reserved	Command for no operation
0 0	A0 A[7:0]	1 0	0 A ₆	1 0	0 A ₄	0 0	0 A ₂	0 A ₁	0 A ₀	Set Re-map	Re-map setting in Graphic Display Data RAM (GDDRAM) A[0] = 0b, Disable Column Address Re-map (RESET) A[0] = 1b, Enable Column Address Re-map A[1] = 0b, Disable Nibble Re-map (RESET) A[1] = 1b, Enable Nibble Re-map A[2] = 0b, Enable Horizontal Address Increment (RESET) A[2] = 1b, Enable Vertical Address Increment A[3] = 0b, Reserved (RESET) A[4] = 0b, Disable COM Re-map (RESET) A[4] = 1b, Enable COM Re-map A[5] = 0b, Reserved (RESET) A[6] = 0b, Disable COM Split Odd Even (RESET) A[6] = 1b, Enable COM Split Odd Even A[7] = 0b, Reserved (RESET)
0 0	A1 A[6:0]	1 *	0 A ₆	1 A ₅	0 A ₄	0 A ₃	0 A ₂	0 A ₁	1 A ₀	Set Display Start Line	A[6:0]: Vertical shift by setting the starting address of display RAM from 0 ~ 127 (RESET = 00h)

I. Fundamental Command Table											
D/C#	Hex	D7	D6	D5	D4	D3	D2	D1	D0	Command	Description
0 0	A2 A[6:0]	1 *	0 A ₆	1 A ₅	0 A ₄	0 A ₃	0 A ₂	1 A ₁	0 A ₀	Set Display Offset	A[6:0]: Set vertical offset by COM from 0 ~ 127 (RESET = 00h) e.g. Set A[6:0] to 010000b to move COM16 towards COM0 direction for 16 row
0	A4 ~ A7	1	0	1	0	0	1	X ₁	X ₀	Set Display Mode	A4h = Normal display (RESET) A5h = All ON (All pixels have gray scale of 15, GS15) A6h = All OFF (All pixels have gray scale of 0, GS0) A7h = Inverse Display (GS0 → GS15, GS1 → GS14, GS2 → GS13, ...)
0 0	A8 A[6:0]	1 *	0 A ₆	1 A ₅	0 A ₄	1 A ₃	0 A ₂	0 A ₁	0 A ₀	Set MUX Ratio	A[6:0]: Set MUX ratio from 16MUX ~ 128MUX: A[6:0] = 15 represents 16MUX A[6:0] = 16 represents 17MUX : A[6:0] = 126 represents 127MUX A[6:0] = 127 represents 128MUX (RESET) It should be noted that A[6:0]=0~14 is not allowed
0 0	AB A[0]	1 0	0 0	1 0	0 0	1 0	0 0	1 0	1 A ₀	Function Selection A	A[0]=0b, Select external V _{DD} (i.e. Disable internal V _{DD} regulator) A[0]=1b, Enable internal V _{DD} regulator (RESET)
0	AE / AF	1	0	1	0	1	1	1	X ₀	Set Display ON/OFF	AEh = Display OFF (sleep mode) (RESET) AFh = Display ON in normal mode
0 0	B1 A[7:0]	1 A ₇	0 A ₆	1 A ₅	1 A ₄	0 A ₃	0 A ₂	0 A ₁	1 A ₀	Set Phase Length	A[3:0]: Phase 1 period of 1~15 DCLK's e.g. A[3:0] = 1111b, 15 DCLK Clock (RESET = 0100b) A[7:4]: Phase 2 period of 1~15 DCLK's e.g. A[7:4] = 1111b, 15 DCLK Clocks (RESET = 0111b) Note ⁽¹⁾ 0 DCLK is invalid in phase 1 & phase 2 ⁽²⁾ GS15 level pulse width must be set larger than the period of phase 1 + phase 2
0	B2	1	0	1	1	0	0	1	0	NOP	Command for no operation

1. Fundamental Command Table											
D/C#	Hex	D7	D6	D5	D4	D3	D2	D1	D0	Command	Description
0 0	B3 A[7:0]	1 A ₇	0 A ₆	1 A ₅	1 A ₄	0 A ₃	0 A ₂	1 A ₁	1 A ₀	Set Front Clock Divider /Oscillator Frequency	A[3:0]: Define divide ratio (D) of display clock (DCLK) Divide ratio=A[3:0]+1 (RESET is 0000b, i.e. divide ratio = 1) A[7:4]: Set the Oscillator Frequency, F _{osc} . Oscillator Frequency increases with the value of A[7:4] and vice versa. (Range:0000b~1111b) (RESET = 0000b)
0 0	B5 A[1:0]	1 0	0 0	1 0	1 0	0 0	1 0	0 A ₁	1 A ₀	GPIO	A[1:0] = 00b represents GPIO pin HiZ, input disable (always read as low) A[1:0] = 01b represents GPIO pin HiZ, input enable A[1:0] = 10b represents GPIO pin output Low (RESET) A[1:0] = 11b represents GPIO pin output High
0 0	B6 A[3:0]	1 *	0 *	1 *	1 *	0 A ₃	1 A ₂	1 A ₁	0 A ₀	Set Second pre-charge Period	A[3:0]: Second Pre-charge period of 1~15 DCLK's e.g. A[3:0] = 1111b, 15 DCLK Clock (RESET = 0100b) Note (1) This command is used to adjust the second pre-charge period after enabling the second pre-charge by setting A[1] = 1b in command D5h
0 0 0 0 0	B8 A1[5:0] A2[5:0] A14[5:0] A15[5:0]	1 * * * *	0 * * * *	1 A1 ₅ A2 ₅ A14 ₅ A15 ₅	1 A1 ₄ A2 ₄ A14 ₄ A15 ₄	1 A1 ₃ A2 ₃ A14 ₃ A15 ₃	0 A1 ₂ A2 ₂ A14 ₂ A15 ₂	0 A1 ₁ A2 ₁ A14 ₁ A15 ₁	0 A1 ₀ A2 ₀ A14 ₀ A15 ₀	Set Gray Scale Table	The next 15 data bytes set the gray scale pulse width in unit of DCLK's. A1[5:0], value for GS1 level Pulse width A2[5:0], value for GS2 level Pulse width ... A14[5:0], value for GS14 level Pulse width A15[5:0], value for GS15 level Pulse width Note (1) The pulse width value of GS1, GS2, ..., GS15 should not be equal. i.e. 0<GS1<GS2 ... <GS15 (2) GS15 level pulse width must be set larger than the period of phase 1 + phase 2
0	B9	1	0	1	1	1	0	0	1	Linear LUT	The default Linear Gray Scale table is set in unit of DCLK's as follow GS0 level pulse width = 0; GS1 level pulse width = 0; GS2 level pulse width = 2; GS3 level pulse width = 4; : : GS14 level pulse width = 26; GS15 level pulse width = 28

1. Fundamental Command Table																																
D/C#	Hex	D7	D6	D5	D4	D3	D2	D1	D0	Command	Description																					
0	BB	1	0	1	1	1	0	1	1	NOP	Command for no operation																					
0 0	BC A[3:0]	1 0	0 0	1 0	1 0	1 A ₃	1 A ₂	0 A ₁	0 A ₀	Set Pre-charge voltage	Set pre-charge voltage level. <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>A[3:0]</th> <th>Hex code</th> <th>Pre-charge voltage</th> </tr> </thead> <tbody> <tr> <td>0000</td> <td>00h</td> <td>0.20 x V_{CC}</td> </tr> <tr> <td>:</td> <td>:</td> <td>:</td> </tr> <tr> <td>0101</td> <td>05h</td> <td>0.5 x V_{CC} (RESET)</td> </tr> <tr> <td>:</td> <td>:</td> <td>:</td> </tr> <tr> <td>0111</td> <td>07h</td> <td>0.613 x V_{CC}</td> </tr> <tr> <td>1xxx</td> <td>08h</td> <td>V_{COMH}</td> </tr> </tbody> </table>	A[3:0]	Hex code	Pre-charge voltage	0000	00h	0.20 x V _{CC}	:	:	:	0101	05h	0.5 x V _{CC} (RESET)	:	:	:	0111	07h	0.613 x V _{CC}	1xxx	08h	V _{COMH}
A[3:0]	Hex code	Pre-charge voltage																														
0000	00h	0.20 x V _{CC}																														
:	:	:																														
0101	05h	0.5 x V _{CC} (RESET)																														
:	:	:																														
0111	07h	0.613 x V _{CC}																														
1xxx	08h	V _{COMH}																														
0 0	BE A[2:0]	1 0	0 0	1 0	1 0	1 0	1 A ₂	1 A ₁	0 A ₀	Set V _{COMH}	Set COM deselect voltage level. <table border="1" style="margin-left: 20px;"> <thead> <tr> <th>A[2:0]</th> <th>Hex code</th> <th>V_{COMH}</th> </tr> </thead> <tbody> <tr> <td>000</td> <td>00h</td> <td>0.72 x V_{CC}</td> </tr> <tr> <td>:</td> <td>:</td> <td>:</td> </tr> <tr> <td>101</td> <td>05h</td> <td>0.82 x V_{CC} (RESET)</td> </tr> <tr> <td>:</td> <td>:</td> <td>:</td> </tr> <tr> <td>111</td> <td>07h</td> <td>0.86 x V_{CC}</td> </tr> </tbody> </table>	A[2:0]	Hex code	V _{COMH}	000	00h	0.72 x V _{CC}	:	:	:	101	05h	0.82 x V _{CC} (RESET)	:	:	:	111	07h	0.86 x V _{CC}			
A[2:0]	Hex code	V _{COMH}																														
000	00h	0.72 x V _{CC}																														
:	:	:																														
101	05h	0.82 x V _{CC} (RESET)																														
:	:	:																														
111	07h	0.86 x V _{CC}																														
0 0	D5 A[2:0]	1 0	1 1	0 1	1 0	0 0	1 0	0 A ₁	1 A ₀	Function Selection B	A[1] = 0b: Disable second precharge (RESET) A[1] = 1b: Enable second precharge A[0] = 0b: Internal VSL (RESET) A[0] = 1b: Enable external VSL Note ⁽¹⁾ Refer to Table 6-1 for VSL pin details																					
0 0	FD A[2]	1 0	1 0	1 0	1 1	1 0	1 A ₂	0 1	1 0	Set Command Lock	A[2]: MCU protection status. A[2] = 0b, Unlock OLED driver IC MCU interface from entering command (RESET) A[2] = 1b, Lock OLED driver IC MCU interface from entering command Note ⁽¹⁾ The locked OLED driver IC MCU interface prohibits all commands and memory access except the FDh command																					

Note: *Do not use any other commands, or the system malfunction may result.

■ INITIALIZATION CODE (初始化代码)

```
void Init_SSD1327(void)
{
    Write_Command(0XFD);           //Set Command Lock
    Write_Command(0X12);           //(12H=Unlock,16H=Lock)

    Write_Command(0XAE);           //Display OFF(Sleep Mode)

    Write_Command(0XB3);           //Display Clock Divider
    Write_Command(0X50);

    Write_Command(0XA8);           //Set Multiplex Ratio
    Write_Command(0X3F);

    Write_Command(0X15);           //Set Column Address
    Write_Command(S_COL);          //Start Column Address
    Write_Command(E_COL);          //End Column Address

    Write_Command(0X75);           //Set Row Address
    Write_Command(S_ROW);          //Start Row Address
    Write_Command(E_ROW);          //End Row Address

    Write_Command(0X81);           //Set Contrast
    Write_Command(0XDF);

    Write_Command(0XA0);           //Set Remap
    Write_Command(0X51);

    Write_Command(0XA1);           //Set Display Start Line
    Write_Command(0X00);

    Write_Command(0XA2);           //Set Display Offset
    Write_Command(0X40);

    Write_Command(0XA4);           //Normal Display

    Write_Command(0XAB);           //Set VDD regulator
    Write_Command(0X01);           //Regulator Enable

    Write_Command(0XB1);           //Set Phase Length
    Write_Command(0X25);

    Write_Command(0XD5);           //Set second precharge and VSL
    Write_Command(0X62);

    Write_Command(0XB6);           //Set Second precharge Period
    Write_Command(0X0A);

    Write_Command(0xB8);
    Write_Command(0x02);
    Write_Command(0x04);
    Write_Command(0x07);
    Write_Command(0x0B);
```

```
Write_Command(0x0F);
Write_Command(0x13);
Write_Command(0x17);
Write_Command(0x1C);
Write_Command(0x20);
Write_Command(0x25);
Write_Command(0x2A);
Write_Command(0x2F);
Write_Command(0x35);
Write_Command(0x3A);
Write_Command(0x3F);

Write_Command(0XBC);      //Set pre-charge voltage level
Write_Command(0X10);      //0.5*VCC

Write_Command(0XBE);      //Set COM deselect voltage level
Write_Command(0X05);      //0.82*VCC

Write_Command(0XAF);      //Display ON
```

```
}
```

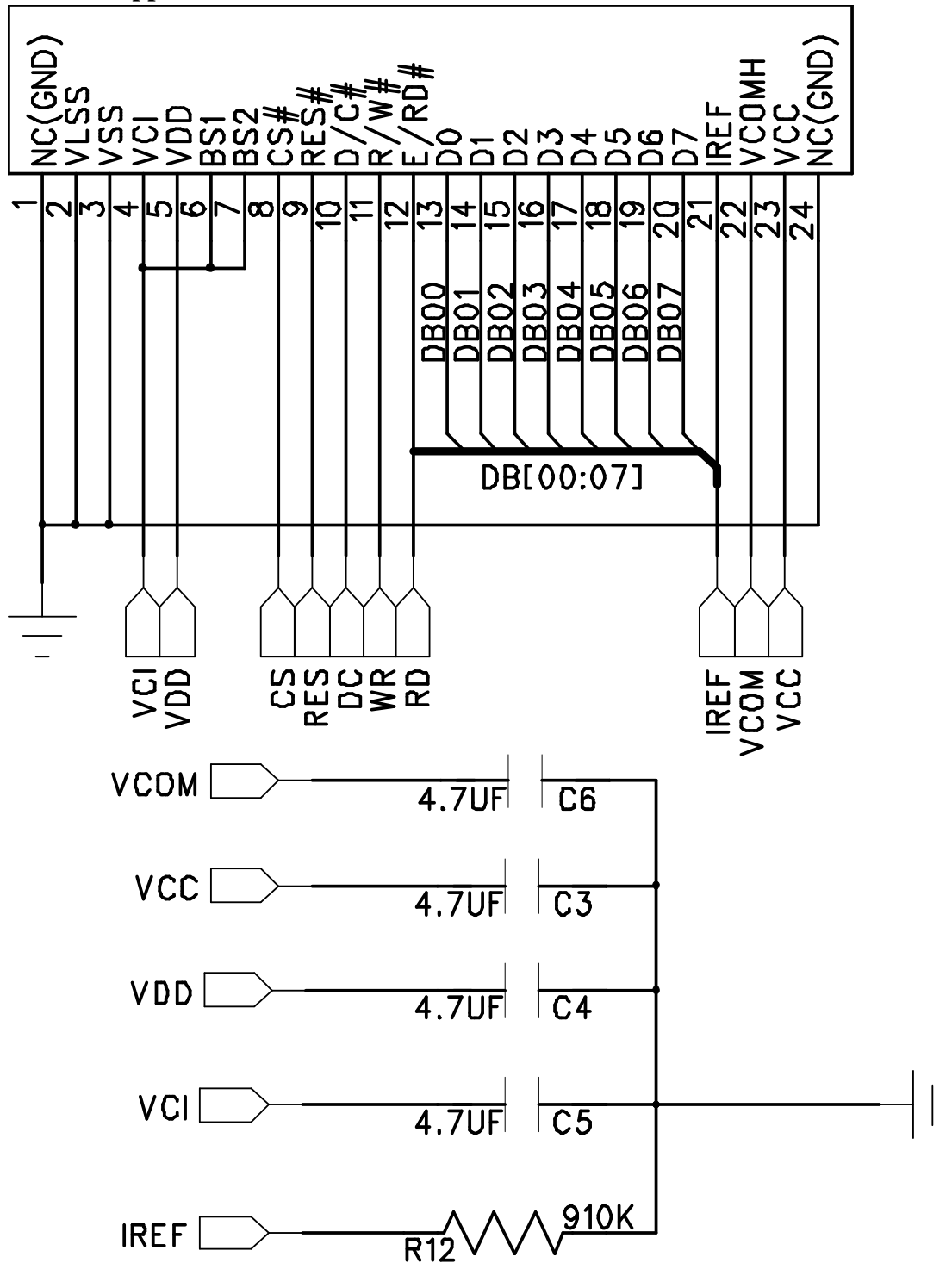
Note:

Please set appropriate parameters of initialization based on actual application.

注意：请基于实际的应用程序设置合适的初始化的参数。

■ SCHEMATIC EXAMPLE (应用电路)

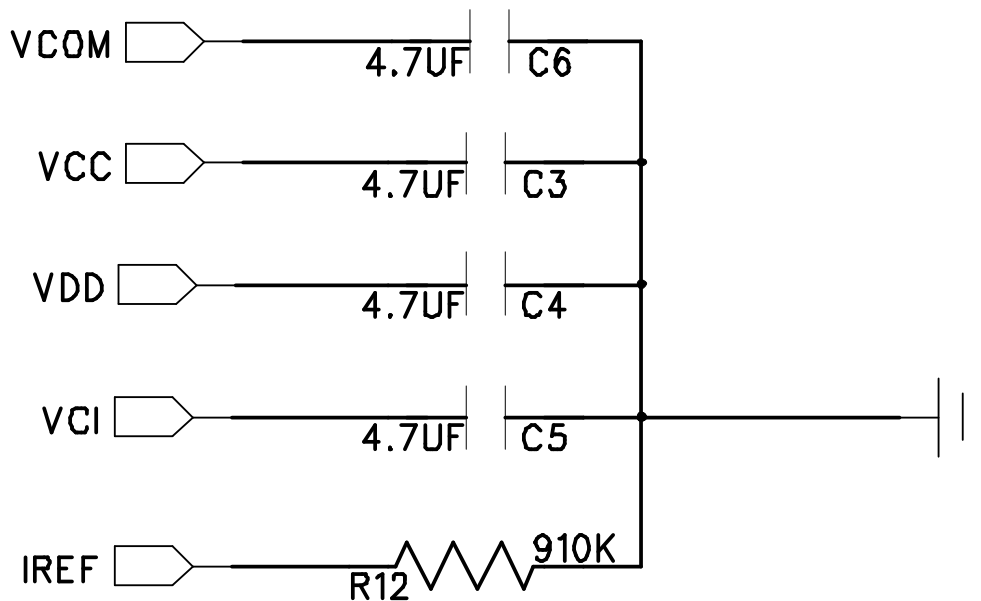
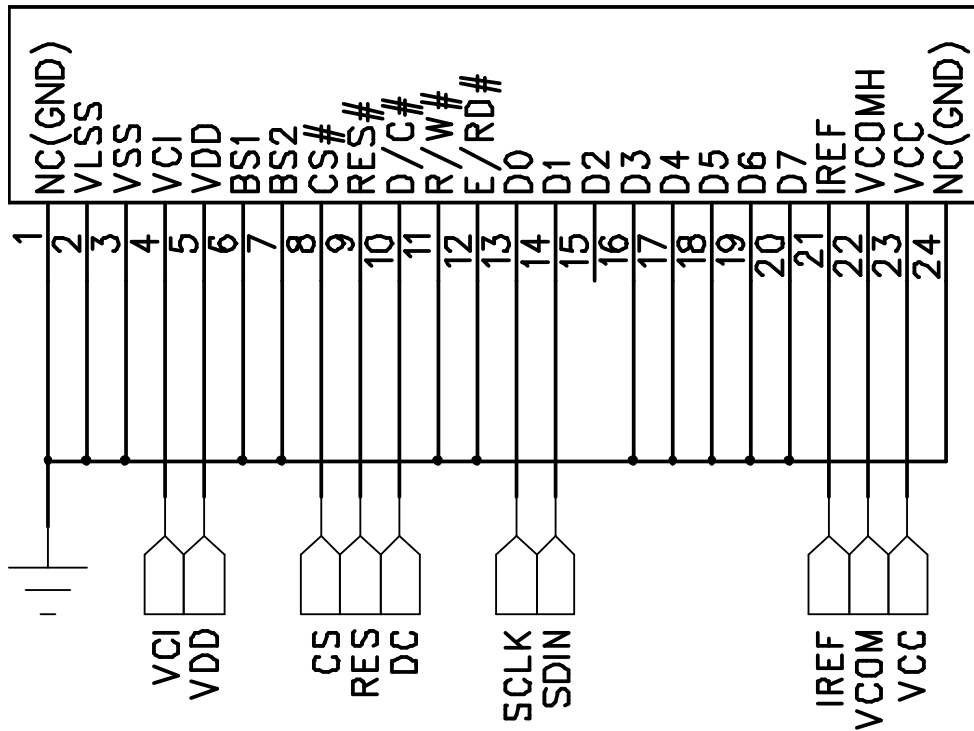
◆ 8080Interface Application Circuit (8080 接口应用电路)



NOTE:

1. $R12$: about 910 K Ω , $R12 = (\text{Voltage at IREF} - VSS)/IREF$
 $C3=C4=C5= C6=4.7\mu F$.
2. The VCC ,VCI should connect an external voltage.
 VCC,VCI 应连接到外部电压。
3. The capacitor and the resistor value are recommended value. Select the appropriate value against module application. It's recommended to use Tantalum capacitors for Vcom pin.
 电容和电阻的值仅为推荐的值，根据模块的实际应用选择合适的值。建议 Vcom 脚电容选用钽电容。

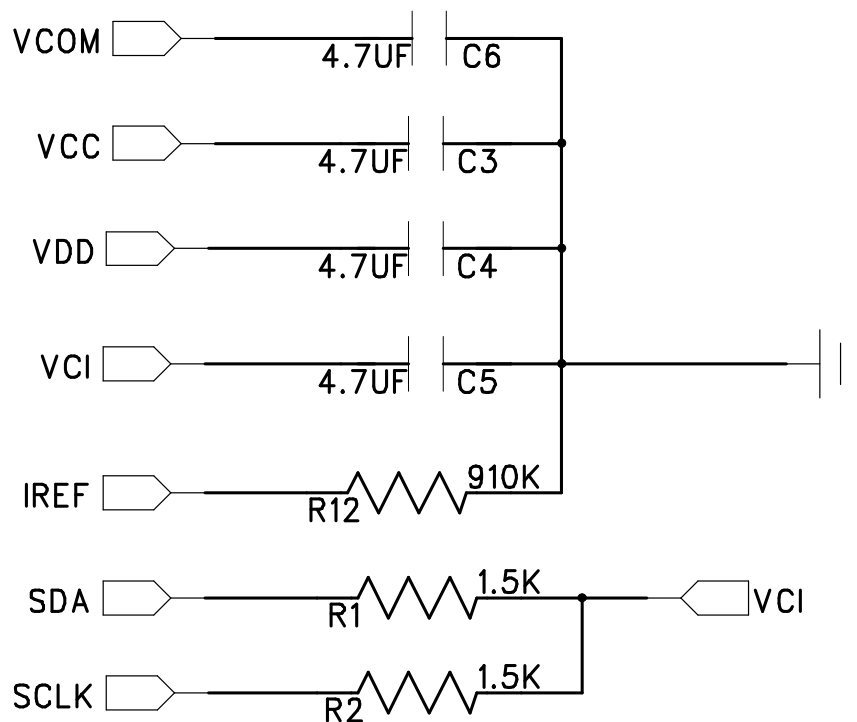
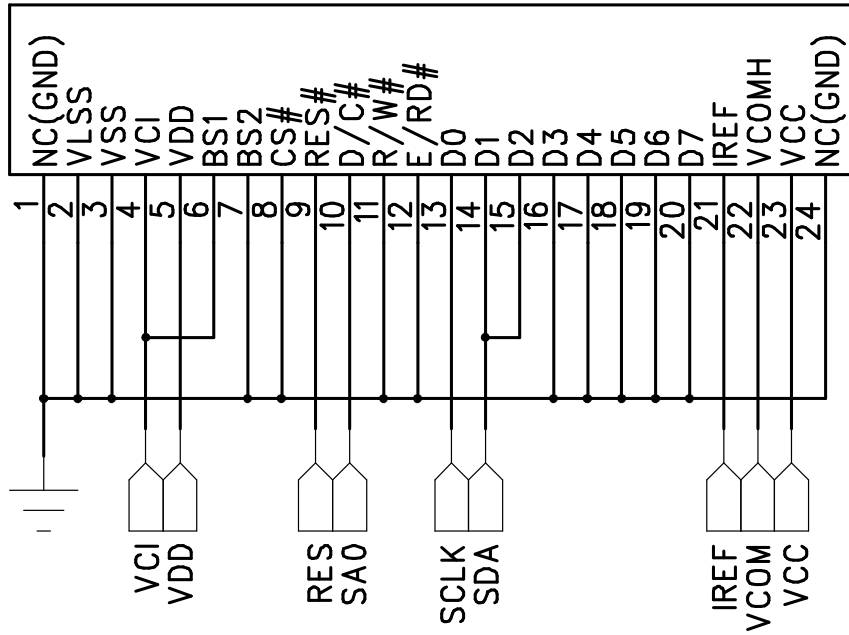
◆ 4-SPI Interface Application Circuit (4-SPI 接口应用电路)



NOTE:

4. $R12$: about 910 K Ω , $R12 = (\text{Voltage at IREF} - VSS)/IREF$
 $C3=C4=C5= C6=4.7\mu F$.
5. The VCC ,VCI should connect an external voltage.
 VCC,VCI 应连接到外部电压。
6. The capacitor and the resistor value are recommended value. Select the appropriate value against module application. It's recommended to use Tantalum capacitors for Vcom pin.
 电容和电阻的值仅为推荐的值，根据模块的实际应用选择合适的值。建议 Vcom 脚电容选用钽电容。

◆ I²C Interface Application Circuit (I²C 接口应用电路)



NOTE:

7. $R1=R2=1.5K\Omega$, $R12$: about $910 K\Omega$, $R12 = (Voltage\ at\ IREF - VSS)/IREF$
 $C3=C4=C5= C6=4.7\mu F$.
8. The VCC ,VCI should connect an external voltage.
 VCC,VCI 应连接到外部电压。
9. The capacitor and the resistor value are recommended value. Select the appropriate value against module application.It's recommended to use Tantalum capacitors for Vcom pin.
 电容和电阻的值仅为推荐的值，根据模块的实际应用选择合适的值。建议 Vcom 脚电容选用钽电容。

■ **RELIABILITY TESTS (可靠性测试)**

NO.	Item	Condition	Quantity
1	High Temperature Storage (HTS)	80±2℃, 200 hours	3
2	High Temperature Operating (HTO)	70±2℃, 96 hours	3
3	Low Temperature Storage (LTS)	-40±2℃, 200 hours	3
4	Low Temperature Operating (LTO)	-30±2℃, 96 hours	3
5	High Temperature / High Humidity Storage (HTHHS)	50±3℃, 90% ±3%RH, 120 hours	3
6	Thermal Shock (Non-operation) (TS)	-20±2℃ ~ 25℃ ~ 70±2℃ (30min) (5min) (30min) 10cycles	3
7	Vibration (Packing)	10~55~10Hz, amplitude 1.5mm, 1 hour for each direction x, y, z	1 Carton
8	Drop (Packing)	Height : 1 m, each time for 6 sides, 3 edges, 1 angle	1 Carton
9	ESD (finished product housing)	±4kVR:330Ω; C:150pF 10times,air discharge	3

Test and measurement conditions (测试与测量条件)

1. All measurements shall not be started until the specimens attain to temperature stability.
2. The degradation of Polarizer are ignored for item 1, 2 & 5.

Evaluation criteria (评估标准)

1. The function test is ok.
2. No addition to the defect.
3. The change of luminance should be within ±50% of initial value.
4. The change for the color must be within (±0.02)of initial value based on 1931 CIE coordinates.
5. The change of total current consumption should be within ±50% of initial value.
6. In case of malfunction or defect caused by ESD damage, it would be judged as a good part if it would be recovered to normal state after resetting.

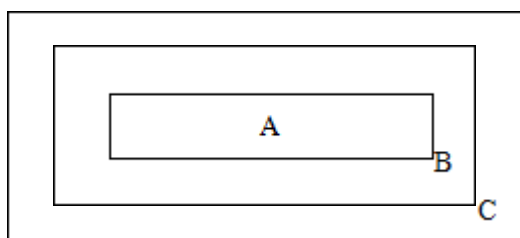
■ OUTGOING QUALITY CONTROL SPECIFICATION (出厂质量控制规范)

◆ Standard (标准)

According to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993, General Inspection Level II.

◆ Definition (定义)

1. Major defect: The defect that greatly affect the usability of product.
2. Minor defect: The other defects, such as cosmetic defects, etc.
3. Definition of inspection zone:



Zone A: Active Area

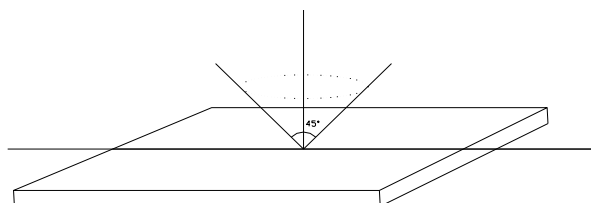
Zone B: Viewing Area except Zone A

Zone C: Outside Viewing Area

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble of quality and assembly to customer`s product.

◆ Inspection Methods (检查方法)

1. The general inspection: under 20W x 2 or 40W fluorescent light, about 30cm viewing distance, within 45 °viewing angle, under 25 ±5 °C.



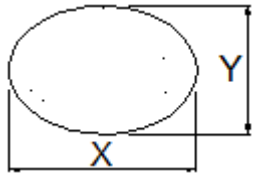
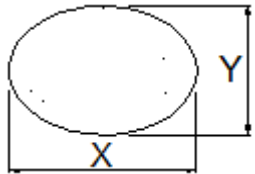
2. The luminance and color coordinate inspection: By CS2000/09A-OLED-117 or the equal equipment, in the dark room, under 25 ±5 °C.

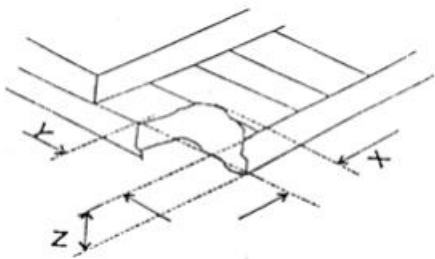
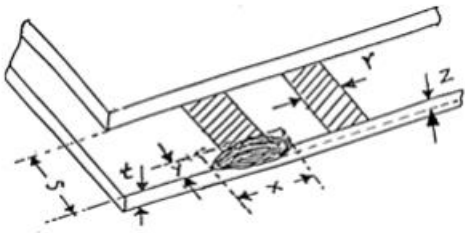
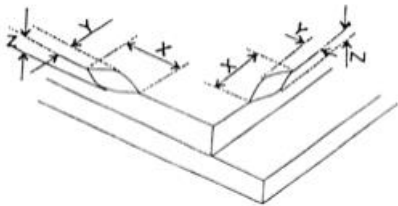
◆ Inspection Criteria (检查标准)

1. Major defect: AQL= 0.65

Item	Criterion
Function Defect	1. No display or abnormal display is not accepted
	2. Open or short is not accepted.
	3. Power consumption exceeding the spec is not accepted.
Outline Dimension	Outline dimension exceeding the spec is not accepted.
Glass Crack	Glass crack tends to enlarge is not accepted.

2. Minor Defect: AQL= 1.5

Item	Criterion			
	Size (mm)		Accepted Qty.	
Spot Defect (dimming and lighting spot)		$\Phi \leq 0.10$	Ignored	
		$0.10 < \Phi \leq 0.15$	3	Ignored
		$0.15 < \Phi \leq 0.2$	1	
		$0.2 < \Phi$	0	
	Note : $\Phi = (x + y) / 2$			
Line Defect (dimming and lighting line)	L (Length) : mm	W (Width) : mm	Area A + Area B	Area C
	/	$W \leq 0.02$	Ignored	
	$L \leq 3.0$	$0.02 < W \leq 0.03$	2	Ignored
	$L \leq 2.0$	$0.03 < W \leq 0.05$	1	
	/	$0.05 < W$	As spot defect	
Remarks: The total of spot defect and line defect shall not exceed 4 PCS. The distance between two lines defects must exceed 1 mm				
Polarizer Stain	Stain which can be wiped off lightly with a soft cloth or similar cleaning is accepted, otherwise, according to the Spot Defect and the Line Defect.			
Polarizer Scratch	1. If scratch can be seen during operation, according to the criterions of the Spot Defect and the Line Defect.			
	2. If scratch can be seen only under non-operation or some special angle, the criterion is as below:			
	L (Length) : mm	W (Width) : mm	Area A + Area B	Area C
	/	$W \leq 0.02$	Ignore	
	$3.0 < L \leq 5.0$	$0.02 < W \leq 0.04$	2	Ignore
	$L \leq 3.0$	$0.04 < W \leq 0.06$	1	
/	$0.06 < W$	0		
Polarizer Air Bubble	Size		Area A + Area B	Area C
		$\Phi \leq 0.20$	Ignored	
		$0.20 < \Phi \leq 0.30$	2	Ignored
		$0.30 < \Phi \leq 0.50$	1	
		$0.50 < \Phi$	0	

Glass Defect (Glass Chipped)	1. on the corner (mm)  <table border="1" style="float: right;"> <tr> <td>x</td> <td>≤ 1.5</td> </tr> <tr> <td>y</td> <td>≤ 1.5</td> </tr> <tr> <td>z</td> <td>$\leq t$</td> </tr> </table>	x	≤ 1.5	y	≤ 1.5	z	$\leq t$
	x	≤ 1.5					
	y	≤ 1.5					
	z	$\leq t$					
2. On the bonding edge (mm)  <table border="1" style="float: right;"> <tr> <td>x</td> <td>$\leq a/4$</td> </tr> <tr> <td>y</td> <td>$\leq s/3 \ \& \ \leq 0.7$</td> </tr> <tr> <td>z</td> <td>$\leq t$</td> </tr> </table>	x	$\leq a/4$	y	$\leq s/3 \ \& \ \leq 0.7$	z	$\leq t$	
x	$\leq a/4$						
y	$\leq s/3 \ \& \ \leq 0.7$						
z	$\leq t$						
3. On the other edges (mm)  <table border="1" style="float: right;"> <tr> <td>x</td> <td>$\leq a / 8$</td> </tr> <tr> <td>y</td> <td>≤ 0.7</td> </tr> <tr> <td>z</td> <td>$\leq t$</td> </tr> </table>	x	$\leq a / 8$	y	≤ 0.7	z	$\leq t$	
x	$\leq a / 8$						
y	≤ 0.7						
z	$\leq t$						
Note: t: glass thickness; s: pad width; a: the length of the edge.							
TCP Defect	Crack, deep fold and deep pressure mark on the TCP are not accepted						
Pixel Size	The tolerance of display pixel dimension should be within $\pm 20\%$ of the spec.						
Luminance	Refer to the spec or the reference sample.						
Color	Refer to the spec or the reference sample.						

■ CAUTIONS IN USING OLED MODULE (OLED 模块使用注意事项)

◆ Precautions for Handling OLED Module (处理 OLED 模块的注意事项)

1. The display panel is made of glass and polarizer. As glass is fragile. It tends to become chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

显示屏由玻璃和偏光片组成。由于玻璃是脆弱的，使用过程中要特别防止边缘区损伤。请避免显示屏因跌落或振动而受到机械冲击。

2. Do not apply excessive force to the display surface or the adjoining areas since this may cause abnormal. Do not touch the display with bare hands. This will stain the display area (some cosmetics are determined to the polarizer).

请勿施加过大的压力于显示屏或连接部位，否则可能会引起显示异常。不要用手接触显示屏，这将弄脏显示区（一些外观是由偏光片决定的）。

3. The polarizer covering the display surface of the OLED module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on it. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in to contact with room temperature air.

覆盖 OLED 显示模块显示平面的偏光片是软性的且易被擦伤，请小心，轻拿。请勿用任何硬度大于 HB 铅笔芯的物品（玻璃，镊子等）接触、撞压或摩擦裸露偏光片。不要放置或粘附物体在显示区域上以免留下痕迹。冷凝在表面和端子将会损坏或弄脏偏光片。产品在低温下测试之后，与室温空气接触之前必须在容器内升温。

4. If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents.

- Isopropyl alcohol
- Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

如果显示平面受污，可对平面吹热气且轻轻地用软性干布擦除。如果受污严重，用含下列一种溶剂的湿布擦除：

- 甘油
- 酒精

请勿用力擦拭以免损坏显示平面。

5. Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

- Water
- Ketone
- Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil and fats.

除以上提到的溶剂外，其他溶剂可能会损坏偏光片，特别要避免使用以下溶剂：

- 水
- 酮
- 芳烃溶剂

立即擦掉唾液或水滴，长时间与水接触会引起变形或褪色。避免接触油和油脂。

6. Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

特别注意最小限度地减少电极腐蚀，电极腐蚀会因水滴、湿度冷凝或高湿环境下通电而加速。

7. When mounting the OLED module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable.



安装 OLED 模块时一定不要弯曲、扭曲和变形。要特别注意不要用力拔，弯曲传输线。

8. Do not attempt to disassemble or process the OLED module.
请勿拆卸 OLED 模块。
9. NC terminal should be open. Do not connect anything.
悬空端应断开，不要连接任何器件。
10. If the logic circuit power is off, do not apply the input signals.
如果逻辑电路电源是断开的，不要施加输入信号。
11. Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
由于 OLED 显示模块使用 CMOS 集成，要特别注意静电放电问题。对 CMOS 器件，要特别注意静电。为防止静电造成的损坏，注意保持合宜的工作环境。
 - Before removing OLED from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the OLED modules.
-OLED 模块移出包装盒和安装之前，要保证模块和人体具有相同的电位。处理模块时可靠接地。
 - Tools required for assembling, such as soldering irons, must be properly grounded. Make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- 使用工具如电烙铁，必须正确接地，并确保烙铁使用的交流电不会漏电。用电动螺丝刀固定模块时，电动螺丝刀应接地，尽可能降低电动换向器火花产生的电磁波。
 - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dry. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- 为减少静电产生，不要在干燥的条件下进行组装等工作。为降低静电，工作环境一定不要干燥。建议相对湿度为 50%-60%。尽可能使你的工作服和工作台接地。
 - The OLED module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.
- OLED 模块表面有保护膜。需要小心操作因为撕保护膜时可能产生静电。


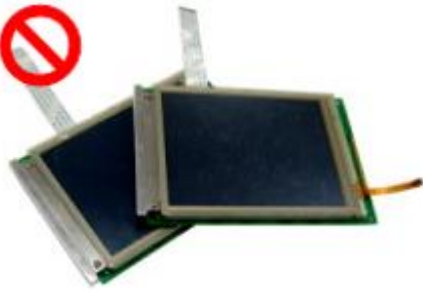



12. OLED module is easy to be damaged. Please note below and be careful for handling.

OLED 显示模块很容易被损坏. 请注意以下并小心操作

i. Correct handling (正确操作):

	
<p>As above picture, please handle with anti-static gloves around OLED module edges. 像上面的图片,请戴抗静电手套,并拿模块边缘.</p>	

ii. Incorrect handling(错误操作):

 <p>Don't touch IC directly 不要直接触摸 IC</p>	 <p>Don't stack OLED modules 不要堆叠 OLED 模块</p>
 <p>Don't hold the surface of panel 不要拿着面板的表面</p>	 <p>Don't stretch interface of input, such as FPC 不要拉扯输入接口, 如软排线</p>
 <p>Don't hold the surface of IC 不要拿着 IC 的表面</p>	 <p>Don't operate with sharp stick such as pens 不要用尖锐的物体来操作, 如笔尖</p>

◆ Precautions for Storing OLED Module (OLED 模块存储注意事项)**1. When storing the OLED modules, the following precautions are necessary.**

OLED 模块的存储依照以下几点:

- i. Store them in a sealed polyethylene bag with the desiccant.

使用干燥剂和聚乙烯袋密封包装。

- ii. Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C, and keep the relative humidity between 40%RH and 60%RH.

避光保存, 避免直接暴露在太阳光或黄光灯下, 保持温度在 0~35 摄氏度之间, 保持相对湿度在 40%RH 和 60%RH 之间。

- iii. The polarizer surface should not come in contact with any other objects (We advise you to store them in the anti-static electricity container in which they were shipped).

偏光片表面避免接触其他物质 (建议存放在货运防静电包装中)。

2. Others

- i. If the OLED modules have been operating for a long time, it will cause brightness decay. It is not recommended to showing the same display patterns for a long time, otherwise the display patterns may remain on the screen as ghost images and it is unrecoverable.

OLED 长时间点亮会有亮度衰减, 所以尽量避免长时间工作于同一个显示图案, 否则会造成鬼影, 这是不可恢复的。

- ii. To minimize the performance degradation of the OLED modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

为最小限度地降低由静电等导致 OLED 模块性能降低, 使用模块时慎重使用下列区域:

- Exposed area of the printed circuit board.

印制电路板裸露区域。

-Terminal electrode sections.

印制电路板引出端子区域。

◆ Using OLED Modules (OLED 模块使用注意事项)

Installing OLED Modules (安装 OLED 模块)

- i. When assembling the OLED module into other equipment, the spacer to the bit between the OLED module and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be $\pm 0.1\text{mm}$.

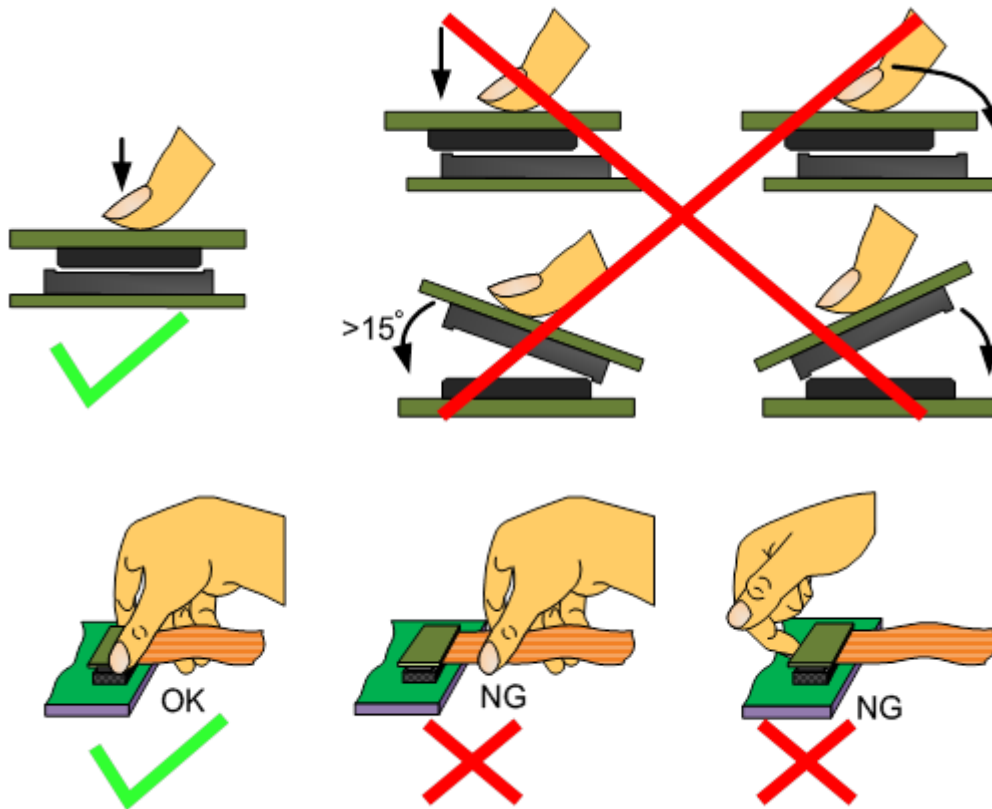
将 OLED 模块安装进入其它设备时，模块和安装板之间间隔应有足够的高度以避免模块表面受压。参照专业度量技术标准。量度公差应是 ± 0.1 毫米。

- ii. Precaution for assemble the module with BTB connector:

用板对板连接器安装 OLED 显示模块注意事项:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows.

请注意连接器的公母及连接位置，请勿出现下图所示的连接方式。



◆ Precautions for Soldering OLED Module (OLED 模块焊接注意事项)

	Manual soldering 手工焊接	Machine drag soldering 机器拖焊	Machine press soldering 机器压焊
No RoHS Product 非环保产品	290°C ~350°C. Time: 3-5S.	330°C ~350°C. Speed : 15-17 mm/s.	300°C ~330°C. Time: 3-6S. Press: 0.8~1.2Mpa
RoHS Product 环保产品	340°C ~370°C. Time: 3-5S.	350°C ~370°C. Speed : 15-17 mm/s.	330°C ~360°C. Time: 3-6S. Press: 0.8~1.2Mpa

1. If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation (This does not apply in the case of a non-halogen type of flux). It is recommended that you protect the OLED surface with a cover during soldering to prevent any damage due to flux spatters.
如果使用助焊剂，完成焊接后一定要清除剩余的助焊剂（除非卤化物助焊剂）。建议焊接时用盖子保护显示屏面以避免因助焊剂溅出造成的任何损坏。
2. When soldering the OLED module and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
焊接 OLED 模块和线路板时，不应装卸多于三次。尽管焊接温度会有变化，但不应超过上面提到的焊接温度和时间最大值。
3. When remove the OLED module from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.
从线路板上移除 OLED 模块时，要保证焊锡已完全熔化，不要损坏线路板上的焊接位。

◆ **Precautions for Operation (工作运行注意事项)**

1. OLED is a self-light device, do not use back character on a white background display mode, otherwise the power consumption will be higher and it will cause crosstalk.
OLED 是自发光器件，不要使用白底黑字显示模式，否则功耗增大，并且会有交叉效应。
2. It is an indispensable condition to drive OLED's within the specified voltage limit since the higher voltage than the limit will damage the driver IC.
在 OLED 驱动电压内来操作模块是必要的。超过限定电压会损坏集成电路。
3. The brightness will be lower at lower temperature than the normal temperature and will be higher at higher temperature. However those phenomena do not mean malfunction or out of order with OLED's, which will come back in the normal temperature.
OLED 亮度在低温时比常温要暗，高温时，会比常温要高。然而,这并不是指 OLED 显示屏工作异常，显示屏在恢复常温时，效果会恢复正常。
4. If the display area is pushed hard, the display will cause pixel short, it will become the display defect.
如果在运行过程中显示区受到挤压,将可能引起像素短路，引起显示缺陷。
5. A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
接线端冷凝会引起电化学反应而断路。因此必须在最大的操作温度之内，湿度小于 50% 的条件下使用 OLED 模块。
6. Input logic voltage before apply analog high voltage such as OLED driving voltage when power on. Remove analog high voltage before logic voltage when power off the module. Input each signal after the positive/negative voltage becomes stable.
开机时，先接通逻辑电压，再接通模拟高压，比如 OLED 驱动电压。关机时，先断开模拟高压，再关逻辑电压。正负电源都稳定后再送控制信号。
7. Please keep the temperature within the specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
模块在操作和存储规格范围内使用。高温高湿可能会引起偏振退化，起泡，偏光片脱落等问题。

◆ **Safety (安全)**

1. It is recommended to crush damaged or unnecessary OLED into pieces.
建议将损坏的 OLED 显示屏压成碎片。
2. If any solid or powder leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.
如果任何固体或粉末从玻璃种泄漏出且与手接触,要用肥皂和水彻底清洗。

◆ **Limited Warranty (有限质保)**

Unless agreed between TRULY and the customer, TRULY will replace or repair any of its OLED modules which are found to be functionally defective when inspected in accordance with TRULY OLED acceptance standards (copies available upon request) for a period of one year from date of production. Cosmetic/visual defects must be returned to TRULY within 90 days of shipment. Confirmation of such date shall be based on data code on product. The warranty liability of TRULY limited to repair and/or replace on the terms set forth above. TRULY will not be responsible for any subsequent or consequential events.

除信利和客户之间另有协议外，自生产之日起一年内，根据信利的OLED显示屏品质标准，信利将对有功能缺陷的OLED显示模块换货或返工。外观/视觉缺陷产品，必须在出货后90天内归还信利。以产品上标识日期为准。信利保修责任仅限于对符合上述规定的货品进行返工和/或换货。对此后发生的任何情况，信利均不承担任何责任。

◆ **Return OLED Module under Warranty (OLED 模块返修质保)**

1. No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are :

保修是以上述注意事项未被忽视为先决条件的。典型的违反例子如下：

- Broken OLED glass.
-断裂的 OLED 显示屏玻璃。
- PCB eyelet is damaged or modified.
-印制线路板孔修改或损坏。
- PCB conductors damaged.
-线路板导体损坏。
- Circuit modified in any way, including addition of components.
-线路随意变更，包括元件变化。
- PCB tampered with by grinding, engraving or painting varnish.
-印制电路板已修改，如研磨，雕刻，绘涂等。
- Soldering to or modifying the bezel in any manner.
-焊接或变动模块

2. Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB eyelet, conductors and terminals.

模块维修清单将按双方协议送呈客户。模块详细缺陷描述须模块一并退回。顾客安装的连接器和电缆必须在不破坏线路板孔，线路和引线端条件下全部移去。

■ **PRIOR CONSULT MATTER (提前商议事项)**

1. For Truly standard products, we keep the right to change material, process ... for improving the product property without prior notice to our customer.
对于信利的标准产品,我们保留在不通知客户的情况下,为提高产品性能而改变原材料及加工方法等的权利。
2. For OEM products, if any changes are needed which may affect the product property, we will consult with our customer in advance.
对于 OEM 产品, 如果需要做任何会影响到产品性能的改变, 我们会提前和客户商议。
3. If you have special requirement about reliability condition, please let us know before you start the test on our samples.
如对可靠性条件有特殊要求, 请在产品测试前通知我们。

■ **FACTORY CONTACT INFORMATION (工厂联系信息)**

1. FACTORY NAME: TRULY SEMICONDUCTORS LTD.
工厂名称:信利半导体有限公司
2. FACTORY ADDRESS: Truly Industrial Area, ShanWei City,GuangDong,China
工厂地址: 中国广东省汕尾市信利工业城
3. P.C: 516600 URL: <http://www.truly.com.hk>; <http://www.trulysemi.com>
邮政编码: 516600 网站:<http://www.truly.com.hk>; <http://www.trulysemi.com>