V30D202C



Vishay General Semiconductor

Dual High-Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier







DESIGN SUPPORT TOOLS AVAILABLE



PRIMARY CHARACTERISTICS					
I _{F(AV)}	2 x 15.0 A				
V _{RRM}	200 V				
I _{FSM}	260 A				
V_F at I_F = 15.0 A (T_A = 125 °C)	0.66 V				
T _J max.	175 °C				
Package	SMPD (TO-263AC)				
Circuit configuration	Common cathode				

FEATURES

- Trench MOS Schottky technology generation 2
- Very low profile typical height of 1.7 mm
- Ideal for automated placement
- · Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: SMPD (TO-263AC) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3_X - halogen-free, RoHS-compliant, and AEC-Q101 qualified (X_denotes revision code e.g. A, B, ...)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

Polarity: As marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V30D202C	UNIT	
Maximum repetitive peak reverse voltage		V _{RRM}	200	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	30	А	
	per diode		15	~	
Maximum DC reverse voltage		V _{DC}	160	V	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	260	А	
Voltage rate of change (rated V _R)		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T _J , T _{STG}	-40 to +175	°C	



COMPLIANT

HALOGEN

FREE

V30D202C



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F (1)	0.72	-	V	
	I _F = 10 A			0.78	-		
	I _F = 15 A			0.8	0.88		
	I _F = 5 A	T _A = 125 °C		0.56	-		
	I _F = 10 A			0.64	-		
	I _F = 15 A			0.66	0.73		
Reverse current at rated V_R per diode	V _B = 160 V	T _A = 25 °C	I _R (2)	1	-	μA	
	$v_{\rm R} = 100 v$	T _A = 125 °C		2	-	mA	
	V _R = 200 V	T _A = 25 °C		_	200	μA	
		T _A = 125 °C		5	25	mA	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V30D202C	UNIT	
Typical thermal resistance	per diode	$\frac{R_{\theta JC}}{R_{\theta JA}^{(1)(2)}}$	2.0		
	per device		1.1	°C/W	
	per device		50		

Notes

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$ - junction-to-mount

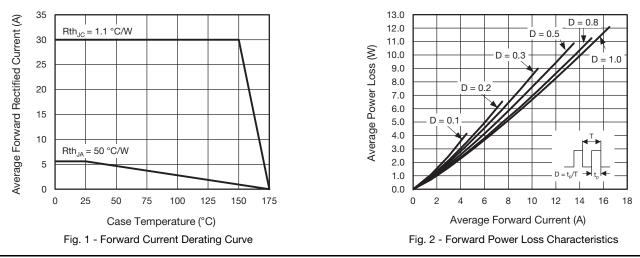
(2) Free air, without heatsink

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
SMPD (TO-263AC)	V30D202C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel	
SMPD (TO-263AC)	V30D202CHM3_A/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



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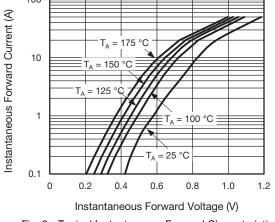


Fig. 3 - Typical Instantaneous Forward Characteristics

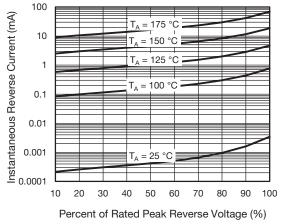


Fig. 4 - Typical Reverse Characteristics

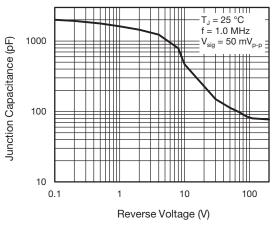


Fig. 5 - Typical Junction Capacitance

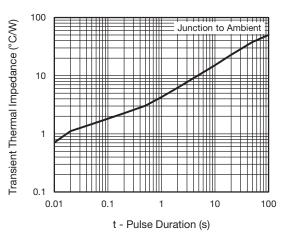
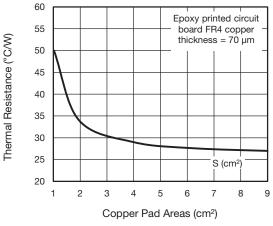
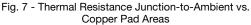


Fig. 6 - Typical Transient Thermal Impedance





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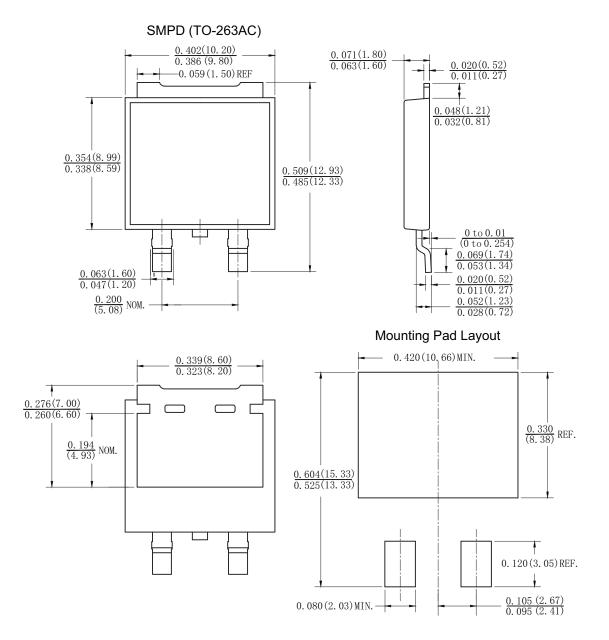
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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