

COMPLIANT

High Performance Schottky Rectifier, 400 A



PRIMARY CHARACTERISTICS				
I _{F(AV)}	400 A			
V _R	45 V			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

FEATURES

- 150 °C T_J operation
- · Center tap module
- · Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

The VS-400CNQ045PbF center tap, high current, Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	400	А		
V _{RRM}		45	V		
I _{FSM}	t _p = 5 µs sine	29 000	Α		
V _F	200 A _{pk} , T _J = 125 °C (per leg)	0.52	V		
T _J	Range	-55 to +150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-400CNQ045PbF	UNITS	
Maximum DC reverse voltage	V _R	45	V	
Maximum working peak reverse voltage	V_{RWM}	7	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current per leg		50 % duty cycle at T _C = 114 °C, rectangular waveform		200	
See fig. 5 per device	I _{F(AV)}	30 % duty cycle at 16 = 114 C	o, rectangular waveloim	400	A
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated	29 000	
See fig. 7		10 ms sine or 6 ms rect. pulse	V _{RRM} applied	3400	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 19 A, L = 1 mH		180	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		40	А



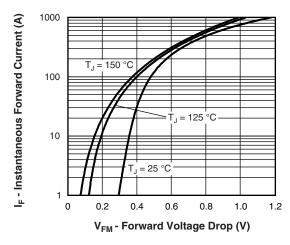
ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	200 A	T _{.1} = 25 °C	0.57	
		400 A	1]=25 0	0.73	V
See fig. 1		200 A	T _{.1} = 125 °C	0.52	
		400 A	1j = 125 C	0.7	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	V _B = Rated V _B	20	mA
See fig. 2		T _J = 125 °C	v _R = nateu v _R	1.2	Α
Threshold voltage	V _{F(TO)}	T _J = T _J maximum		0.32	V
Forward slope resistance	r _t			0.81	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		10 300	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane 5.0		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000			V/µs

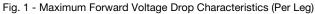
Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}	-55	-	150	°C	
Thermal resistance, junction to case per leg	В		-	0.19		
Thermal resistance, junction to case per module	R _{thJC}	-	-	0.095	°C/W	
Thermal resistance, case to heatsink	R _{thCS}	-	0.10	-		
Weight		-	68	-	g	
		-	2.4	-	OZ.	
Mounting torque		35.4 (4)		53.1 (6)	lbf · in (N · m)	
Mounting torque center hole		30 (3.4)		40 (4.6)		
Terminal torque		30 (3.4)	-	44.2 (5)		
Vertical pull		-	-	80		
2" lever pull		-	-	35	lbf · in	







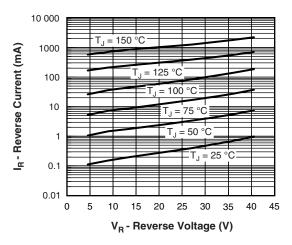


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

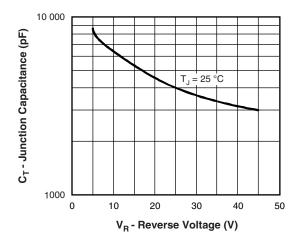


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

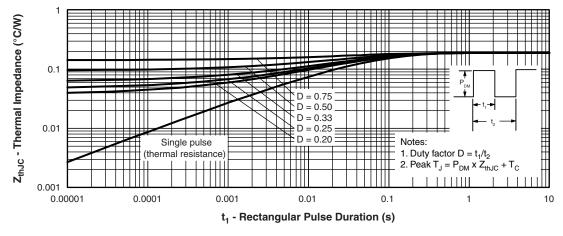


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics (Per Leg)



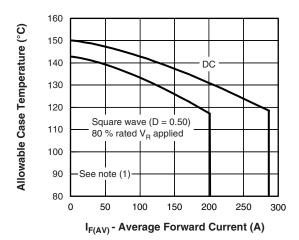


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

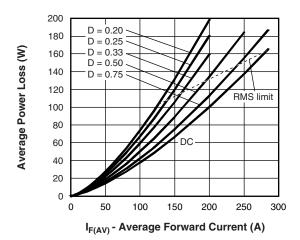


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

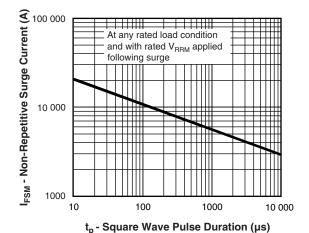


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

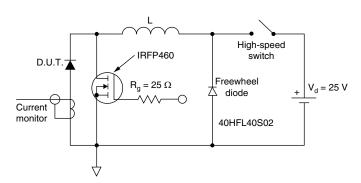


Fig. 8 - Unclamped Inductive Test Circuit

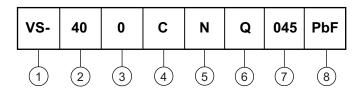
Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{th,JC}; Pd = forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R



ORDERING INFORMATION TABLE

Device code



1 - Vishay Semiconductors product

Average current rating (x 10)

Product silicon identification

- C = circuit configuration

5 - N = not isolated

6 - Q = Schottky rectifier diode

7 - Voltage rating (045 = 45 V)

8 - Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95021			



TO-244

DIMENSIONS in millimeters (inches)









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