

(OPTOELECTRONIC DIV.)

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TXG15007BFL70C ROHS DATA SHEET

Acceptance

ISSUE	VERSION	APPROVER	CHECKER	ENGINEER
拿典 03/06 Jy-Hao	В	筝典 03/06 Donlin		拿典 03/06 Alan

Messrs.						
Product Specification	Model:	TVC15007DEL 70C	Rev. NO.	Issued Date.		
	Miduel.	TXG15007BFL70C	В	Mar,06.20		

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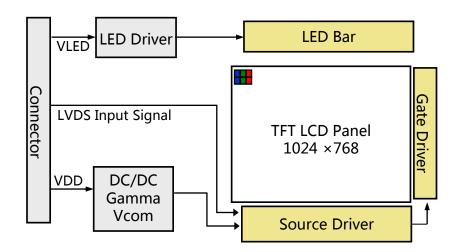


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	Middel.	IAG 1500/ BFL/0C	В	Mar,06.20

1.0 GENERAL DESCRIPTION

1.0.1 Introduction

TXG15007BFL70C is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 15.0 inch diagonally measured active area with XGA resolutions (1024 horizontal by 768 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.2M colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.0.2 Features

- LED back-light
- LED light bar replaceable
- LVDS interface
- RoHS Compliant

1.0.3 Application

- TFT-LCD Monitor
- Reliability Application



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1.0.4 General Specification

< Table 1. General Specifications >

Parameter	Specification	Unit	Remarks
Active area	304.13 (H) × 228.1(V)	mm	
Number of pixels	1024(H) × 768(V)	Pixels	
Pixel pitch	0.297(H) × 0.297 (V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.2M	Colors	6bit+FRC
Display mode	Normally White		
Dimensional outline	$342.8(H) \times 267.8(V) \times 10.8(D)$ typ.	mm	
Weight	TBD	g	
Surface treatment	Haze 25%, 3H		
Back-light	Edge side, 1-LED Lighting Bar Type		128*LED
LED life	30,000	hr	min

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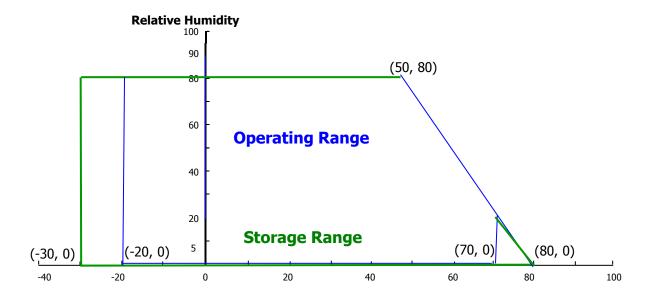
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

[Ta =25 ± 2 °C]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Back-light Power Supply Voltage	HV_{DDOUT}	-0.3	24	V	
Back-light LED Current	I _{HVDD}	-	-	mA	
Back-light LED Reverse Voltage	V _R	-	40	V	
Operating Temperature	T _{OP}	-20	70	$^{\circ}$	Note 1
Storage Temperature	T _{ST}	-30	80	$^{\circ}$	Note.1

Note: 1) Temperature and relative humidity range are shown in the figure below.





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3.0 ELECTRICAL SPECIFICATIONS

3.0.1 TFT LCD Module

< Table 3. LCD Module Electrical Specifications >

[Ta =25 ± 2 °C]

Parameter	Symbol		Values		Unit	Notes	
		Min	Тур	Max		110100	
Power Supply Input Voltage	V_{DD}	3.0	3.3	3.6	V	Note 1	
Power Supply Current	I _{DD}	•	520	700	mA	ivole i	
Positive-going Input Threshold Voltage	V _{IT+}	-		+100	mV	Vcom = 1.2V	
Negative-going Input Threshold Voltage	V _{IT-}	-100		ı	mV	typ.	
Differential input common mode voltage	V _{com}		1.2		V	V _{IH} =100mV, V _{IL} =-100mV	

- Notes : 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for 3.3V at 25 $^{\circ}$ C Max value at Black Pattern
 - 2. Calculated value for reference $I_{LED} \times V_{LED} \div 0.89 = P_{LED}$

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3.2 Back-light Unit

Table 2-1: Electrical specification

Parameter		Symbol	Values			Unit	notes	
ralametei			Symbol	Min	Тур	Max	Unit	notes
LED Driver :								
Power Supply Inpu	t Voltage		VBL	10.2	12.0	13.8	Vdc	1
Power Supply Input	Current		IBL	-	0.85	1.08	Α	1
Power Supply Input Current (In-Rush)		In-rush	-	-	(TBD)	A	V _{BL} = 12.0V ExtV _{BR-B} = 100% 3	
Power Consumption	n		PBL	-	10.2	15	W	1
	On/Off	On	V on	2.5	-	5.5	Vdc	
	On/On	Off	V off	-0.3	0.0	0.5	Vdc	
	Brightness	Adjust	ExtV _{BR-B}	30	-	100	%	On Duty
Input Voltage for	Drigituless	Aujust	LALV BR-B	30	-	100	%	5
Control System Signals	ExtV _{BR-B} Frequency		f _{PWM}	500	-	1500	Hz	
	Pulse Duty Level		High Level	2.5	-	5.5	Vdc	HIGH : on duty
(PWM)		Low Level	0.0	-	0.5	Vdc	LOW : off duty	
LED :	-							
Life Time				30,000	50,000		Hrs	2

notes:

- 1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 60 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 12Vand VBR (ExtVBR-B: 100%), it is total power consumption.
- 2. The life time (MTTF) is determined as the time which luminance of the LED is 50% compared to that of initial value at the typical LED current (ExtVBR-B :100%) on condition of continuous operating in LCM state at $25\pm2^{\circ}$ C.
- 3. The duration of rush current is about 200ms. This duration is applied to LED on time.
- 4. Even though inrush current is over the specified value, there is no problem if I²T spec of fuse is satisfied. ExtV_{BR-B} signal have to input available duty range and sequence.
- 5. After Driver ON signal is applied, $ExtV_{BR-B}$ should be sustained from 30% to 100% more than 500ms. After that, $ExtV_{BR-B}$ 30% and 100% is possible



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4.0 OPTICAL SPECIFICATION

4.0.1 Overview

The test of view angle range shall be measured in a dark room (ambient luminance \leq 1lux and temperature = $25\pm2^{\circ}$ C) with the equipment of Luminance meter system (Goniometer system and TOPCON BM-5A) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta \varnothing = 0$ (= $\theta 3$) as the 3 o'clock direction (the "right"), $\theta \varnothing = 90$ (= $\theta 12$) as the 12 o'clock direction ("upward"), $\theta \varnothing = 180$ (= $\theta 9$) as the 9 o'clock direction ("left") and $\theta \varnothing = 270$ (= $\theta 6$) as the 6 o'clock direction ("bottom"). While scanning θ and/or \varnothing , the center of the measuring spot on the Display surface shall stay fixed. The luminance, color and uniformity (etc) should be tested by BM-5A. The backlight should be operating for 10 minutes prior to measurement. VDD shall be 3.3 \pm 0.3V at 25°C. Optimum viewing angle direction is 6 'clock

<Table 5. Optical Specifications>

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Harizantal	Θ_3		-	80	-	Deg.	
	Horizontal	Θ_9	CR > 5	1	80	-	Deg.	
	Vertical	Θ ₁₂	UK > 5	ı	80	-	Deg.	
Viewing Angle	vertical	Θ_6		-	80	-	Deg.	Note 1
range	Horizontal	Θ_3		-	80	-	Deg.	INOLE
	TIONZONIai	Θ_9	CR > 10	-	80	-	Deg.	
	Vertical	Θ ₁₂	OK > 10	-	80	-	Deg.	
	vertical	Θ_6		ı	80	-	Deg.	
Luminance Co	ntrast ratio	CR	Θ = 0°	400	700	-		Note 2
Luminance of White	9points max	Y _w		720	800	-	cd/m ²	Note 3
White Luminance uniformity	9 Points	ΔΥ9	⊝ = 0°	75	80	-	%	Note 4
Reproduction	\	Wx	0 00	Тур	0.313	Тур		Note 5
of color White		Wy	⊙ = 0°	-0.03	0.329	+0.03		Note 5
Response Time		T _{RT}	Ta= 25° C Θ = 0°		8	12	ms	Note 6
Cross	Гаlk	СТ	⊝ = 0°	-	-	2.0	%	Note 7



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Notes: 1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing angles are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface (see FIGURE 1).

2. Contrast measurements shall be made at viewing angle of Θ = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state . (see FIGURE 1) Luminance Contrast Ratio (CR) is defined mathematically.

3. Luminance of white is defined as luminance values of 9point max across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display. The luminance is measured by BM-5A when the LED current is set at 50mA.

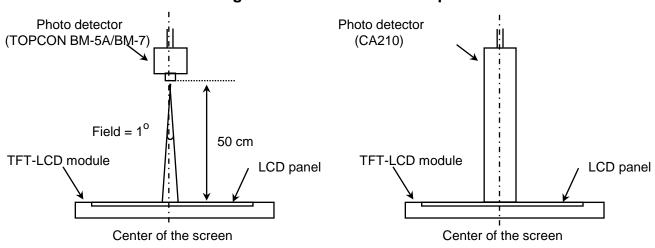
- 4. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = Minimum Luminance of 9 points / Maximum Luminance of 9 points (see FIGURE 2).$
- 5. The color chromaticity coordinates specified in Table 5. shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel.
- 6. The electro-optical response time measurements shall be made as FIGURE 3 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr, and 90% to 10% is Td.
- 7. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See FIGURE 4).



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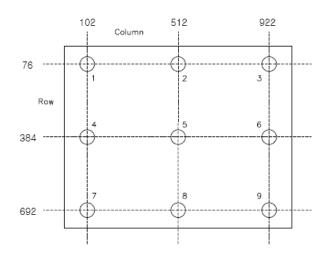
4.0.2 Optical measurements

Figure 1. Measurement Set Up



View angel range, uniformity, etc. measurement setup Flicker, measurement setup

Figure 2. White Luminance and Uniformity Measurement Locations (9 points)



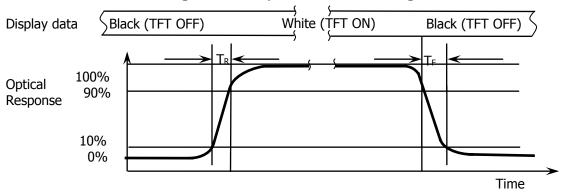
Luminance of white is defined as luminance values of max 9 points across the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.

The White luminance uniformity on LCD surface is then expressed as : $\Delta Y9 = Minimum Luminance of 9 points / Maximum Luminance of 9 points (see FIGURE 2).$



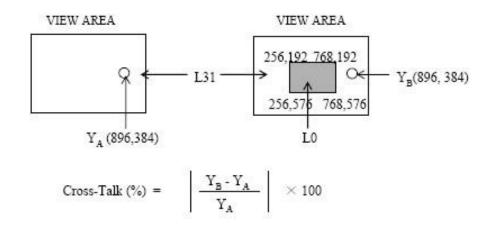
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Figure 3. Response Time Testing



The electro-optical response time measurements shall be made as shown in FIGURE 3 by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Tr and 90% to 10% is Td.

Figure 4. Cross Modulation Test Description



Where:

YA = Initial luminance of measured area (cd/m2)

YB = Subsequent luminance of measured area (cd/m2)

The location measured will be exactly the same in both patterns



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5.0 INTERFACE CONNECTION.

5.0.1 Electrical Interface Connection

The electronics interface connector is DF14H-20P-1.25H.

The connector interface pin assignments are listed in Table 6 and 7.

<Table 4. Pin Assignments for the Interface Connector>

Terminal	Symbol	Functions
Pin No.	Symbol	Description
1	VDD	Power Supply,3.3V(typical)
2	VDD	Power Supply,3.3V(typical)
3	VSS	Ground
4	NC	No Connection
5	RIN0-	-LVDS differential data input
6	RIN0+	+LVDS differential data input
7	VSS	Ground
8	RIN1-	-LVDS differential data input
9	RIN1+	+LVDS differential data input
10	VSS	Ground
11	RIN2-	-LVDS differential data input
12	RIN2+	+LVDS differential data input
13	VSS	Ground
14	CLKIN-	-LVDS differential clock input
15	CLKIN+	+LVDS differential clock input
16	VSS	Ground
17	RIN3-	-LVDS differential data input
18	RIN3+	+LVDS differential data input
19	VSS	Ground
		LVDS 6/8 bit select function control
20	SEL 6/8	High -> 6bit input mode
		Low -> 8bit input mode

5.0.2 Backlight Unit

P001:Input terminal

PH2.0-6 (2.0mm X 6)

Pin No.	Symbol	Description	note
1	VCC	Power supply voltage +12V	
2	VCC	Power supply voltage +12V	
3	ON/OFF	Output enable signal	
4	DIM	Dimming signal	
5	GND	Power ground	
6	GND	Power ground	



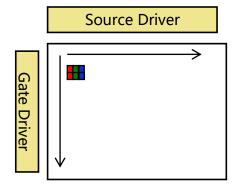
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5.2 Data Input Format

Figure 4. Pixel Format (1,1)(2,1)(1023,1)(1024,1)G G G G 1 Pixel = 3 DotsG G G G G (1023,768) (1024, 768) (1,768)(2,768)

Display Position of Input Data (V-H)

Figure 5. Scan direction





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6.0 SIGNAL TIMING SPECIFICATION

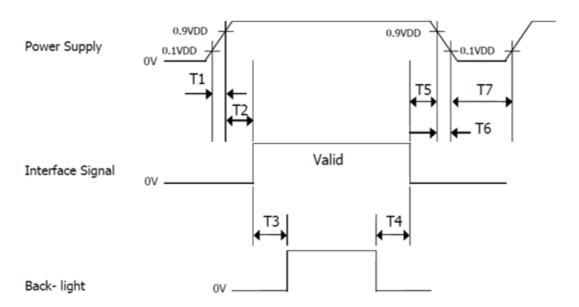
6.0.1 The TXG15007BFL70C is operated by the DE only.

Danamatan	O. was be a l		l limit		
Parameter	Symbol	Min.	Тур.	Max.	Unit
DCLK Frequency	fclk	52	65	71	MHz
Horizontal display area	thd		1024		pixel
HSYNC period time	th	1114	1344	1400	pixel
HSYNC blanking	thb+ thfp	90	320	376	pixel
Vertical display area	Tvd		768		Н
VSYNC period time	Tv	778	806	845	н
VSYNC blanking	Tvb+ Tvfp	10	38	77	Н

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7.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



Parameter		Units		
	Min	Тур	Max	Units
T1	0	-	10	ms
T2	0	-	50	ms
Т3	200	-	-	ms
T4	500	-	-	ms
T5	0	-	50	ms
Т6	0	-	10	ms
Т7	500	-	-	ms

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8.0 MECHANICAL CHARACTERISTICS

8.0.1 Dimensional Requirements

<Table 8. Dimensional Parameters>

Parameter	Specification	Unit
Active Area	304.128 (H) $ imes$ 228.096(V)	mm
Number of pixels	1024(H) X768 (V) (1 pixel = R + G + B dots)	
Pixel pitch	0.297(H) $ imes$ 0.297 (V)	mm
Pixel arrangement	RGB Vertical stripe	
Display colors	16.2M (6bit+FRC)	colors
Display mode	Normally White	
Dimensional outline	342.8(H) $ imes$ 267.8(V) $ imes$ 10.8	mm
Weight	TBD	gram
Back-light	Edge side, 1-LED Lighting Bar Type	
LED life	30,000 (Min.)	hr



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9.0 RELIABILITY TEST

The Reliability test items and its conditions are shown in below.

<Table 9. Reliability test>

Item			Test condition	
High temperature storage			80 ℃, 240 hrs	
Low tempera	ature stora	age	-30 ℃, 240 hrs	
High temperature & high humidity operation			50 ℃, 80%RH, 240hrs	
High tempera	ture opera	ation	70 ℃, 240hrs	
Low temperat	ture opera	ition	-20℃, 240hrs	
Vibration test		Frequency	10/ 300/10 Hz,Sine X/Y/Z Direction	
		Gravity / AMP	1.5 G	
		Period	±X, ±Y, ±Z 30 min	
Shock test		Gravity	50G	
		Pulse width	11msec, sine wave	
		Direction	±X, ±Y, ±Z	
On/O	On/1min, Off/1min, 3,000 cycles			
ESD	Air		± 15KV, 150pF(330) 1sec, 8 points, 25 times/ point	
	Contact		± 8KV, 150pF(330) 1sec, 8 points, 25 times/ point	



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10.0 HANDLING & CAUTIONS

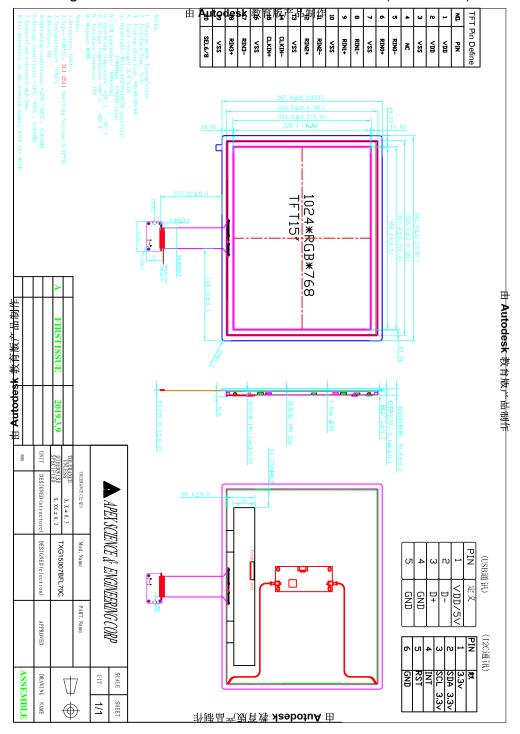
- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - When returning the module for repair or etc., Please pack the module not to be broken.
 We recommend to use the original shipping packages.



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11.0 MECHANICAL OUTLINE DIMENSION

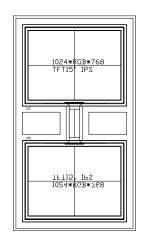
Figure 5. TFT-LCD Module Outline Dimension (Front View)



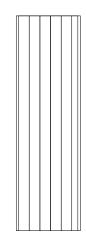


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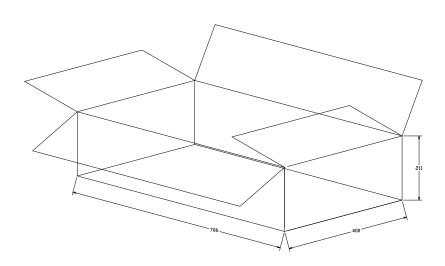
12 Packing



1. 泡棉包装,一层包含个产品



2. 包五层共10个产品



3,装入纸箱: 尺寸706长*400宽*211高