



SPECIFICATION
FOR
LCM Module

MODULE No:	KD043WQFPA042
CUSTOMER:	

STARTEK	INITIAL	DATE
PREPARED BY		
CHECKED BY		
APPROVED BY		

CUSTOMER	INITIAL	DATE
APPROVED BY		

Part. No	KD043WQFPA042	REV	V1.3	Page 1 of 29
常备库存 Stock For Sale	长期供货 Long Time supply	支持小量 NO MOQ	品种齐全 In Full Range	



SHENZHEN STARTEK ELECTRONIC TECHNOLOGY CO., LTD

Revision History

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* Description

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This module is composed of a Transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 4.3''TFT-LCD contains 480X272 pixels, and can display up to 65K/262K/16.7M colors.

* Features

General Information Items	Specification	Unit	Note
	Main Panel		
Display area(AA)	95.04(H)*53.86(V) (4.3inch)	mm	
Driver element	TFT active matrix	-	
Display colors	65K/262K/16.7M	colors	
Number of pixels	480(RGB)*272	dots	
Pixel arrangement	RGB vertical stripe	-	
Pixel pitch	0.198(H)*0.198(V)	mm	
Viewing angle	FREE	o'clock	
Controller IC	SC7283	-	
LCM Interface	24/8 BIT RGB	-	
Display mode	Transmissive /Normally black	-	
Operating temperature	-30~+85	°C	
Storage temperature	-30~+85	°C	

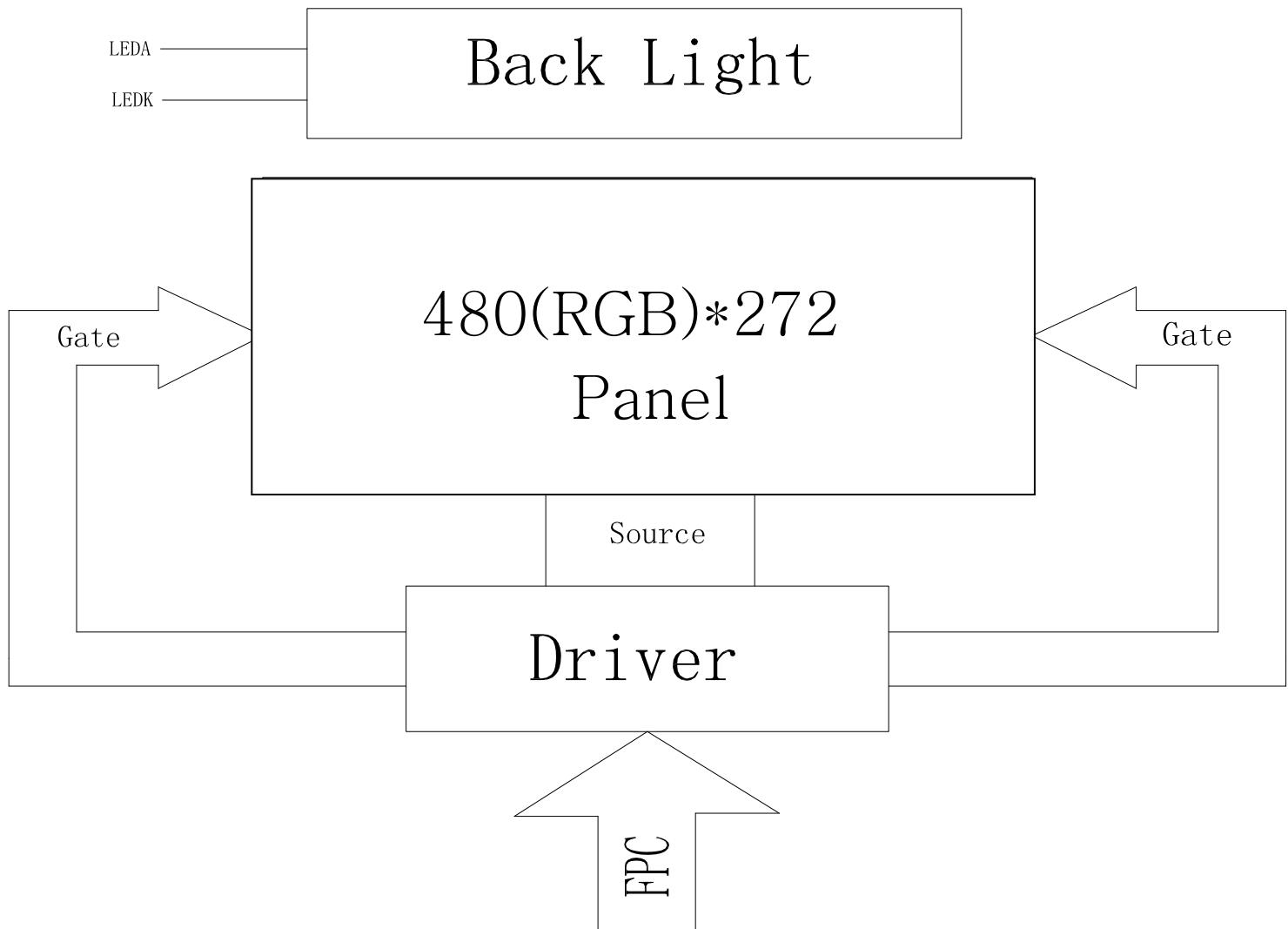
* Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)	-	105.4	-	mm	
	Vertical(V)	-	67.15	-	mm	
	Depth(D)	-	2.85	-	mm	
Weight		-	40	-	g	

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1. Block Diagram



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2. Outline dimension

The unmarked size shall be subject to CAD !

Note:The opening of top case must less than LCD POL 0.3mm at least, the LCD V.A is the Recommended opening of Lens.

Front view

Side view

Bottom view

NOTES:

- DISPLAY TYPE: 4.3", TFT-LCD, 65/262K/16.7M COLORS
- DISPLAY MODE: NORMALLY BLACK/IPS
- VIEWING DIRECTION: Free
- LOW DRIVER IC: SC77283 (COG)
- LCM Interface: 24Bit RGB Interface
- OPERATING TEMP: -30°C TO 85°C
- STORAGE TEMP: -40°C TO 85°C
- BACK LIGHT: LED WHITE, 10 LED, 40mA, 16±0.3V
- ROLLS COMPLIANT.
- * * * critical dimension; () reference dimension.

PCB Layout

PCB Components

PCB Dimensions

PCB Notes

PCB Schematic

PCB Assembly

PCB Test Points

PCB Inspection

PCB Revision History

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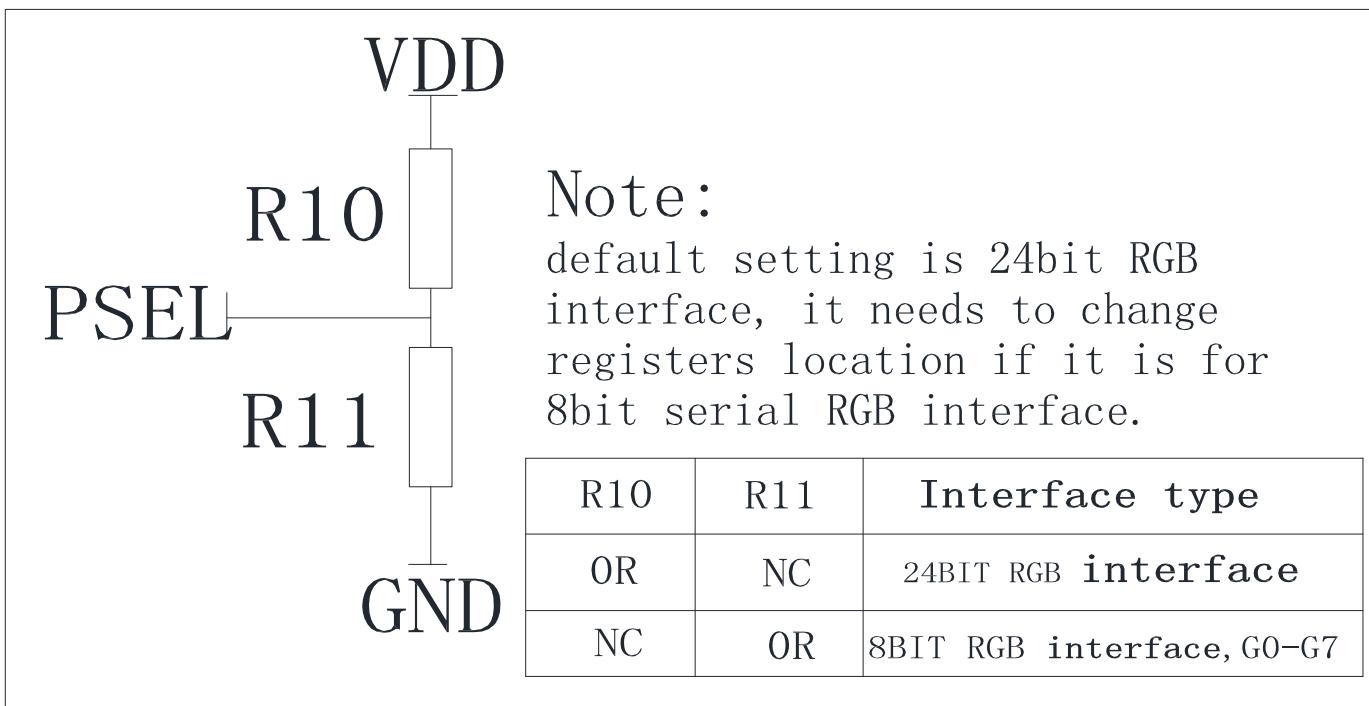
3. Input terminal Pin Assignment

NO.	SYMBOL	DESCRIPTION	I/O
1	LEDK	Cathode pin of backlight	P
2	LEDA	Anode pin of backlight	P
3	GND	Ground.	P
4	VDD	Supply voltage(3.3V).	P
5-12	R0-R7	8-bit digital Red data input	I
13-20	G0-G7	8-bit digital Green data input. Serial 8-bit RGB interface and input through DG[7:0].	I
21-28	B0-B7	8-bit digital Blue data input	I
29	GND	Ground.	P
30	PCLK	Clock signal; latching data at the falling edge	I
31	DISP	Display control / standby mode selection. DISP = “Low” : Standby; (Default) DISP = “High” : Normal display	I
32	H SYNC	Horizontal sync signal; negative polarity	I
33	V SYNC	Vertical sync signal; negative polarity	I
34	DE	Data input enable. Active High to enable the data input When not used in SYNC mode, user should connect it to “Low”.	I
35	NC	--	--
36	GND	Ground.	P
37	XR(NC)	Touch panel Right Glass Terminal	A/D
38	YD(NC)	Touch panel Bottom Film Terminal	A/D
39	XL(NC)	Touch panel Left Glass Terminal	A/D
40	YU(NC)	Touch panel Top Film Terminal	A/D

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FPC Interface set in



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4. LCD Optical Characteristics

4.1 Optical specification

Item	Symbol	Condition	Min.	Typ.	Max.	Unit.	Note
Contrast Ratio	CR	$\Theta=0$ Normal viewing angle	640	800	--		(1)(2)
Response time	Rising Falling		--	30	40	msec	(1)(3)
Uniformity	S(%)		45	50	--	%	C-light
Color Filter Chromacity	White	W _X	0.2813	0.3213	0.3613	CA-310	
		W _Y	0.3126	0.3526	0.3926		
	Red	R _X	0.5567	0.5967	0.6367		
		R _Y	0.3211	0.3611	0.4011		
	Green	G _X	0.3223	0.3623	0.4023		
		G _Y	0.5070	0.5470	0.5870		
	Blue	B _X	0.1095	0.1495	0.1895		
		B _Y	0.0752	0.1152	0.1552		
Viewing angle	Hor.	Θ_L	70	80	--	--	(1)(4)
		Θ_R	70	80	--		
	Ver.	Θ_U	70	80	--		
		Θ_D	70	80	--		
Option View Direction		FREE					(5)

*The data comes from the LCD specification.

Measuring Condition

Measuring surrounding : dark room

Ambient temperature : 25±2°C

15min. warm-up time.

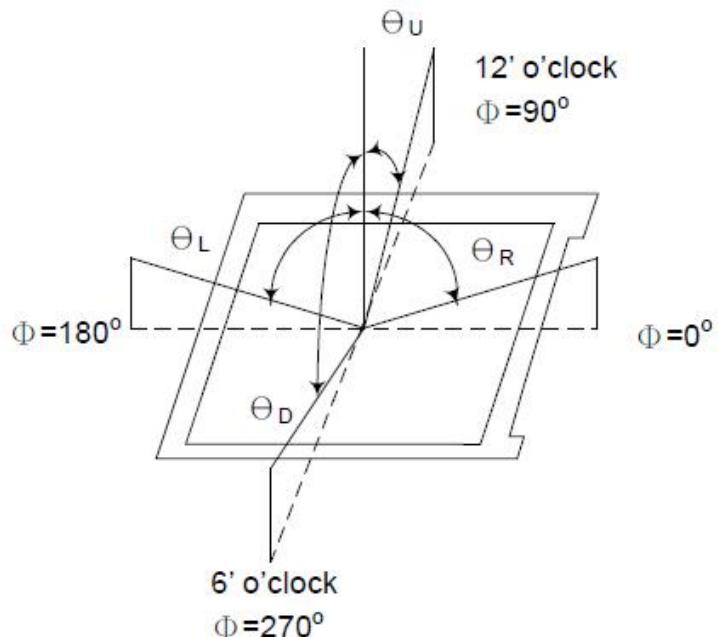
Measuring Equipment

FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.

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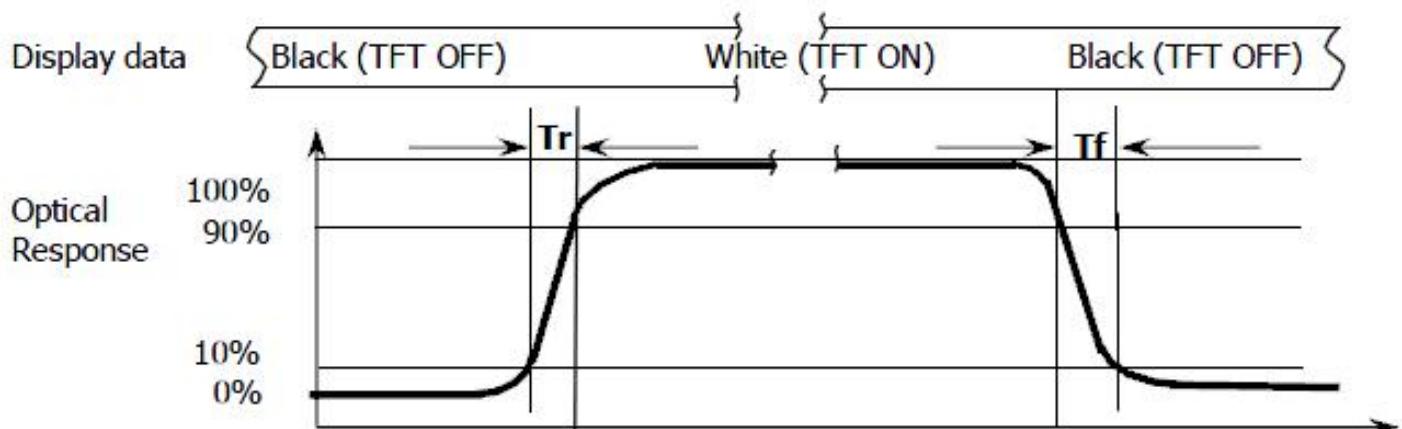
Note (1): Definition of Viewing Angle :



Note (2): Definition of Contrast Ratio(CR) :measured at the center point of panel

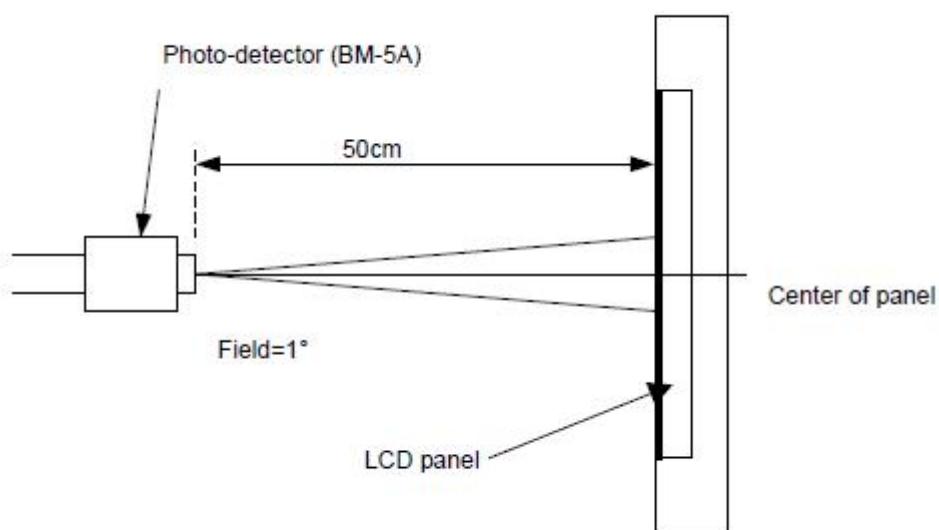
$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

Note (3): Response Time



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Note (4): Definition of optical measurement setup



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5. Electrical Characteristics

5.1 Absolute Maximum Rating

Characteristics	Symbol	Min.	Max.	Unit	Note
Digital Supply Voltage	VDD	-0.3	4.6	V	Note1
Operating temperature	T _{OP}	-30	+85	°C	
Storage temperature	T _{ST}	-30	+85	°C	

NOTE1: If the absolute maximum rating of even is one of the above parameters is exceeded even momentarily, the quality of the product may be degraded. Absolute maximum ratings, therefore, specify the values exceeding which the product may be physically damaged. Be sure to use the product within the range of the absolute maximum ratings.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	VDD	3.0	3.3	3.6	V	
Normal mode Current	IDD	--	23	46	mA	
Level input voltage	V _{IH}	0.7VDD	--	--	VDD	
	V _{IL}	GND	--	0.3VDD	V	
Level output voltage	V _{OH}	VDD-0.4	--	--	V	
	V _{OL}	GND	--	GND+0.4	V	

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5.3 LED Backlight Characteristics

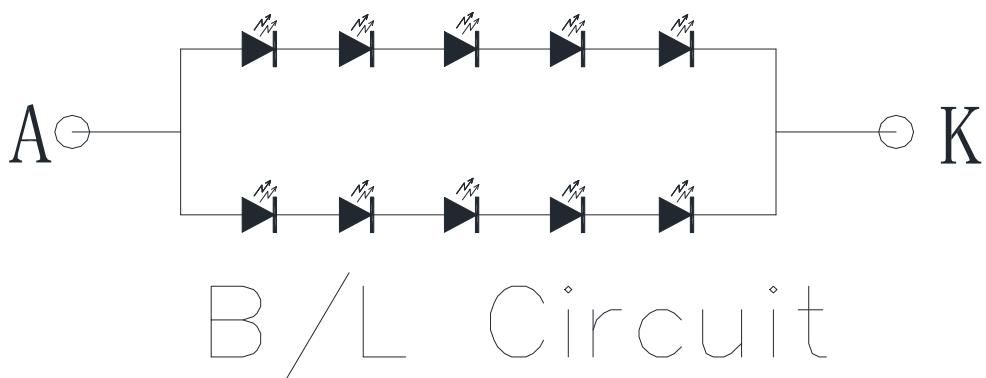
The back-light system is edge-lighting type with 10 chips LED

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I_F	35	40	--	mA	
Forward Voltage	V_F	--	16.0	--	V	
LCM Luminance	LV	850	900	--	cd/m ²	Note3
LED life time	Hr	50000		--	Hour	Note1,2
Uniformity	Avg	80	--	--	%	Note3

Note1: LED life time (Hr) can be defined as the time in which it continues to operate under the condition:

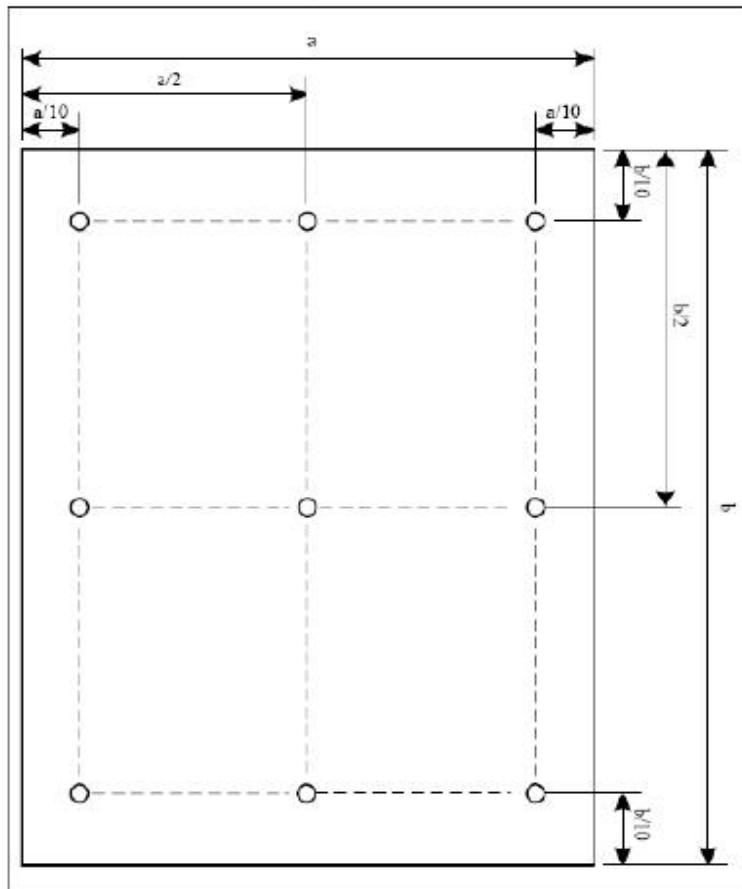
$T_a=25\pm3$ °C, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at $T_a=25$ °C and $IL=40$ mA. The LED lifetime could be decreased if operating IL is larger than 40mA. The constant current driving method is suggested.





Note (3) Luminance Uniformity of these 9 points is defined as below:



$$\text{Uniformity} = \frac{\text{minimum luminance in 9 points (1-9)}}{\text{maximum luminance in 9 points (1-9)}}$$

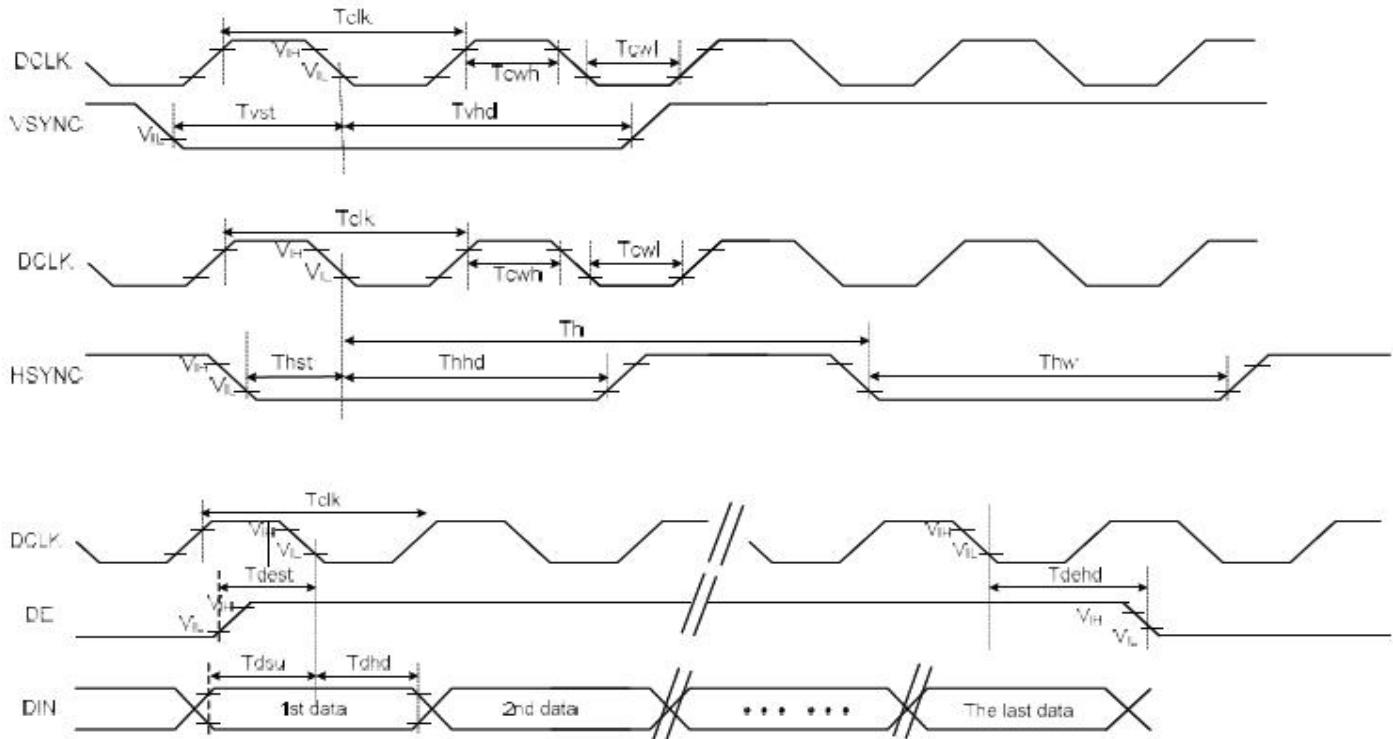
$$\text{Luminance} = \frac{\text{Total Luminance of 9 points}}{9}$$

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6. AC Characteristic

6.1 System Bus Timing for RGB Interface

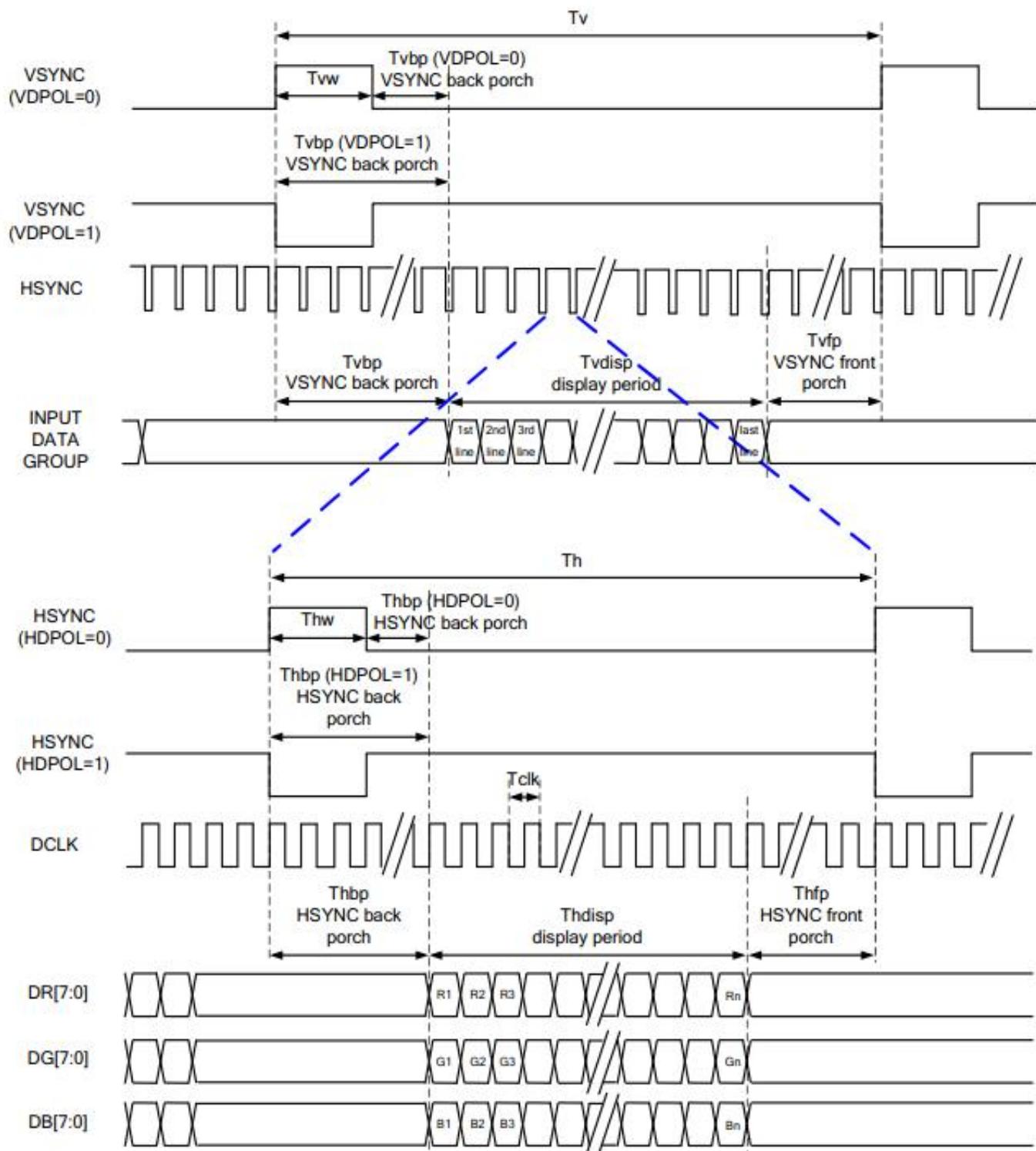


Item	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLK Pulse Duty	Tcw	40	50	60	%	
HSYNC Width	Thw	2	-	-	us	DCLK
HSYNC Period	Th	55	60	65	us	
VSYNC Setup Time	Tvst	12	-	-	ns	
VSYNC Hold Time	Tvh	12	-	-	ns	
HSYNC Setup Time	Thst	12	-	-	ns	
HSYNC Hold Time	Thhd	12	-	-	ns	
Data Setup Time	Tdsu	12	-	-	ns	
Data Hold Time	Tdhd	12	-	-	ns	
DE Setup Time	Tdest	12	-	-	ns	
DE Hold Time	Tdehd	12	-	-	ns	



6.2 RGB Interface

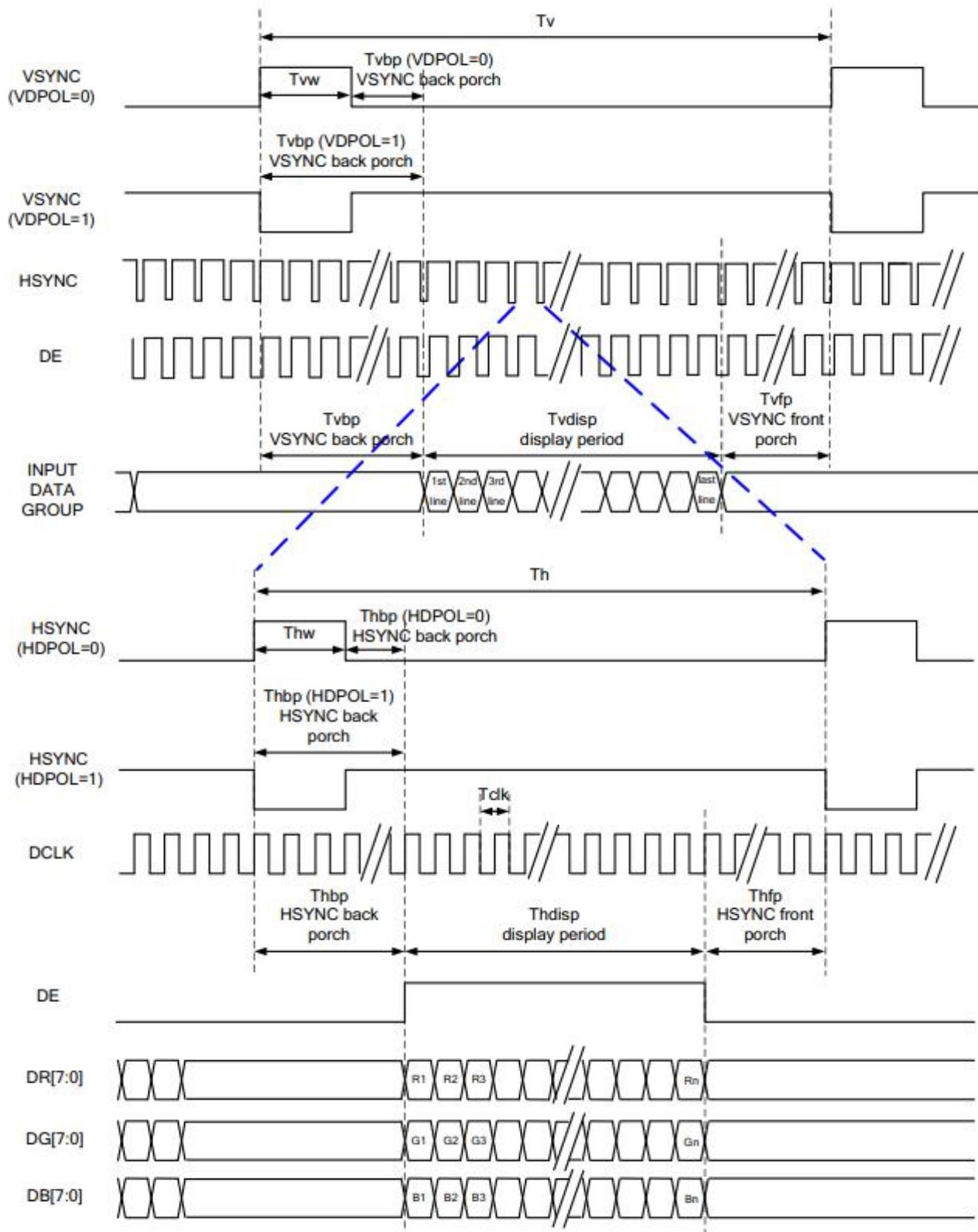
6.2.1 SYNC Mode



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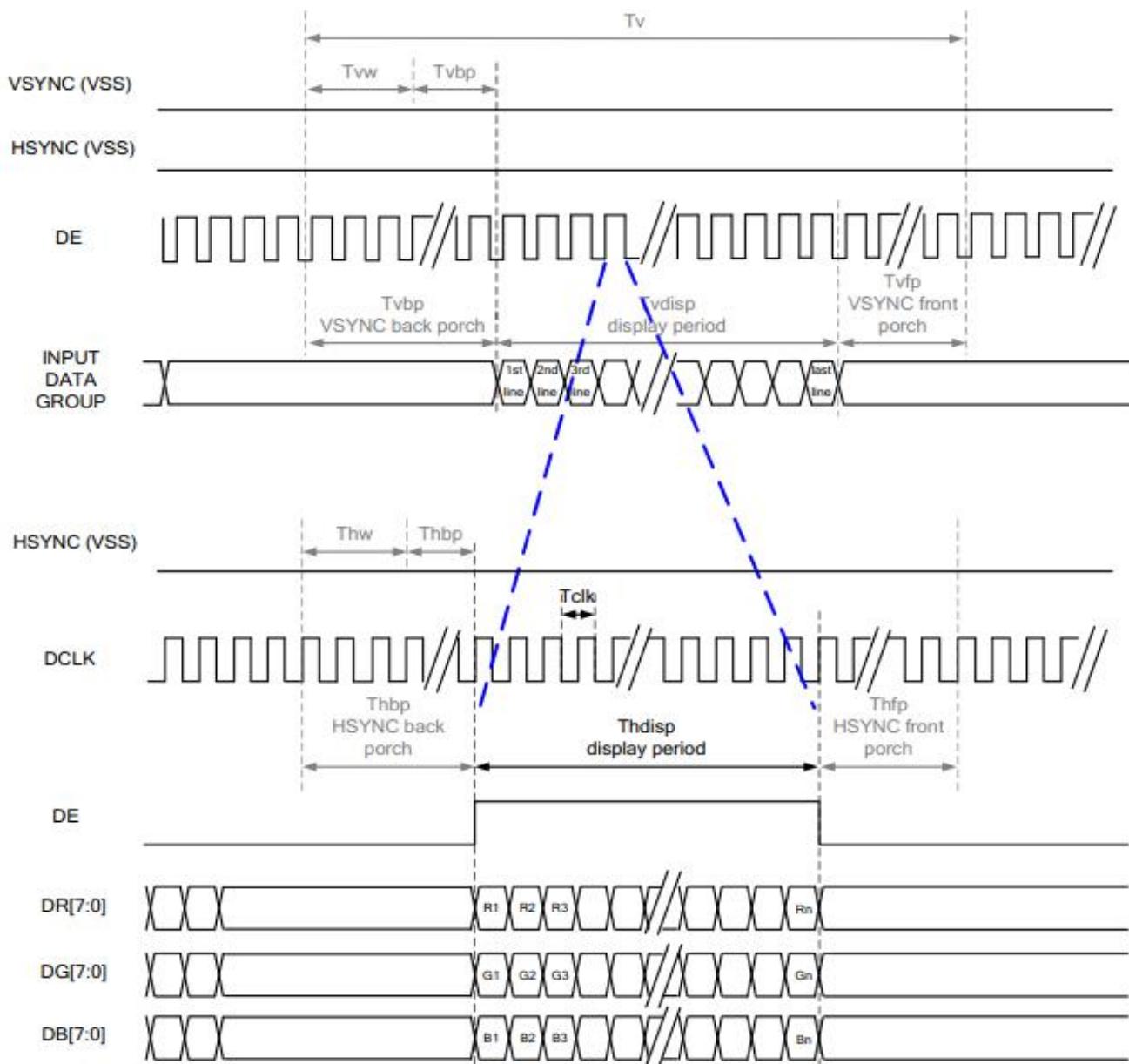


6.2.2 SYNC-DE Mode





6.2.3 DE Mode



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.



6.3 RGB Input Timing Table

6.3.1 Parallel 24-bit RGB Timing Table

Parallel 24-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

480RGB X 272 Resolution Timing Table						
Item	Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency	Fclk	8	9	12	MHz	
DCLK Period	Tclk	83	111	125	ns	
H SYNC	Period Time	Th	485	531	598	DCLK
	Display Period	Thdisp		480		DCLK
	Back Porch	Thbp	3	43	43	DCLK
	Front Porch	Thfp	2	8	75	DCLK
	Pulse Width	Thw	2	4	43	DCLK
V SYNC	Period Time	Tv	276	292	321	H SYNC
	Display Period	Tvdisp		272		H SYNC
	Back Porch	Tvbp	2	12	12	H SYNC
	Front Porch	Tvfp	2	8	37	H SYNC
	Pulse Width	Tvw	2	4	12	H SYNC

Note: It is necessary to keep $Tvbp = 12$ and $Thbp = 43$ in sync mode. DE mode is unnecessary to keep it.

6.3.2 Series 8-bit RGB Timing Table

Serial 8-bit RGB Input Timing (PVDD=VDD=VDDI= 3.3V, AGND= 0V, TA=25°C)

480RGB X 272 Resolution Timing Table						
Item	Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency	Fclk	24	27	30	MHz	
DCLK Period	Tclk	33	37	42	ns	
H SYNC	Period Time	Th	1445	1491	1558	DCLK
	Display Period	Thdisp		1440		DCLK
	Back Porch	Thbp	3	43	43	DCLK
	Front Porch	Thfp	2	8	75	DCLK
	Pulse Width	Thw	2	4	43	DCLK
V SYNC	Period Time	Tv	276	292	321	H SYNC
	Display Period	Tvdisp		272		H SYNC
	Back Porch	Tvbp	2	12	12	H SYNC
	Front Porch	Tvfp	2	8	37	H SYNC
	Pulse Width	Tvw	2	4	12	H SYNC

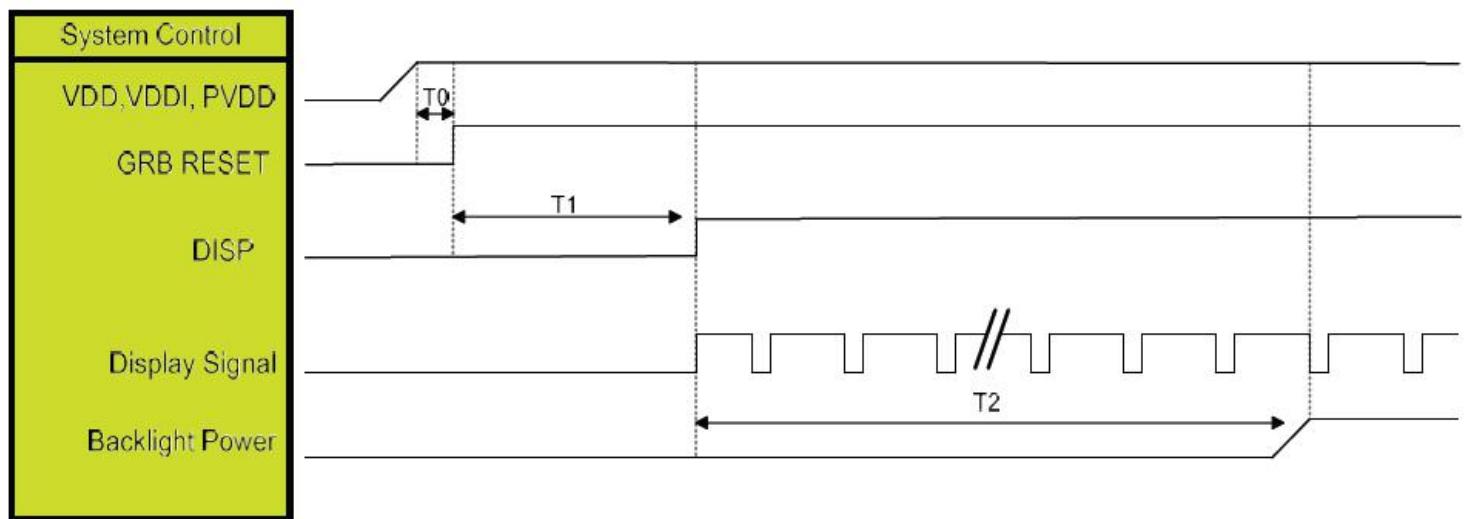
Note: It is necessary to keep $Tvbp = 12$ and $Thbp = 43$ in sync mode. DE mode is unnecessary to keep it.

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7. POWER ON/OFF SEQUENCE

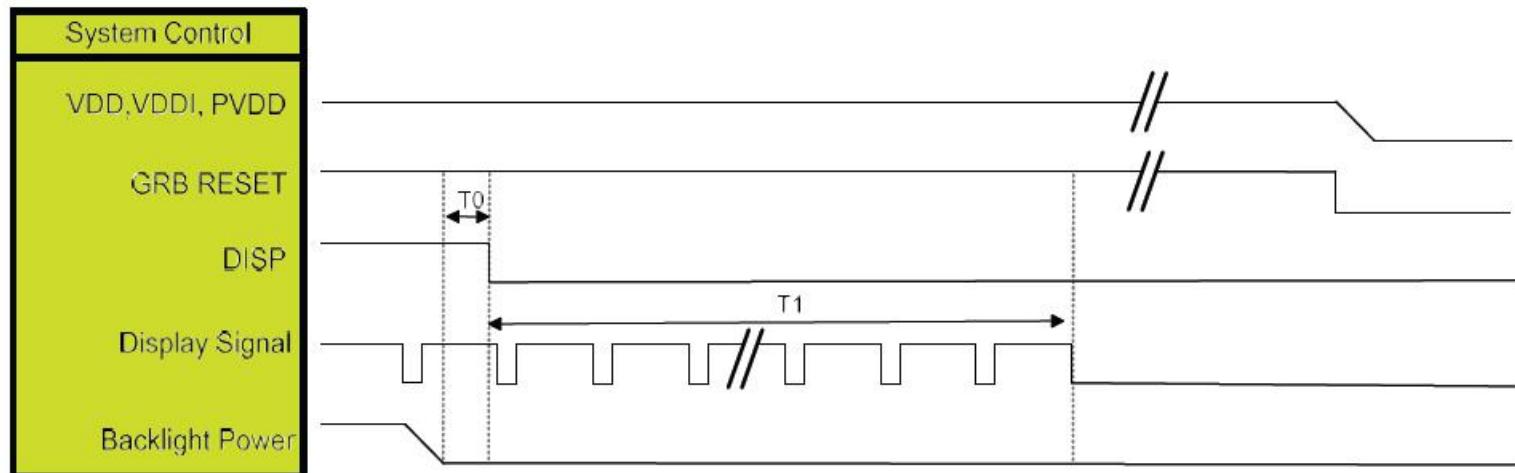
7.1 Power On Sequence



Symbol	Description	Min. Time	Unit
T0	System power stability to GRB RESET signal	0	ms
T1	GRB RESET= "High" to DISP="High"	10	ms
T2	Display Signal output to Backlight Power on	250	ms



7.2 Power Off Sequence



Symbol	Description	Min. Time	Unit
T0	Backlight Power off to DISP="Low"	5	ms
T1	DISP="Low" to IC internal voltage discharge complete	80	ms

8. LCD Module Out-Going Quality Level

8.1 VISUAL & FUNCTION INSPECTION STANDARD

8.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

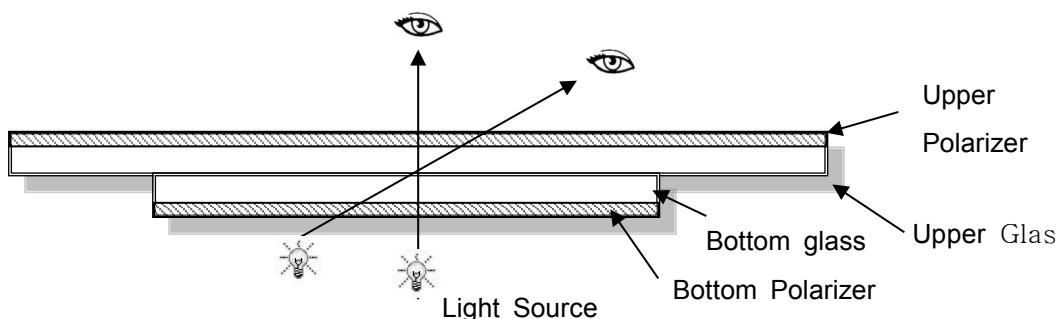
Temperature : $25\pm5^{\circ}\text{C}$

Humidity : $65\%\pm10\%\text{RH}$

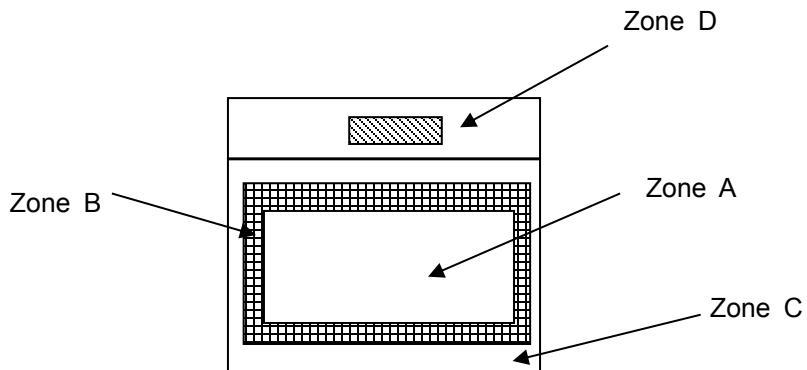
Viewing Angle : Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance:30-50cm



8.1.2 Definition



Zone A : Effective Viewing Area(Character or Digit can be seen)

Zone B : Viewing Area except Zone A

Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .)

Zone D : IC Bonding Area

Note:As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer

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8.1.3 Sampling Plan

According to GB/T 2828.1-2003 ; , normal inspection, Class II

AQL:

Major defect	Minor defect
0.65	1.5

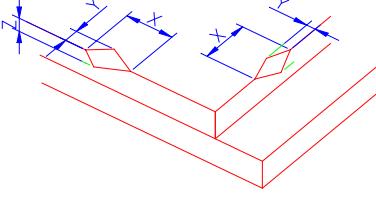
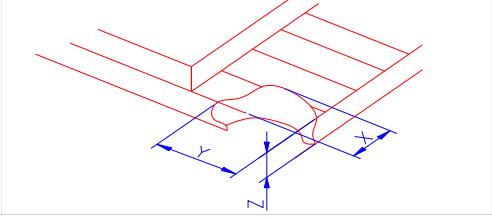
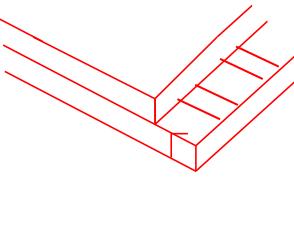
LCD: Liquid Crystal Display , TP: Touch Panel , LCM: Liquid Crystal Module

No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. 4) TP no function	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	
4	Color tone	Color unevenness, refer to limited sample	
5	Spot Line defect	Light dot, Dim spot,Polarizer Bubble ; Polarizer accidented spot.	
6	Soldering appearance	Good soldering , Peeling off is not allowed.	
7	LCD/Polarizer/TP	Black/White spot/line, scratch, crack, etc.	

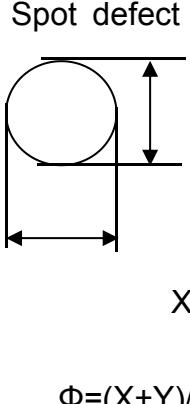
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8.1.4 Criteria (Visual)

Number	Items	Criteria(mm)						
1.0 LCD Crack/Broken NOTE: X: Length Y: Width Z: Height L: Length of IT O, T: Height of LCD	(1) The edge of LCD broken	 <table border="1" data-bbox="754 669 1453 819"> <tr> <th>X</th><th>Y</th><th>Z</th></tr> <tr> <td>$\leq 3.0\text{mm}$</td><td><Inner border line of the seal</td><td>$\leq T$</td></tr> </table>	X	Y	Z	$\leq 3.0\text{mm}$	<Inner border line of the seal	$\leq T$
X	Y	Z						
$\leq 3.0\text{mm}$	<Inner border line of the seal	$\leq T$						
	(2)LCD corner broken	 <table border="1" data-bbox="833 1134 1373 1224"> <tr> <th>X</th><th>Y</th><th>Z</th></tr> <tr> <td>$\leq 3.0\text{mm}$</td><td>$\leq L$</td><td>$\leq T$</td></tr> </table>	X	Y	Z	$\leq 3.0\text{mm}$	$\leq L$	$\leq T$
X	Y	Z						
$\leq 3.0\text{mm}$	$\leq L$	$\leq T$						
	(3) LCD crack	 <p>Crack Not allowed</p>						



2.0	 $\Phi = (X+Y)/2$	① light dot (LCD/TP/Polarizer black/white spot , light dot, pinhole, dent, stain)				
		Zone	Acceptable Qty			
		Size (mm)	A	B	C	
		$\Phi \leq 0.10$	Ignore			
		$0.10 < \Phi \leq 0.25$	4(distance $\geq 10\text{mm}$)			
		$0.25 < \Phi \leq 0.35$	3			
		$\Phi > 0.4$	0			
② Dim spot (LCD/TP/Polarizer dim dot, light leakage、dark spot)						
Zone		Acceptable Qty				
Size (mm)		A	B	C		
$\Phi \leq 0.1$		Ignore				
$0.10 < \Phi \leq 0.25$		4(distance $\geq 10\text{mm}$)				
$0.25 < \Phi \leq 0.35$		3				
$\Phi > 0.4$		0				
③ Polarizer accidented spot						
Zone		Acceptable Qty				
Size (mm)		A	B	C		
$\Phi \leq 0.2$		Ignore				
$0.3 < \Phi \leq 0.5$		3(distance $\geq 10\text{mm}$)				
$\Phi > 0.5$		0				
④ Pixel bad points (light dot, Dim dot, color dot)						
Zone		Acceptable Qty				
Size (mm)		A	B	C		
$\Phi \leq 0.15$		Ignore				
$0.2 < \Phi \leq 0.3$		2(distance $\geq 10\text{mm}$)				
$\Phi > 0.4$		0				
⑤ Polarizer Bubble						
Zone		Acceptable Qty				
Size (mm)		A	B	C		
$\Phi \leq 0.2$		Ignore				
$0.3 < \Phi \leq 0.4$		4(distance $\geq 10\text{mm}$)				
$0.4 < \Phi \leq 0.5$		3				
$\Phi > 0.5$		0				

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3.0	Line defect (LCD/TP /Polarizer backlight black/white line, scratch, stain)	Width(mm) Length(m) m)	Acceptable Qty		
			A	B	C
		Φ≤0.05	Ignore	Ignore	
		0.05<W≤0.06	L≤5.0	N≤3	
		0.07<W≤0.08	L≤4.0	N≤2	
		0.08<W	Define as spot defect		

4.0	Electronic Components SMT	Not allow missing parts, solderless connection, cold solder joint, mismatch, The positive and negative polarity opposite
5.0	Display color& Brightness	1. Color: Measuring the color coordinates, The measurement standard according to the datasheet or samples. 2. Brightness: Measuring the brightness of White screen, The measurement standard according to the datasheet or Samples.
6.0	LCD Mura	By 5% ND filter invisible.

Criteria (functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed
5	TP no function	Not allowed

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9. Reliability Test Result

Item	Condition	Inspection after test
High Temperature Operating	85°C,96H	
Low Temperature Operating	-30°C, 96HR	
High Temperature Storage	85°C, 96HR	Inspection after 2~4hours
Low Temperature Storage	-30°C, 96HR	storage at room temperature,
High Temperature & High	+60°C, 90% RH ,96 hours.	the sample shall be free from
Thermal Shock (Non-operation)	-30°C,30 min ↔ +85°C,30 min, Change time:5min 20CYC.	defects: 1.Air bubble in the LCD; 2.Non-display;
ESD test	C=150pF, R=330,5points/panel Air:±8KV, 5times; Contact:±6KV, 5 times; (Environment: 15°C~35°C, 30%~60%).	3.Missing segments/line; 4.Glass crack; 5.Current IDD is twice higher
Vibration (Non-operation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z. (6 hours for total) (Package condition).	than initial value.
Box Drop Test	1 Corner 3 Edges 6 faces,80cm(MEDIUM BOX)	

Remark:

- 1.The test samples should be applied to only one test item.
- 2.Sample size for each test item is 5~10pcs.
- 3.For Damp Proof Test, Pure water(Resistance > 10MΩ) should be used.
- 4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
- 5.Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

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10. Cautions and Handling Precautions

10.1 Handling and Operating the Module

(1) When the module is assembled, it should be attached to the system firmly.

Do not warp or twist the module during assembly work.

(2) Protect the module from physical shock or any force. In addition to damage, this may cause improper operation or damage to the module and back-light unit.

(3) Note that polarizer is very fragile and could be easily damaged. Do not press or scratch the surface.

(4) Do not allow drops of water or chemicals to remain on the display surface.

If you have the droplets for a long time, staining and discoloration may occur.

(5) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.

(6) The desirable cleaners are water, IPA (Isopropyl Alcohol) or Hexane.

Do not use ketene type materials (ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.

(7) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth. In case of contact with hands, legs, or clothes, it must be washed away thoroughly with soap.

(8) Protect the module from static; it may cause damage to the CMOS ICs.

(9) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.

(10) Do not disassemble the module.

(11) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.

(12) Pins of I/F connector shall not be touched directly with bare hands.

(13) Do not connect, disconnect the module in the "Power ON" condition.

(14) Power supply should always be turned on/off by the item 6.1 Power On Sequence & 6.2 Power Off Sequence

10.2 Storage and Transportation.

(1) Do not leave the panel in high temperature, and high humidity for a long time.

It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%

(2) Do not store the TFT-LCD module in direct sunlight.

(3) The module shall be stored in a dark place. When storing the modules for a long time, be sure to adopt effective measures for protecting the modules from strong ultraviolet radiation, sunlight, or fluorescent light.

(4) It is recommended that the modules should be stored under a condition where no condensation is allowed. Formation of dewdrops may cause an abnormal operation or a failure of the module.

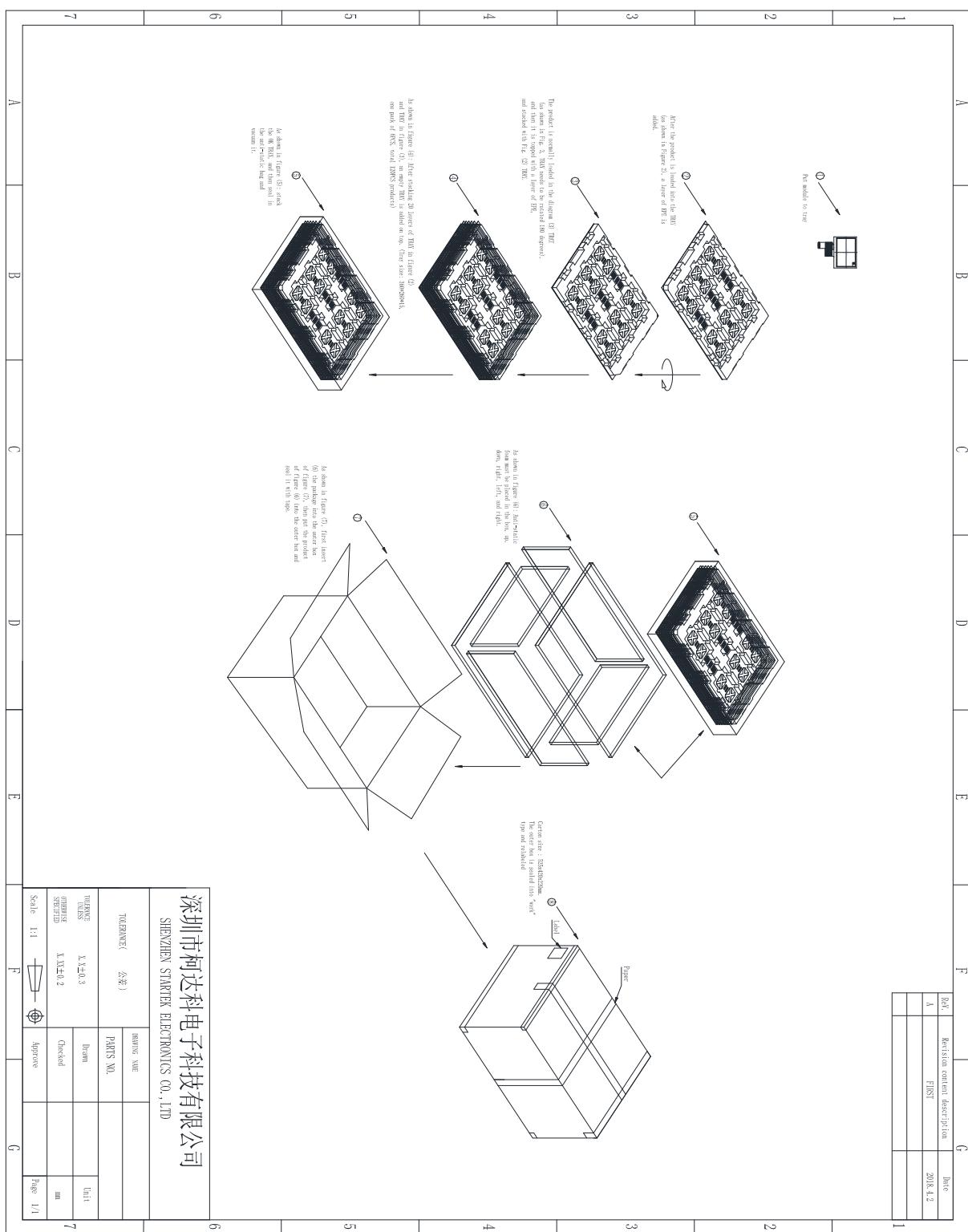
In particular, the greatest possible care should be taken to prevent any module from being operated where condensation has occurred inside.

(5) This panel has its circuitry FPC on the bottom side and should be handled carefully in order not to be stressed.

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11. Packing



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