VS-UFB250FA60

Vishay Semiconductors

Insulated Ultrafast Rectifier Module, 250 A



SOT-227

FEATURES

- Two fully independent diodes
- · Fully insulated package
- Ultrafast, soft reverse recovery, with high COMPLIANT operation junction temperature (T_1 max. = 175 °C)
- Very low forward voltage drop
- Optimized for power conversion: welding and industrial SMPS applications
- · Easy to use and parallel
- Industry standard outline
- UL approved file E78996
- · Designed and qualified for industrial level
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION / APPLICATIONS

The VS-UFB250FA60 insulated modules integrate two state of the art ultrafast recovery rectifiers in the compact, industry standard SOT-227 package. The diodes structure, and its life time control, provide an ultrasoft recovery current shape, together with the best overall performance, ruggedness and reliability characteristics.

These devices are thus intended for high frequency applications in which the switching energy is designed not to be predominant portion of the total energy, such as in the output rectification stage of welding machines, SMPS, DC/DC converters. Their extremely optimized stored charge and low recovery current reduce both over dissipation in the switching elements (and snubbers) and FMI/RFI.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS		
Cathode to anode voltage	V _R		600	V		
Continuous forward current per diode	١ _F	$T_{\rm C} = 90 ^{\circ}{\rm C}$	168	А		
Single pulse forward current per diode	I _{FSM}	$T_{\rm C} = 25 \ ^{\circ}{\rm C}$	1300	А		
Maximum power dissipation per module	PD	$T_{\rm C} = 90 ^{\circ}{\rm C}$	395	W		
RMS isolation voltage	VISOL	Any terminal to case, t = 1 min	2500	V		
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C		

PRIMARY CHARACTERISTICS V_R 600 V $I_{F(AV)}$ ⁽¹⁾ per module at $T_{C} = 113 \text{ °C}$ 250 A 166 ns t_{rr} Туре Modules - diode FRED Pt® Package SOT-227



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ELECTRICAL SPECIFICATIONS PER DIODE ($T_J = 25$ °C unless otherwise specified)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS		TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	600	-	-	
Forward voltage	V _{FM}	I _F = 100 A	-	1.02	1.19	V
		I _F = 100 A, T _J = 175 °C	-	0.87	1.02	
Povereo lookago ourrent	I _{RM}	$V_R = V_R$ rated	-	1.3	50	μA
Reverse leakage current		$T_J = 175 \text{ °C}, V_R = V_R \text{ rated}$	-	-	4	mA
Junction capacitance	CT	V _R = 600 V	-	72	-	pF

DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time	t _{rr}	$T_J = 25 \ ^\circ C$		-	166	-	ns
		T _J = 150 °C	I _F = 50 A dI _F /dt = 500 A/μs V _R = 200 V	-	291	-	
Deels receiver a surrent	I _{RRM}	T _J = 25 °C		-	41	-	А
Peak recovery current		T _J = 150 °C		-	64	-	~
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	3.5	-	μC
		T _J = 150 °C		-	10.0	-	μΟ

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction to case, single leg conducting			-	-	0.43	
Junction to case, both leg conducting	R _{thJC}		-	-	0.215	°C/W
Case to heatsink	R _{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
Mounting torque		Torque to heatsink	-	-	1.8 (15.9)	Nm (lbf.in)
Case style					SOT-227	



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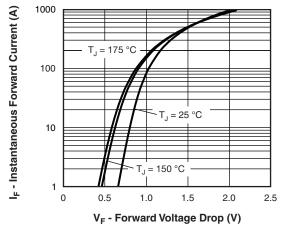


Fig. 1 - Typical Forward Voltage Drop vs. Instantaneous Forward Current (Per Leg)

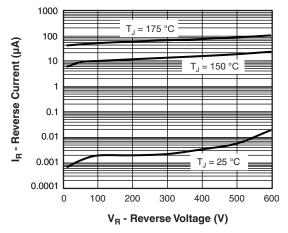


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

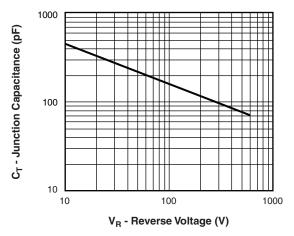


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

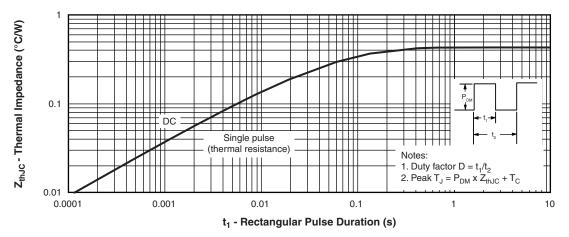


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

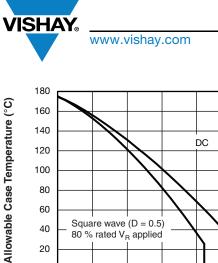
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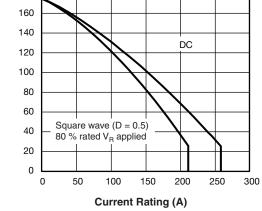


Fig. 5 - Maximum Current Rating (Per Leg)

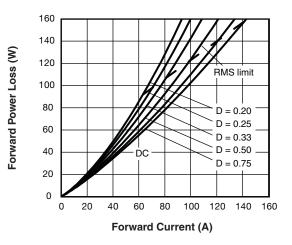


Fig. 6 - Forward Power Loss Characteristics

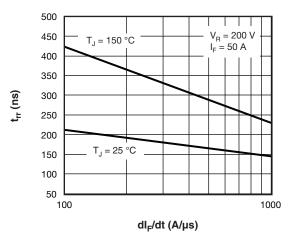


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

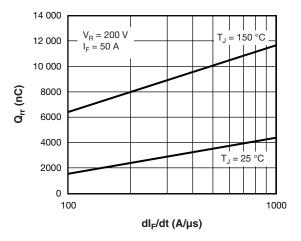


Fig. 8 - Typical Recovery Charge vs. dl_F/dt

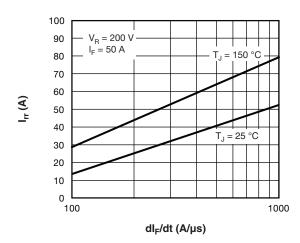


Fig. 9 - Typical Recovery Current vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 6); Pd_{REV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at V_{R1} = 80 % rated V_R

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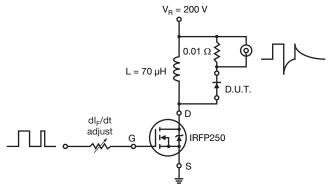


Fig. 10 - Reverse Recovery Parameter Test Circuit

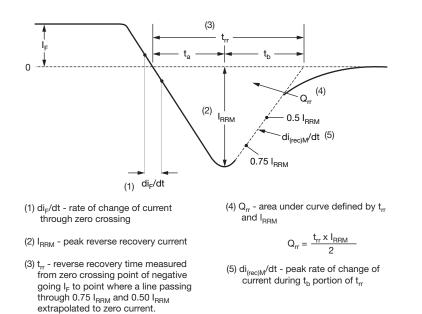


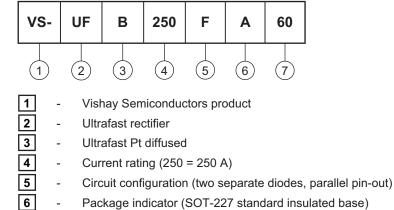
Fig. 11 - Reverse Recovery Waveform and Definitions





ORDERING INFORMATION TABLE

Device code



- Package indicator (SOT-227 standard insulated base) -
- 7 Voltage rating (60 = 600 V)

CIRCUIT CONFI	CIRCUIT CONFIGURATION				
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING			
Two separate diodes, parallel pin-out	F	Lead Assignment			

LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95423				
Packaging information	www.vishay.com/doc?95425				



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