



( ) Preliminary Specification

(V) Final Specification

Module	30" Color TFT-LCD
Model Name	M300DVR01.1 open Cell

Customer	Date
_____	_____
Approved by	
_____	_____
<p>Note: This Specification is subject to change without notice.</p>	

Approved by	Date
<u>Howard Lee</u>	<u>May 11, 2018</u>
Prepared by	Date
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Record of Revision

Version	Date	Page	Old description	New Description	Remark												
0.1	2015/11/13		Preliminary Spec														
0.2	2015/11/30	27-28		Add mechanical drawing													
1.0	2018/2/2	5	<table border="1"> <tr> <td>Weight</td> <td>(Grams)</td> <td>TBC</td> </tr> <tr> <td>Electrical Interface</td> <td>-</td> <td>V.by-one</td> </tr> </table>	Weight	(Grams)	TBC	Electrical Interface	-	V.by-one	<table border="1"> <tr> <td>Weight</td> <td>(Grams)</td> <td>740 (Typ.) / 800 (Max)</td> </tr> <tr> <td>Electrical Interface</td> <td>-</td> <td>4-channel LVDS</td> </tr> </table>	Weight	(Grams)	740 (Typ.) / 800 (Max)	Electrical Interface	-	4-channel LVDS	
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1.1	2018/5/11	5	<table border="1"> <tr> <td>Power Consumption</td> <td>(Watt)</td> <td>6.36W @ White pattern, Fv=14Hz, 12V</td> </tr> </table>	Power Consumption	(Watt)	6.36W @ White pattern, Fv=14Hz, 12V	<table border="1"> <tr> <td>Power Consumption</td> <td>(Watt)</td> <td>6.36W @ White pattern, Fv=60Hz, 12V</td> </tr> </table>	Power Consumption	(Watt)	6.36W @ White pattern, Fv=60Hz, 12V							
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## I Handling Precautions

- 1) Since polarizer is easily damaged, do not touch or press the surface of polarizer with hand.
- 2) Be sure to turn off power supply when inserting or disconnecting from input connector.
- 3) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- 4) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- 5) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface.
- 6) Since CMOS LSI is used in this module, take care of static electricity and insure human earth when handling.
- 7) Do not open or modify the Module Assembly.
- 8) Do not press the reflector sheet at the back of the module to any directions.
- 9) In case a TFT-LCD Module has to be put back into the packing container slot after once it was taken out from the container, do not press the center of the LED lightbar edge. Otherwise the TFT-LCD Module may be damaged.
- 10) Insert or pull out the interface connector, be sure not to rotate nor tilt it of the TFT-LCD Module.
- 11) Do not twist nor bend the TFT -LCD Module even momentary. It should be taken into consideration that no bending/twisting forces are applied to the TFT-LCD Module from outside. Otherwise the TFT-LCD Module may be damaged.
- 12) Please avoid touching COF position while you are doing mechanical design.
- 13) When storing modules as spares for a long time, the following precaution is necessary: Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- 14) Do not apply the same pattern for a long time, it will enhance relevant defect.

## 2 General Description

This specification applies to the 30.0 inch wide Color a-Si TFT-LCD Module M300DVR01.I. The display supports the Wide Full HD - 2560(H) × 1080(V) screen format and 16.7M colors (8bits RGB input). The input interface is 4 channel LVDS.

### 2.1 Display Characteristics

The following items are characteristics summary on the table under 25°C condition:

ITEMS	Unit	SPECIFICATIONS
Screen Diagonal	[mm]	749.3 (29.5")
Active Area	[mm]	690.432 (H) × 291.276 (V)
Pixels H x V	-	2560(x3) × 1080
Pixel Pitch	[um]	269.7 (per one triad) × 269.7
Pixel Arrangement	-	R.G.B. Vertical Stripe
Display Mode	-	VA Mode, Normally Black
Response Time	[msec]	20 GTG (Typ.)
Power Consumption	[Watt]	6.36W @ White pattern, Fv=60Hz, 12V
Weight	[Grams]	760 (Typ.) / 800 (Max.)
Electrical Interface	-	4- channel LVDS
Support Color	-	16.7M colors (RGB 8-bits )
Surface Treatment	-	Anti-Glare, 3H
Temperature Range		
Operating	[°C]	0 to +50
Storage (Shipping)	[°C]	-20 to +60
Cell transmittance	[%]	3.18 (Typ.)
		2.89 (Min.)
Cell thickness	[mm]	1.44 (Thickness of polarizer film: 0.175 mm each side)

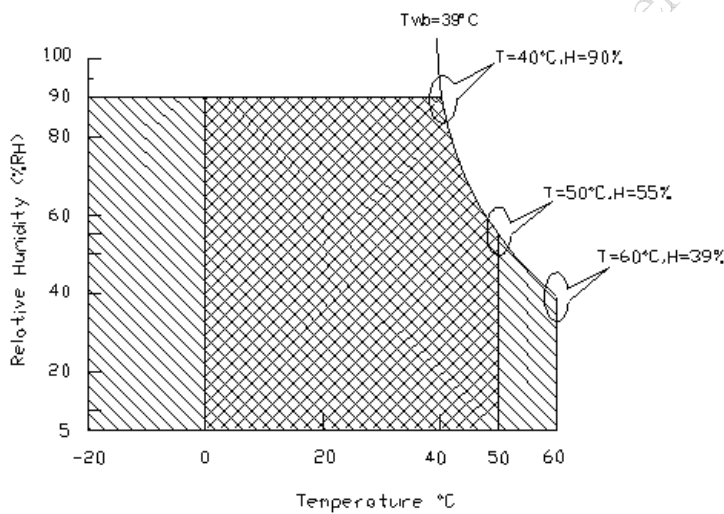
## 2.2 Absolute Maximum Rating of Environment

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit	Remark
TOP	Operating Temperature	0	+50	[°C]	<b>Note 2-1</b>
TGS	Glass surface temperature (operation)	0	+65	[°C]	<b>Note 2-1</b> Function judged only
HOP	Operation Humidity	5	90	[%RH]	<b>Note 2-1</b>
TST	Storage Temperature	-20	+60	[°C]	
HST	Storage Humidity	5	90	[%RH]	

**Note 2-1:** Temperature and relative humidity range are shown as the below figure.

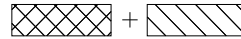
1. 90% RH Max (  $T_a \leq 39^\circ\text{C}$  )
2. Max wet-bulb temperature at  $39^\circ\text{C}$  or less. (  $T_a \leq 39^\circ\text{C}$  )
3. No condensation



Operating Range



Storage Range



## 2.3 Optical Characteristics

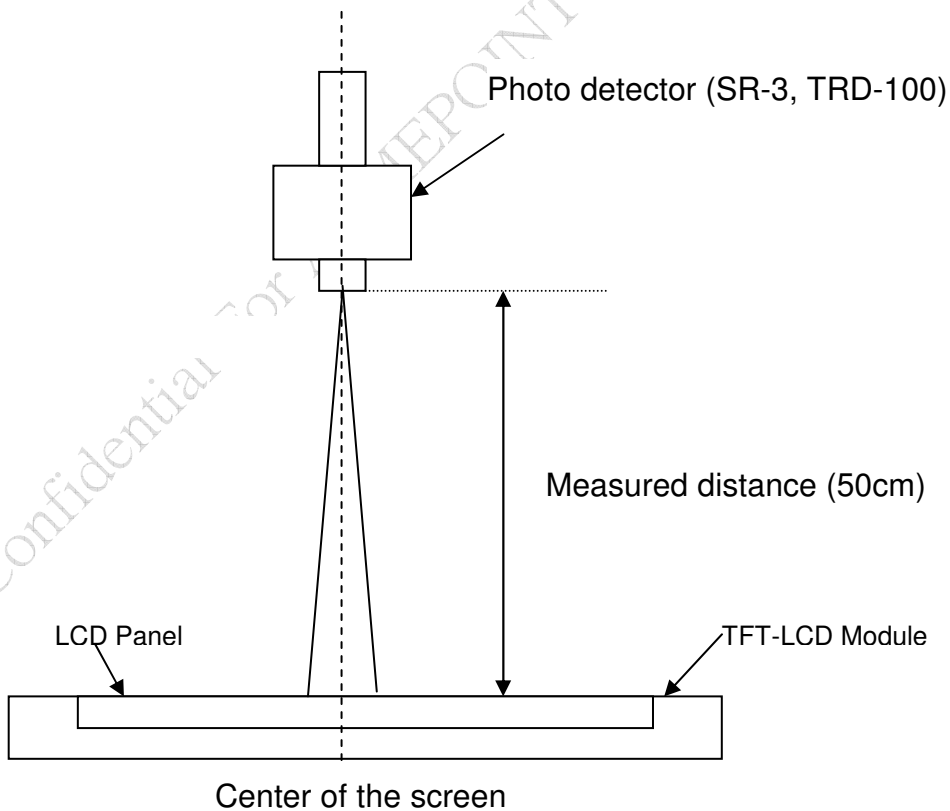
The optical characteristics are measured on the following test condition.

### Test Condition:

1. Equipment setup: Please refer to *Note 2-2*.
2. Panel Lighting time: 30 minutes
3. VDD=12.0V, Fv=75Hz, Is=90mA, Ta=25°C

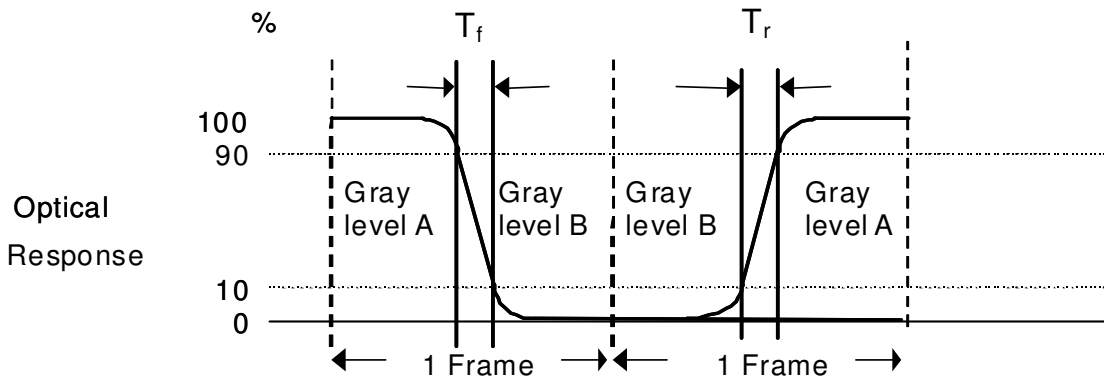
Symbol	Description		Min.	Typ.	Max.	Unit	Remark
T <sub>GTG</sub>	Response Time	Gray to Gray	-	20	-	[msec]	<i>Note 2-3</i> <i>By TRD-100</i>
CT	Crosstalk		-	-	1.5	[%]	<i>Note 2-4</i> <i>By SR-3</i>
F <sub>dB</sub>	Flicker (Center of screen)		-	-	-20	[dB]	<i>Note 2-5</i> <i>By SR-3</i>

**Note 2-2:** Equipment setup :



**Note 2-3:** Response time measurement

The output signals of photo detector are measured when the input signals are changed from “Gray level A” to “Gray level B” (falling time,  $T_f$ ), and from “Gray level B” to “Gray level A” (rising time,  $T_r$ ), respectively. The response time is interval between the 10% and 90% of optical response.



The gray to gray response time is defined as the following table.

Gray Level to Gray Level		Target gray level				
		L0	L63	L127	L191	L255
Start gray level	L0					
	L63					
	L127					
	L191					
	L255					

■  $T_{GTG\_typ}$  is the total average time at rising time and falling time of gray to gray.

**Note 2-4:** Crosstalk measurement

**Definition:**

$$CT = \text{Max.} (CT_H, CT_V);$$

Where

a. Maximum Horizontal Crosstalk :

$$CT_H = \text{Max.} (|Y_{BL} - Y_{AL}| / Y_{AL} \times 100 \%, |Y_{BR} - Y_{AR}| / Y_{AR} \times 100 \%);$$

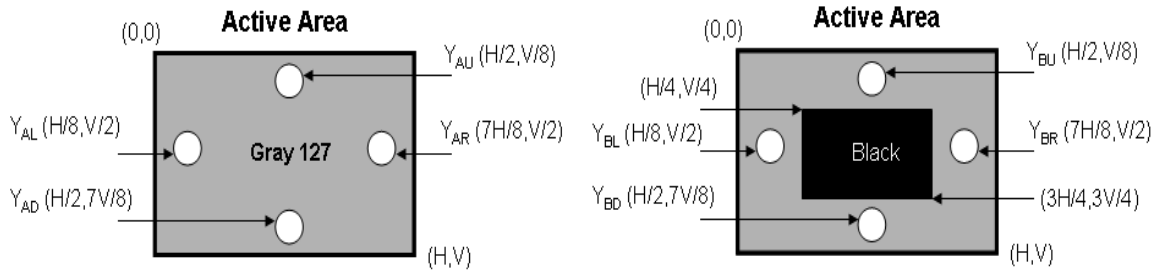
Maximum Vertical Crosstalk:

$$CT_V = \text{Max.} (|Y_{BU} - Y_{AU}| / Y_{AU} \times 100 \%, |Y_{BD} - Y_{AD}| / Y_{AD} \times 100 \%);$$

b.  $Y_{AU}, Y_{AD}, Y_{AL}, Y_{AR}$  = Luminance of measured location without Black pattern

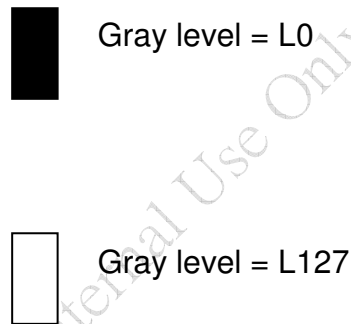
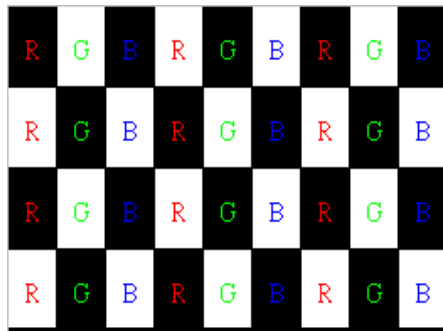
$Y_{BU}, Y_{BD}, Y_{BL}, Y_{BR}$  = Luminance of measured location with Black pattern





**Note 2-5:** Flicker measurement

a. Test pattern: It is listed as following.



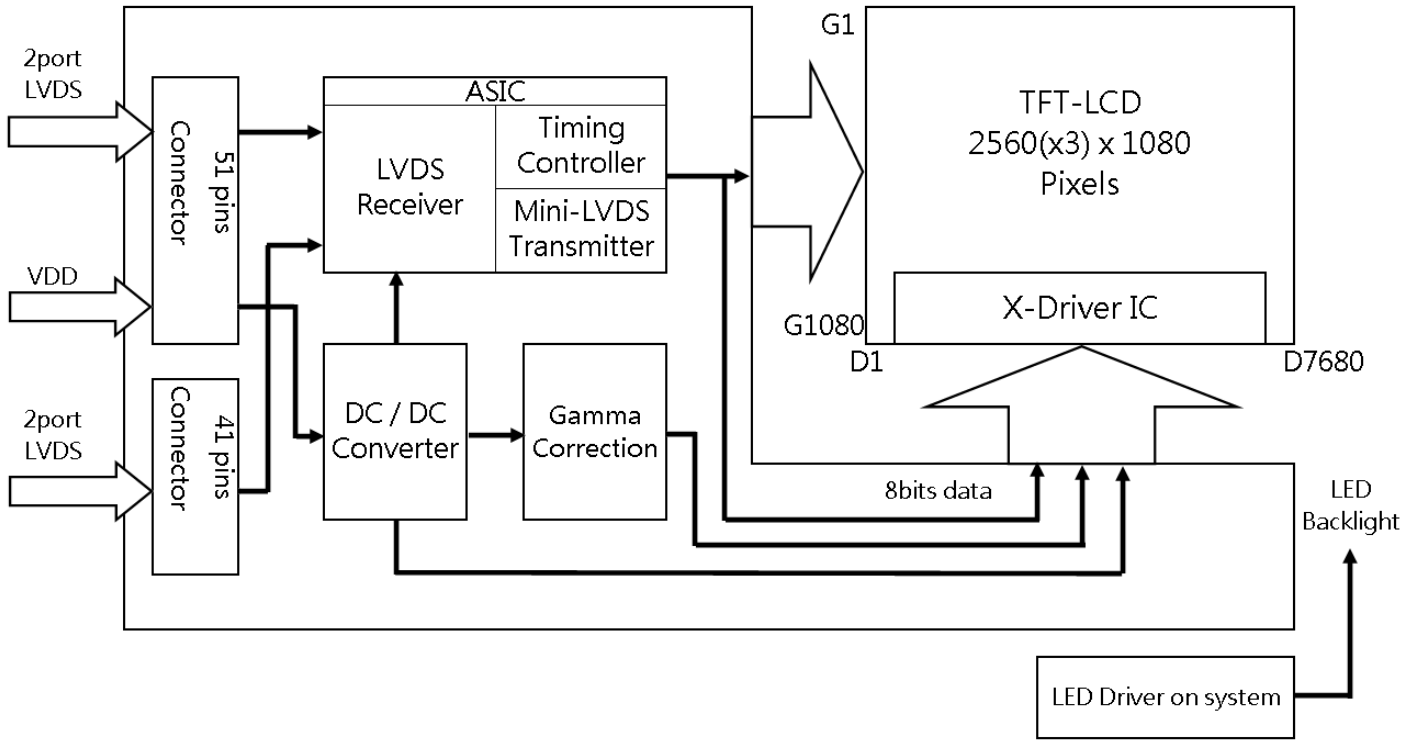
R: Red, G: Green, B:Blue

b. Measured position: Center of screen (P5) & perpendicular to the screen ( $\theta = \Phi = 0^\circ$ )

### 3 TFT-LCD Module

#### 3.1 Block Diagram

The following shows the block diagram of the 30 inch Color TFT-LCD Module.



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## 3.2 Interface Connection

### 3.2.1 Connector Type

TFT-LCD Connector (CNT1)	Manufacturer	JAE	P-TWO
	Part Number	FI-RTE51SZ-HF	187059-5122
TFT-LCD Connector (CNT2)	Manufacturer	JAE	P-TWO
	Part Number	FI-RE41S-HF	187060-4122
Mating Connector (CNT1)	Manufacturer	JAE	
	Part Number	FI-RE51HL	
Mating Connector (CNT2)	Manufacturer	JAE	
	Part Number	FI-RE41HL	

### 3.2.2 Connector Pin Assignment

#### LVDS CNI

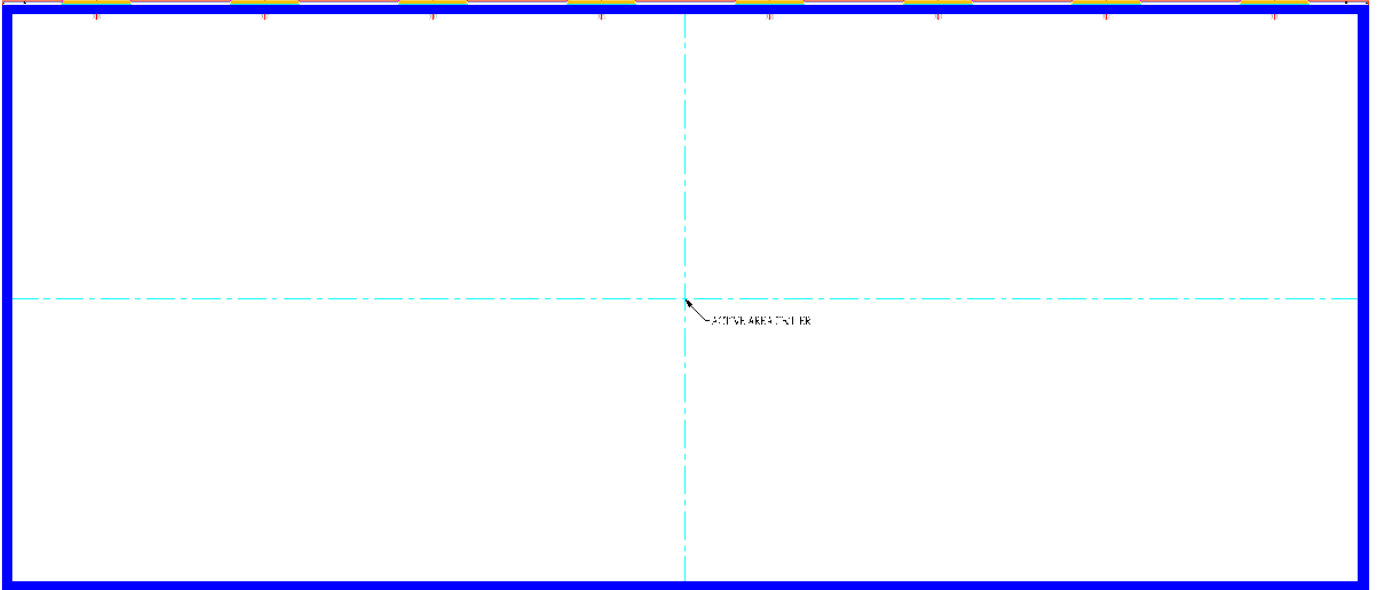
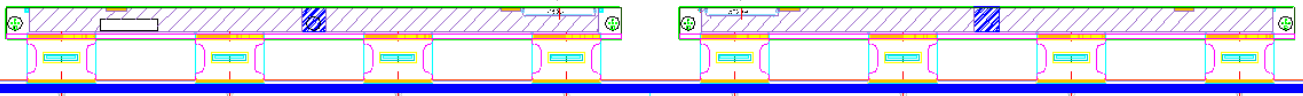
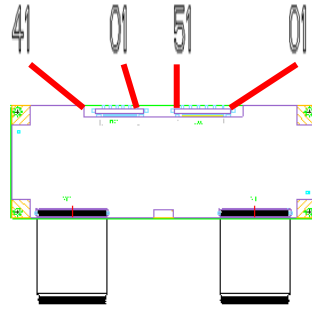
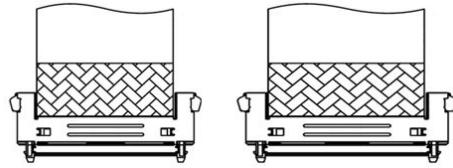
PIN #	Symbol	Description	Remark
1	NC	No Connection (for AUO test only. Do not connect)	
2	NC	No Connection (for AUO test only. Do not connect)	
3	NC	No Connection (for AUO test only. Do not connect)	
4	NC	No Connection (for AUO test only. Do not connect)	
5	NC	No Connection (for AUO test only. Do not connect)	
6	IMS	Interlace Mode Selection	
7	NC	No Connection (for AUO test only. Do not connect)	
8	NC	No Connection (for AUO test only. Do not connect)	
9	NC	No Connection (for AUO test only. Do not connect)	
10	NC	No Connection (for AUO test only. Do not connect)	
11	GND	Power Ground	
12	RI_0N	FIRST_ Negative LVDS differential data input	
13	RI_0P	FIRST_ Positive LVDS differential data input	
14	RI_1N	FIRST_ Negative LVDS differential data input	
15	RI_1P	FIRST_ Positive LVDS differential data input	
16	RI_2N	FIRST_ Negative LVDS differential data input	
17	RI_2P	FIRST_ Positive LVDS differential data input	
18	GND	Power Ground	
19	RI_CLKN	FIRST_ Negative LVDS differential clock input	

20	R1_CLKP	FIRST_ Positive LVDS differential clock input	
21	GND	Power Ground	
22	R1_3N	FIRST_ Negative LVDS differential data input	
23	R1_3P	FIRST_ Positive LVDS differential data input	
24	NC	No Connection (for AUO test only. Do not connect)	
25	NC	No Connection (for AUO test only. Do not connect)	
26	GND	Power Ground	
27	GND	Power Ground	
28	R2_0N	SECOND_ Negative LVDS differential data input	
29	R2_0P	SECOND_ Positive LVDS differential data input	
30	R2_1N	SECOND_ Negative LVDS differential data input	
31	R2_1P	SECOND_ Positive LVDS differential data input	
32	R2_2N	SECOND_ Negative LVDS differential data input	
33	R2_2P	SECOND_ Positive LVDS differential data input	
34	GND	Power Ground	
35	R2_CLKN	SECOND_ Negative LVDS differential clock input	
36	R2_CLKP	SECOND_ Positive LVDS differential clock input	
37	GND	Power Ground	
38	R2_3N	SECOND_ Negative LVDS differential data input	
39	R2_3P	SECOND_ Positive LVDS differential data input	
40	NC	No Connection (for AUO test only. Do not connect)	
41	NC	No Connection (for AUO test only. Do not connect)	
42	GND	Power Ground	
43	GND	Power Ground	
44	GND	Power Ground	
45	NC	No Connection	
46	VDD	Power +12V	
47	VDD	Power +12V	
48	VDD	Power +12V	
49	VDD	Power +12V	
50	VDD	Power +12V	
51	VDD	Power +12V	

### LVDS CN2

PIN #	Symbol	Description	Remark
1	NC	No Connection (for AUO test only. Do not connect)	
2	NC	No Connection (for AUO test only. Do not connect)	
3	NC	No Connection (for AUO test only. Do not connect)	
4	NC	No Connection (for AUO test only. Do not connect)	

5	NC	No Connection (for AUO test only. Do not connect)	
6	NC	No Connection (for AUO test only. Do not connect)	
7	NC	No Connection (for AUO test only. Do not connect)	
8	NC	No Connection (for AUO test only. Do not connect)	
9	GND	Power Ground	
10	R3_0N	THIRD_ Negative LVDS differential data input	
11	R3_0P	THIRD_ Positive LVDS differential data input	
12	R3_1N	THIRD_ Negative LVDS differential data input	
13	R3_1P	THIRD_ Positive LVDS differential data input	
14	R3_2N	THIRD_ Negative LVDS differential data input	
15	R3_2P	THIRD_ Positive LVDS differential data input	
16	GND	Power Ground	
17	R3_CLKN	THIRD_ Negative LVDS differential clock input	
18	R3_CLKP	THIRD_ Positive LVDS differential clock input	
19	GND	Power Ground	
20	R3_3N	THIRD_ Negative LVDS differential data input	
21	R3_3P	THIRD_ Positive LVDS differential data input	
22	NC	No Connection (for AUO test only. Do not connect)	
23	NC	No Connection (for AUO test only. Do not connect)	
24	GND	Power Ground	
25	GND	Power Ground	
26	R4_0N	FOURTH_ Negative LVDS differential data input	
27	R4_0P	FOURTH_ Positive LVDS differential data input	
28	R4_1N	FOURTH_ Negative LVDS differential data input	
29	R4_1P	FOURTH_ Positive LVDS differential data input	
30	R4_2N	FOURTH_ Negative LVDS differential data input	
31	R4_2P	FOURTH_ Positive LVDS differential data input	
32	GND	Power Ground	
33	R4_CLKN	FOURTH_ Negative LVDS differential clock input	
34	R4_CLKP	FOURTH_ Positive LVDS differential clock input	
35	GND	Power Ground	
36	R4_3N	FOURTH_ Negative LVDS differential data input	
37	R4_3P	FOURTH_ Positive LVDS differential data input	
38	NC	No Connection (for AUO test only. Do not connect)	
39	NC	No Connection (for AUO test only. Do not connect)	
40	GND	Power Ground	
41	GND	Power Ground	



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### 3.3 Electrical Characteristics

#### 3.3.1 Absolute Maximum Rating

Permanent damage may occur if exceeding the following maximum rating.

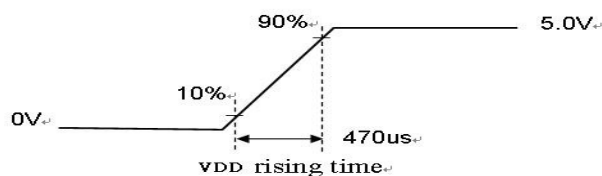
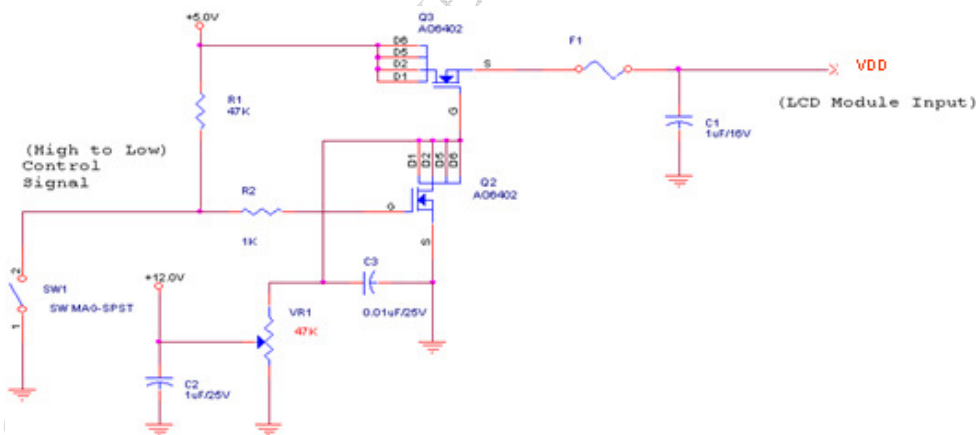
Symbol	Description	Min	Max	Unit	Remark
VDD	Power Supply Input Voltage	GND-0.3	14.0	[Volt]	Ta=25°C

#### 3.3.2 Recommended Operating Condition

Symbol	Description	Min	Typ	Max	Unit	Remark
VDD	Power supply Input voltage	10.8	12.0	13.2	[Volt]	
IDD	Power supply Input Current (RMS)	-	0.54	0.65	[A]	VDD= 12.0V, White pattern, Fv=60Hz
		-	0.61	0.73	[A]	VDD= 12.0V, White pattern, Fv=75Hz
PDD	VDD Power Consumption	-	6.48	7.80	[Watt]	VDD= 12.0V, White pattern, Fv=60Hz
		-	7.32	8.76	[Watt]	VDD= 12.0V, White pattern, Fv=75Hz
IRush	Inrush Current			3.0	[A]	<b>Note 3-1</b>
VDDrp	Allowable VDD Ripple Voltage	-		VDD*5%	[mV]	VDD= 12.0V, Black Pattern, Fv=75Hz

**Note 3-1:** Inrush Current measurement:

Test circuit:

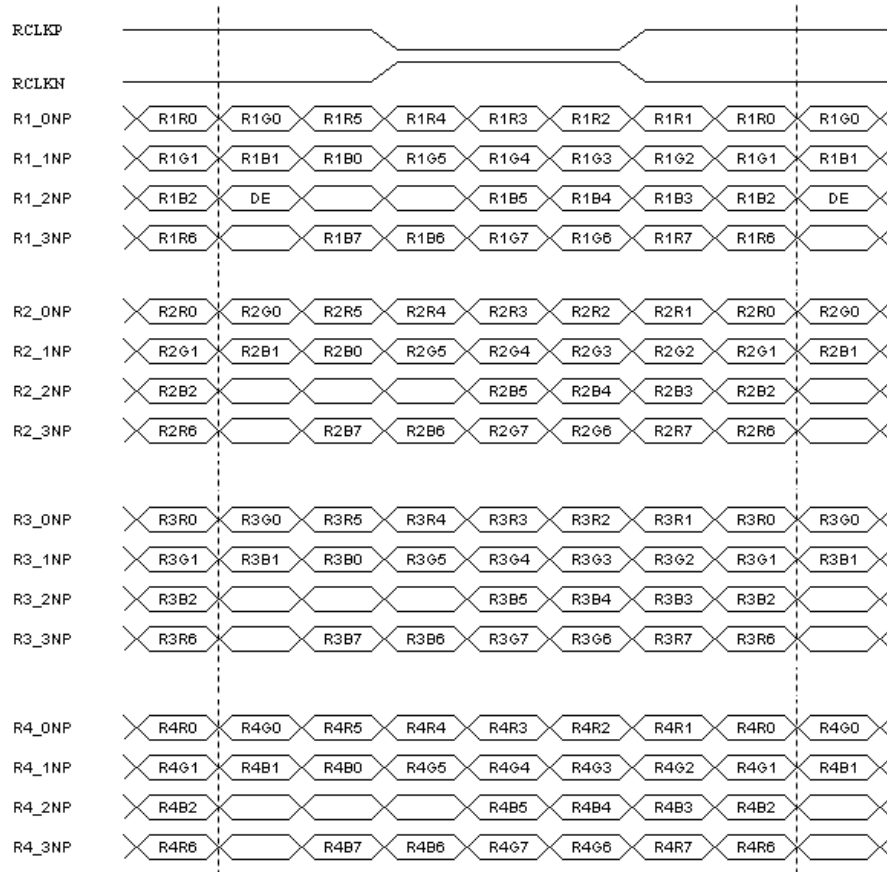


The duration of VDD rising time: 470us.





## 3.4.2 LVDS Data Format



**Note 3-3:** LVDS Data Mapping of NS Format for Quad Channel

### 3.4.3 Color versus Input Data

The following table is for color versus input data (8bit). The higher the gray level, the brighter the color.

Color	Gray Level	Color Input Data																								Remark
		RED data (MSB:R7, LSB:R0)								GREEN data (MSB:G7, LSB:G0)								BLUE data (MSB:B7, LSB:B0)								
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0	
Black	-	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
Red	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
Green	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
Blue	L0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

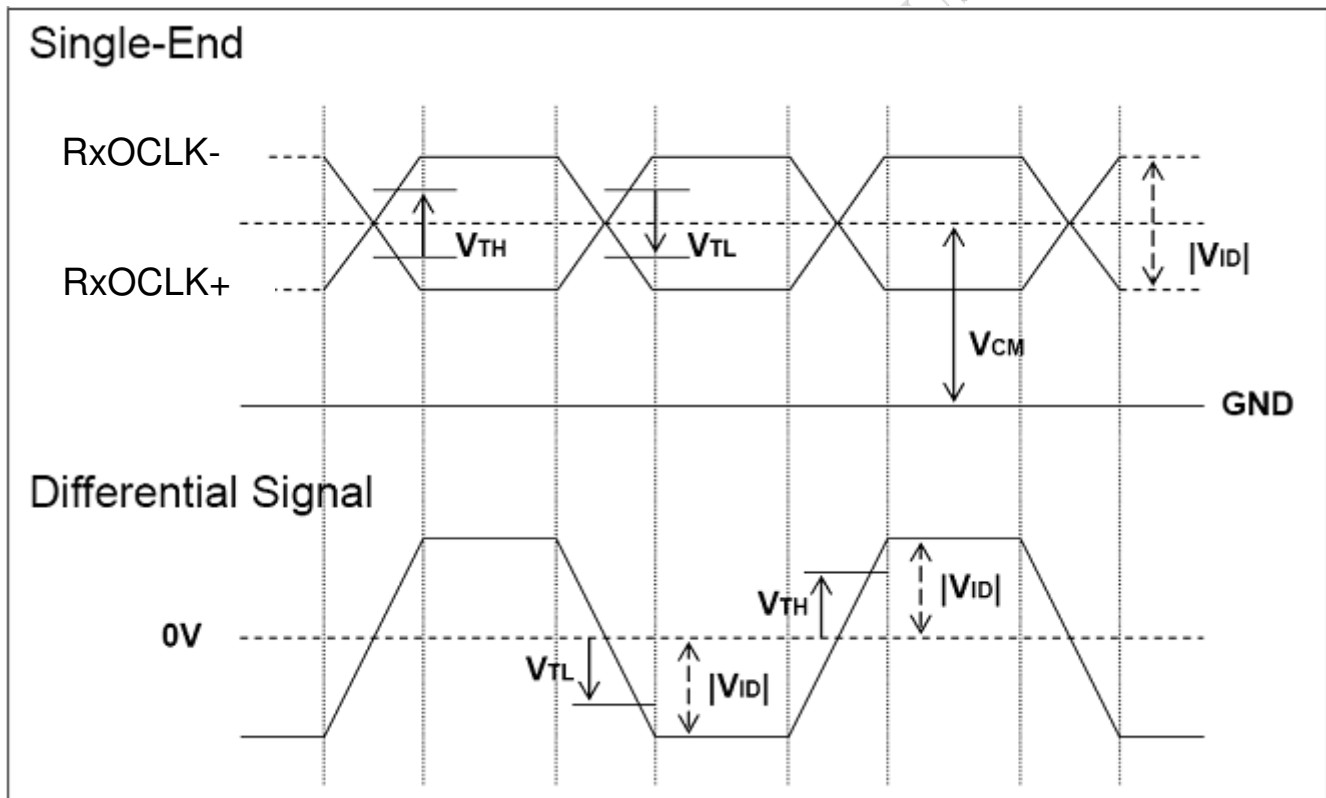
### 3.4.4 LVDS Specification

#### a. DC Characteristics:

Symbol	Description	Min	Typ	Max	Units	Condition
$V_{TH}$	LVDS Differential Input High Threshold	-	-	+100	[mV]	$V_{CM} = 1.2V$
$V_{TL}$	LVDS Differential Input Low Threshold	-100	-	-	[mV]	$V_{CM} = 1.2V$
$ V_{ID} $	LVDS Differential Input Voltage	100	-	600	[mV]	
$V_{CM}$	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	$V_{TH}-V_{TL} = 200mV$

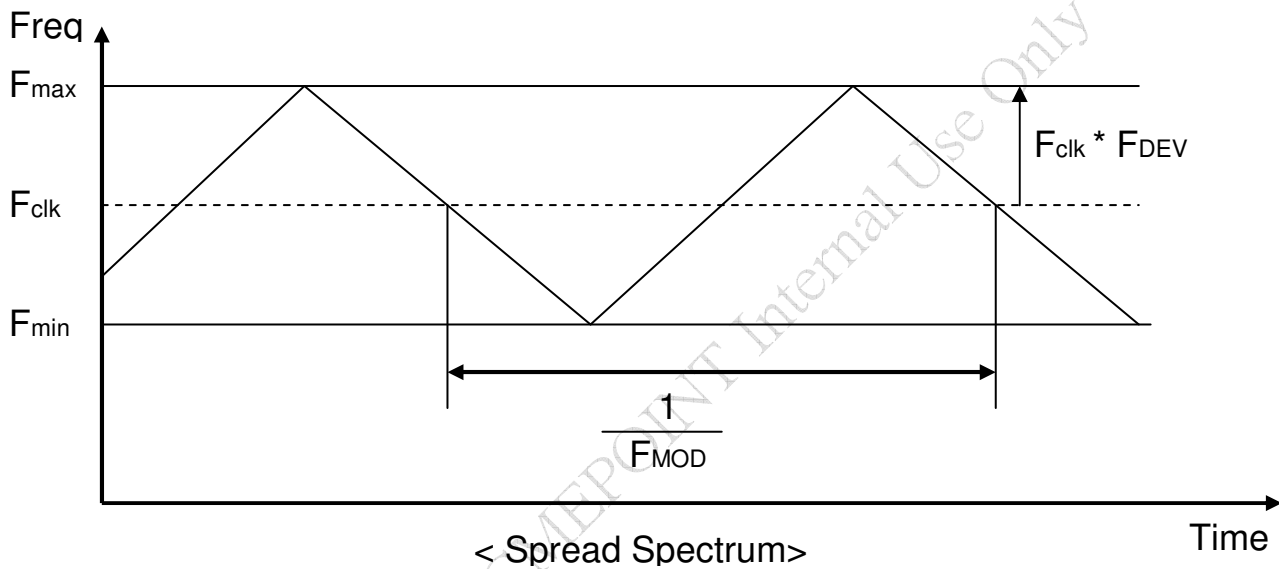
#### LVDS Signal Waveform:

Use RxOCLK- & RxOCLK+ as example.



**b. AC Characteristics:**

Symbol	Description	Min	Max	Unit	Remark
$F_{DEV}$	Maximum deviation of input clock frequency during Spread Spectrum	-	$\pm 3$	%	
$F_{MOD}$	Maximum modulation frequency of input clock during Spread Spectrum	-	200	KHz	



F<sub>clk</sub>: LVDS Clock Frequency

### 3.4.5 Input Timing Specification

It only support DE mode, and the input timing are shown as the following table.

Symbol	Description		Min.	Typ.	Max.	Unit	Remark
Tv	Vertical Section	Period	1100	1130	1752	Th	
Tdisp (v)		Active	1080	1080	1080	Th	
Tblk (v)		Blanking	20	50	672	Th	
Fv		Frequency	49	60	76	Hz	
Th	Horizontal Section	Period	679	680	1023	Tclk	
Tdisp (h)		Active	640	640	640	Tclk	
Tblk (h)		Blanking	39	40	383	Tclk	
Fh		Frequency	53.9	67.8	85.9	KHz	<b>Note 3-4</b>
Tclk	LVDS Clock	Period	17.2	21.7	27.3	ns	1/Fclk
Fclk		Frequency	36.6	46.1	58.3	MHz	<b>Note 3-5</b>

**Note 3-4:** The equation is listed as following. Please don't exceed the above recommended value.

$$Fh (\text{Min.}) = Fclk (\text{Min.}) / Th (\text{Min.});$$

$$Fh (\text{Typ.}) = Fclk (\text{Typ.}) / Th (\text{Typ.});$$

$$Fh (\text{Max.}) = Fclk (\text{Max.}) / Th (\text{Min.});$$

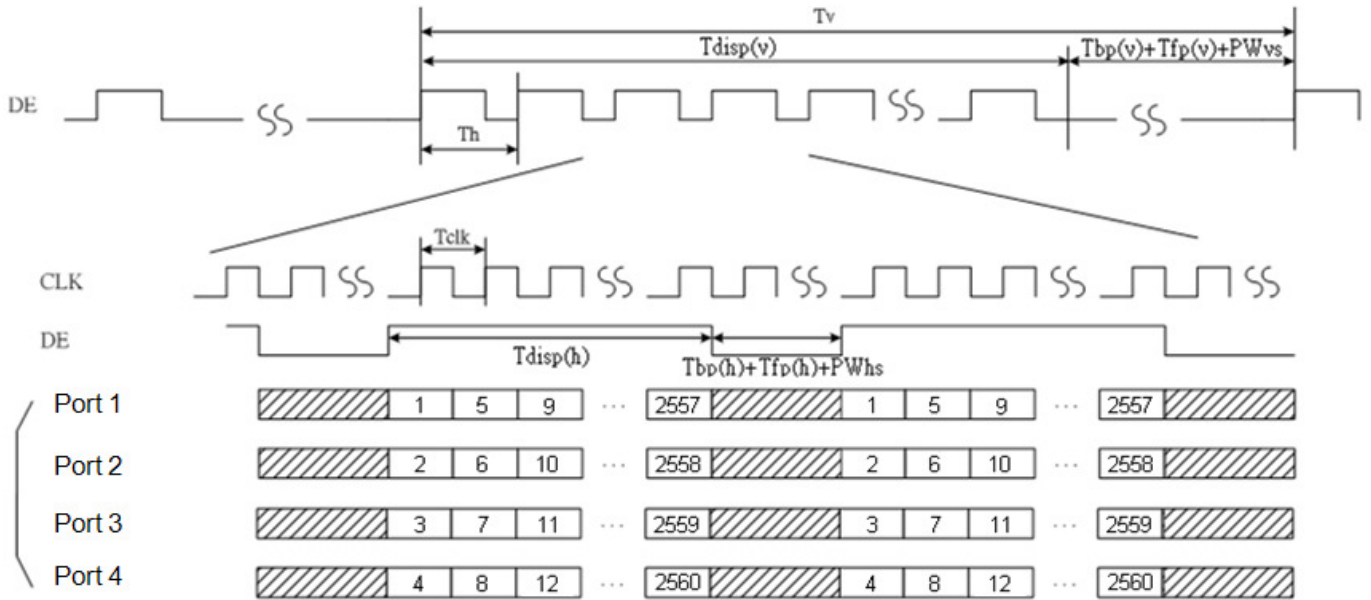
**Note 3-5:** The equation is listed as following. Please don't exceed the above recommended value.

$$Fclk (\text{Min.}) = Fv (\text{Min.}) \times Th (\text{Min.}) \times Tv (\text{Min.});$$

$$Fclk (\text{Typ.}) = Fv (\text{Typ.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.});$$

$$Fclk (\text{Max.}) = Fv (\text{Max.}) \times Th (\text{Typ.}) \times Tv (\text{Typ.});$$

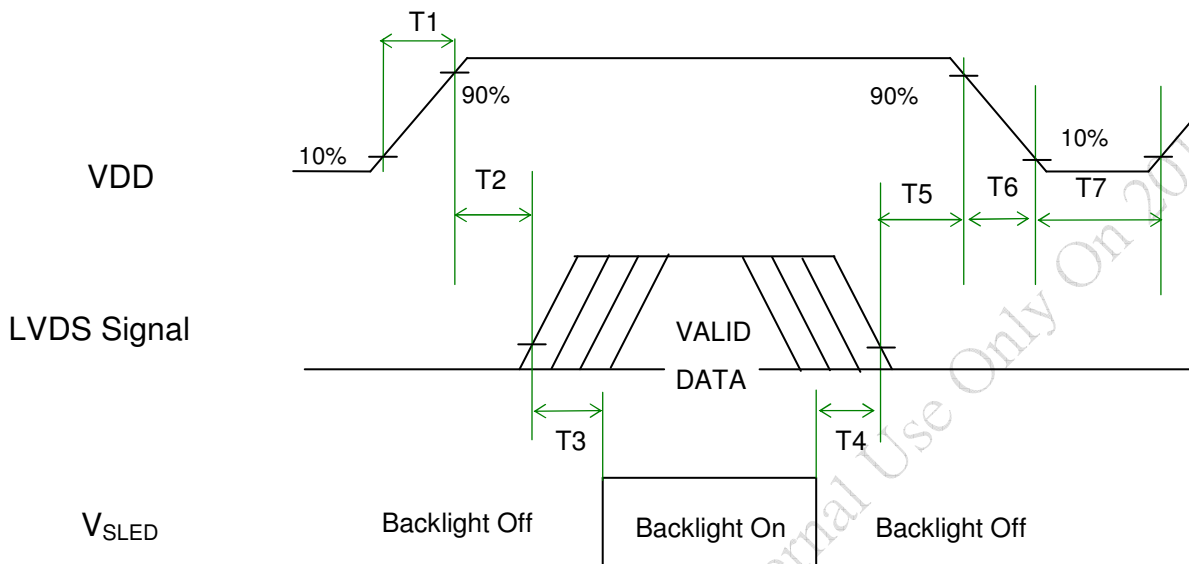
### 3.4.6 Input Timing Diagram



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### 3.5 Power ON/OFF Sequence

VDD power, LVDS signal and backlight on/off sequence are as following. LVDS signals from any system shall be Hi-Z state when VDD is off.



Power Sequence Timing

Symbol	Value			Unit	Remark
	Min.	Typ.	Max.		
T1	0.5	-	10	[ms]	
T2	0	-	50	[ms]	
T3	500	-	-	[ms]	
T4	100	-	-	[ms]	
T5	0	-	50	[ms]	<i>Note 3-6</i> <i>Note 3-7</i>
T6	0	-	200		<i>Note 3-7</i> <i>Note 3-8</i>
T7	1000	-	-	[ms]	

**Note 3-6 :** Recommend setting T5 = 0ms to avoid electronic noise when VDD is off.

**Note 3-7 :** During T5 & T6 period , please keep the level of input LVDS signals with Hi-Z state.

**Note 3-8 :** Voltage of VDD must decay smoothly after power-off.(customer system decide this value)

## 4 Reliability Test

AUO reliability test items are listed as following table. (*Bare Panel only*)

Items	Condition	Remark
Temperature Humidity Bias (THB)	Ta= 50°C , 80%RH, 300hours	
High Temperature Operation (HTO)	Ta= 50°C , 50%RH, 300hours	
Low Temperature Operation (LTO)	Ta= 0°C , 300hours	
High Temperature Storage (HTS)	Ta= 60°C , 300hours	
Low Temperature Storage (LTS)	Ta= -20°C , 300hours	
Thermal Shock Test (TST)	-20°C/30min, 60°C/30min, 100 cycles	<b>Note 4-1</b>

- Note 4-1:**
- A cycle of rapid temperature change consists of varying the temperature from -20°C to 60°C, and back again. Power is not applied during the test.
  - After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.



5 Shipping Label

5.1 Small Shipping Label

The label is on the PCBA as shown below :

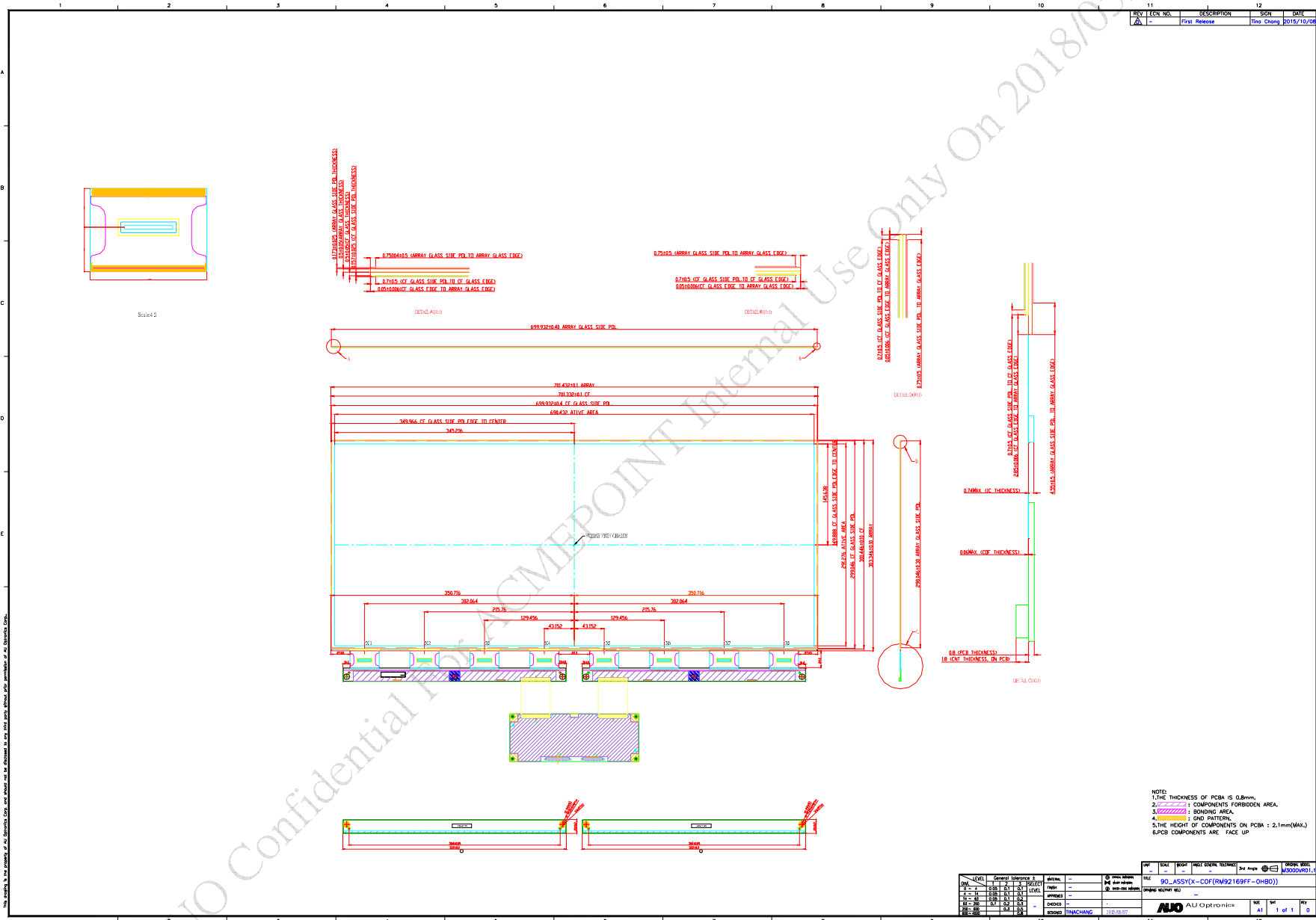


5.2 Run Card Label

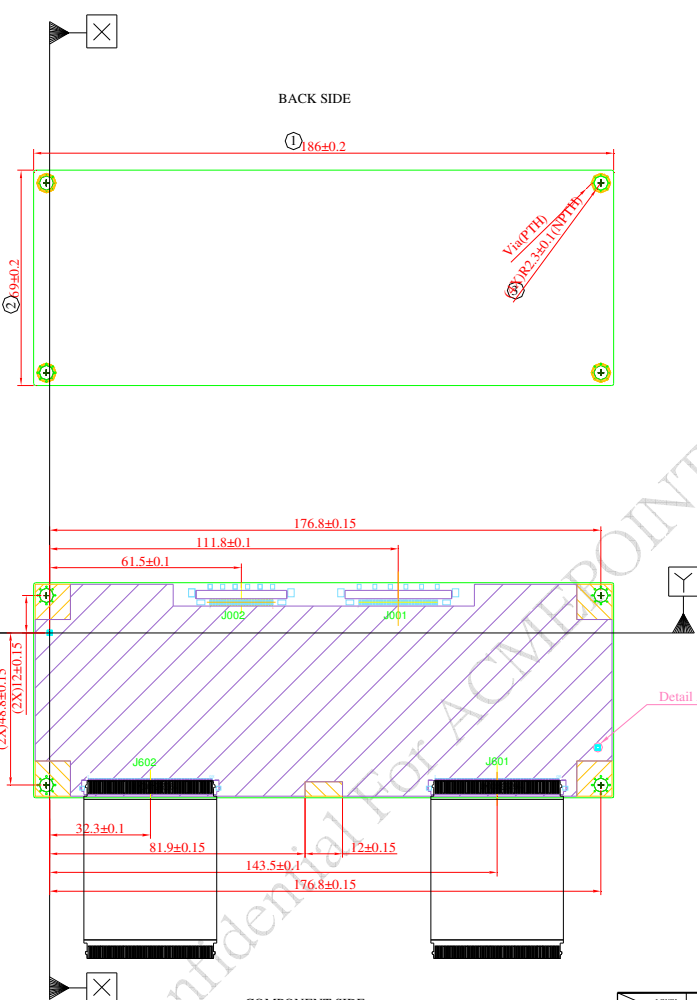
The label is on the panel as shown below :



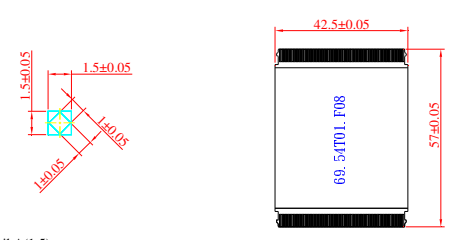
# 6 Mechanical Characteristics



REV	ECN NO.	DESCRIPTION	SIGN	DATE
△	-	PRELIMINARY	Tina Chang	2015/11/10
△	-			
△	-			
△	-			



- NOTES
- ① CARD THICKNESS TO BE 0.8±0.1 mm.
  - ② UNSPECIFIED DIMENSION TOLERANCE IS ±0.2.
  - ③ ALL COMPONENTS SHALL BE SURFACE MOUNT TYPE.
  - ④ GROUND PATTERN
  - ⑤ CONNECTOR: J001: JAE/FI-RE51S\_HF / J002: FI-RE41SHF  
J601 & J602: 196225-80041
  - ⑥ CHECK CODE : 1
  - ⑦ MATERIAL : FR4-6L
  - ⑧ THE CARD WARP SHOULD NOT LARGER THAN 1.5mm AND 7.5/1000.
  - ⑨ COMPONENT AREA. COMPONENT HEIGHT LIMIT IS 3.0 mm CONNECTOR EXCEPT



DTL	LEVEL	General Tolerance			SELECT
		1	2	3	
0	4	0.05	0.1	0.1	LEVEL
4	14	0.05	0.1	0.1	LEVEL
14	63	0.05	0.1	0.2	LEVEL
63	250	0.1	0.2	0.3	LEVEL
250	630	0.2	0.3	0.5	LEVEL
630	4000	0.3	0.5	0.8	LEVEL

PROPERTY	VALUE
MATERIAL	-
FINISH	-
APPROVED	-
CHECKED	-
DESIGNED	Tina Chang

ENT	SCALE	UNIT	ANGLE	GENERAL TOLERANCE	3rd Angle	ORIGINAL MODEL
MM	1:1	-	-	-		M300DVR01.1

TITLE: LVDS\_CB\_186MM\*69MM(75HZ)

DRAWING NO. (PART NO.): 55.30M01.C02

**AUO** AU Optronics Corp. SIZE: 1 OF 1 SHEET: 0

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