

	SPECIFIC	ATIONS						
CUSTOMER	: F	РΤС						
SAMPLE CODE		NSC1602LRU-GW	/A-H					
MASS PRODUCTION CODE		NPC1602LRU-GW	/A-H					
SAMPLE VERSION		)1						
SPECIFICATIONS EDITION	: C	001						
DRAWING NO. (Ver.)	: •	JLMD- NPC1602L	RU-GWA-H_002	2				
PACKAGING NO. (Ver.)	: .	JPKG- NPC1602L	RU-GWA-H_00 <sup>4</sup>	1				
		Date	:	POWERTIP 2014.06.09 JS RD APPROVED				
Approved	Check	ked	Designer	,				
閆偉	劉進	Ē	周志仙					
<ul> <li>Preliminary specification for design input</li> <li>Specification for sample approval</li> </ul>								
P Headquarters: No.8, 6 <sup>th</sup> Road, Taichung Ir	OWERTIP TE	CH. CORP.						



# History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
06/04/2014	01	001	New Sample	-	周志仙
				Tota	al:30 Pages



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Note : For detailed information please refer to IC data sheet : SITRONIX---ST7066U-0A



#### **1. SPECIFICATIONS**

#### 1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Y/G , Positive , Transflective
Driver Condition	LCD Module: 1/16 Duty · 1/5 Bias
Viewing Direction	6 O'clock
Weight	32.8g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer web site :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

## **1.2 Mechanical Specifications**

Item	Standard Value	Unit
Outline Dimension	80.0 (L) * 36.0 (W) *12.7 (H)	mm
Viewing Area	66.0 (L) * 16.0 (W)	mm
Active Area	56.2 (L) * 11.5 (W)	mm
Character Size	2.95(L) * 5.55(W)	mm
Character Pitch	3.55(L) * 5.95(W)	mm

Note : For detailed information please refer to LCM drawing

#### 1.3 Absolute Maximum Ratings

	<u> </u>				
Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	$V_{DD}$	-	-0.3	7.0	V
LCD Driver Supply Voltage	$V_{LCD}$	-	Vdd -10.0	VDD +0.3	
Input Voltage	V <sub>IN</sub>	-	-0.3	V <sub>DD</sub> +0.3	V
Operating Temperature	T <sub>OP</sub>	-	-20	70	°C
Storage Temperature	T <sub>ST</sub>	-	-30	80	°C
Storage Humidity	$H_{D}$	Ta<60 ℃	-	90	%RH

## **1.4 DC Electrical Characteristics**

					Ta = 1	<u>25℃</u>
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	$V_{DD}$	-	4.5	5.0	5.5	V
"H" Input Voltage	VIH	-	0.7 Vdd	-	Vdd	V
"L" Input Voltage	V <sub>IL</sub>		-0.3	-	0.6	V
"H" Output Voltage	V <sub>OH</sub>	IOH=-0.1mA	3.9	-	Vdd	V
"L" Output Voltage	V <sub>OL</sub>	IOL=0.1mA	-	-	0.4	V
Supply Current	I <sub>DD</sub>	V <sub>DD</sub> = 5.0 V ,Vop= 4.5 V Pattern= Horizontal *1	-	2.0	3.0	mA
	V <sub>OP</sub>	<b>-20</b> °C	4.4	4.6	4.8	
LCM Driver Voltage		<b>25</b> ℃	4.3	4.5	4.7	V
	*2	<b>70</b> °C	4.1	4.3	4.5	

NOTE: \*1 The Maximum current display

\*2 The  $V_{OP}$  test point is (V\_DD - Vo)



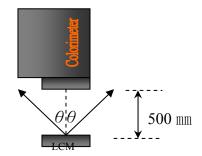
#### **1.5 Optical Characteristics**

			LCD Panel	:1/16 Dut	:y,1/5 Bia	as,V <sub>LCD</sub> :	= 4.5 V,	Ta =25℃
Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Response Time	Rise	tr		-	80	125	ms	Note 2
Response nine	Fall	tf	_	-	220	330	1115	NOLE 2
	Тор	θ+		-	40			
Viewing angle	Bottom	θ-	C>2.0	-	40	-	Dog	Note 1
range	Left	θL	C <u>~</u> 2.0	-	45	-	Deg.	NOLE I
	Right	θR		_	45	-		
Contrast Ra	tio	С	-	-	10		-	Note 3
Average Bright (with LCD)		IV		30	35	-	cd/m <sup>2</sup>	
Wavelength (with LCD) Uniformity		λρ	IF= 100 mA	568	571	574	nm	Note 4
		∆B		70	-		%	

Note 4 :

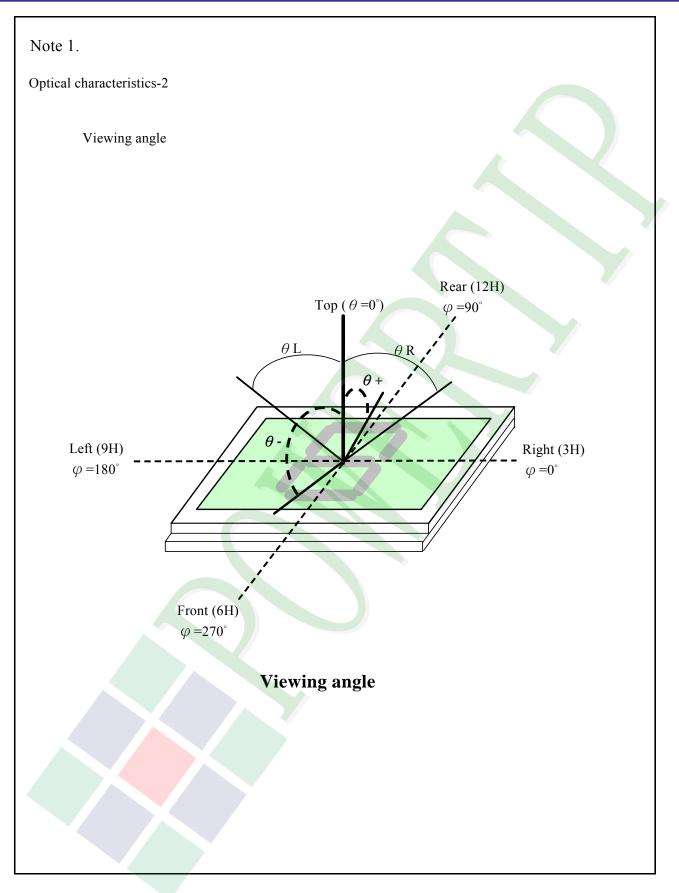
- $1 : \triangle B = B(\min) / B(\max) * 100\%$
- 2 : Measurement Condition for Optical Characteristics:
  - a : Environment: 25℃±5℃ / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
  - b : Measurement Distance:  $500 \pm 50 \text{ mm}$  , ( $\theta = 0^{\circ}$ )
  - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
  - d : The uncertainty of the C.I.E coordinate measurement  $\pm 0.01$  , Average Brightness  $\pm 4\%$



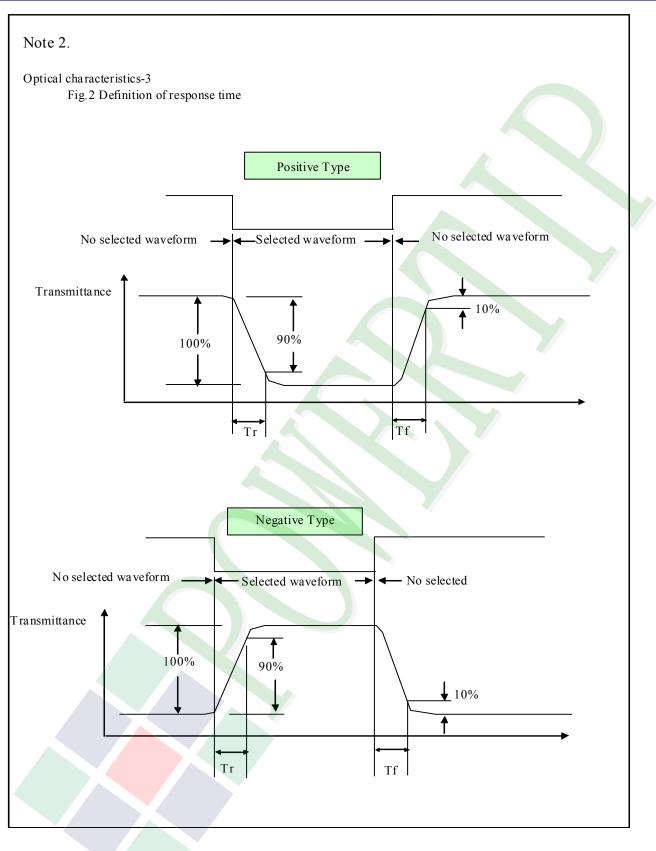


Colorimeter=BM-7 fast

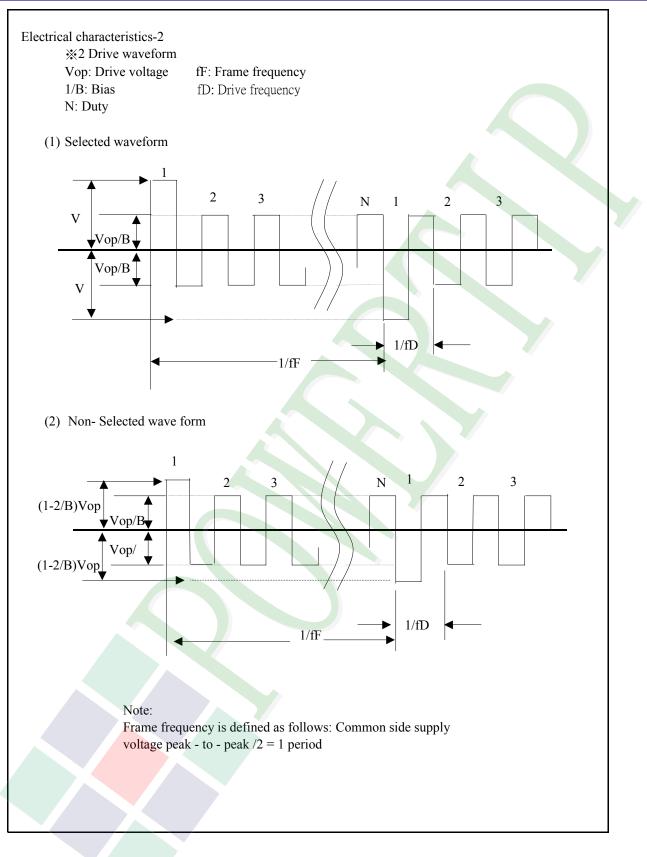




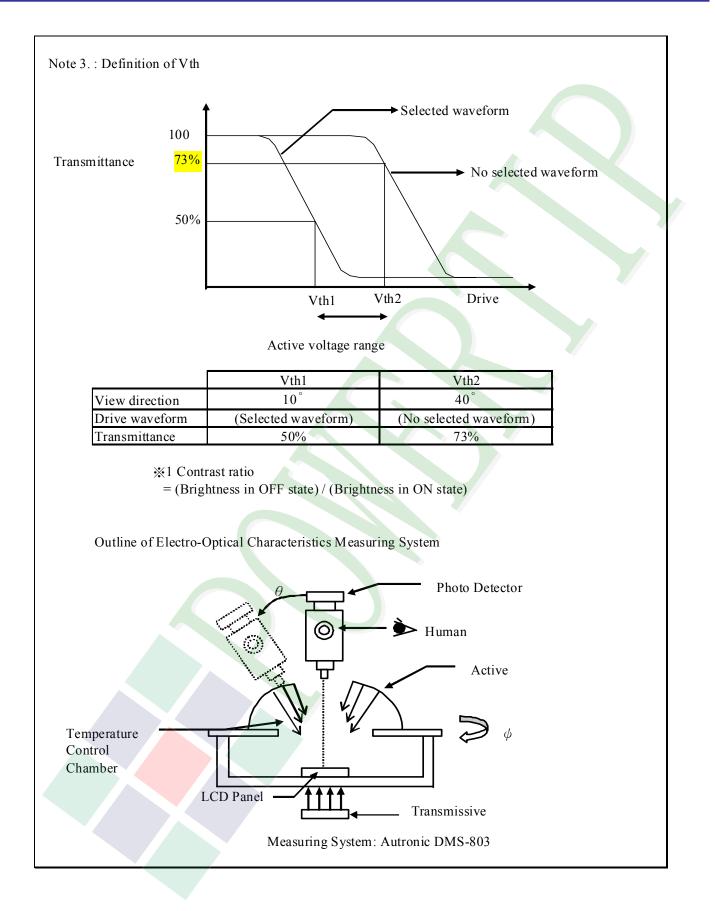














## **1.6 Backlight Characteristics**

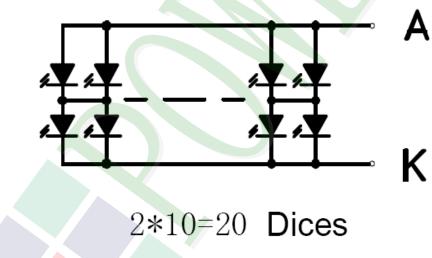
#### Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	<b>Ta =25</b> ℃	-	150	mA
Reverse Voltage	VR	<b>Ta =25</b> ℃	-	8	V
Power Dissipation	PD	<b>Ta =25</b> ℃	-	660	mW

#### Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF		4.0	4.2	4.4	V
Average Brightness (without LCD)	IV	IF= 100 mA	160	190	<b>Y</b> -	cd/m <sup>2</sup>
Wavelength (Without LCD)	λρ		569	572	575	nm
Color	Yellow/Green					

#### Internal Circuit Diagram:





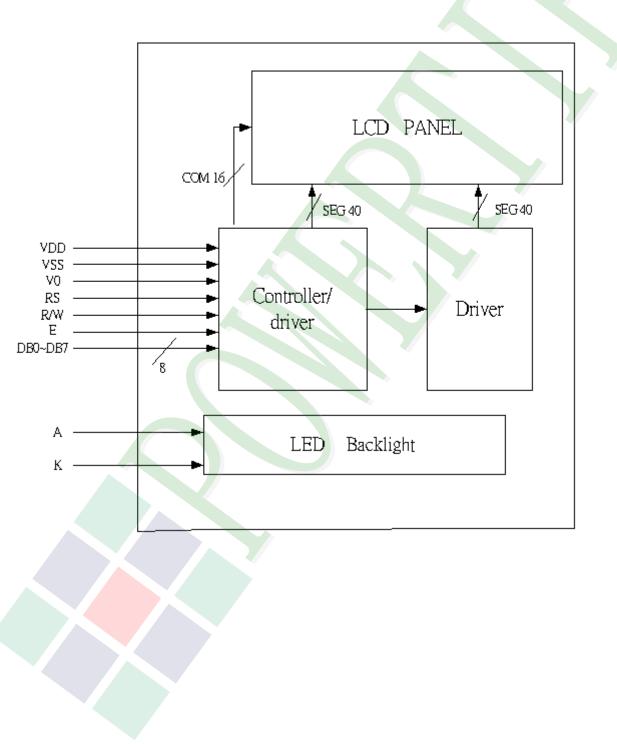
## 2. MODULE STRUCTURE

## 2.1 Counter Drawing

#### 2.1.1 LCM Mechanical Diagram

\* See Appendix

#### 2.1.2 Block Diagram





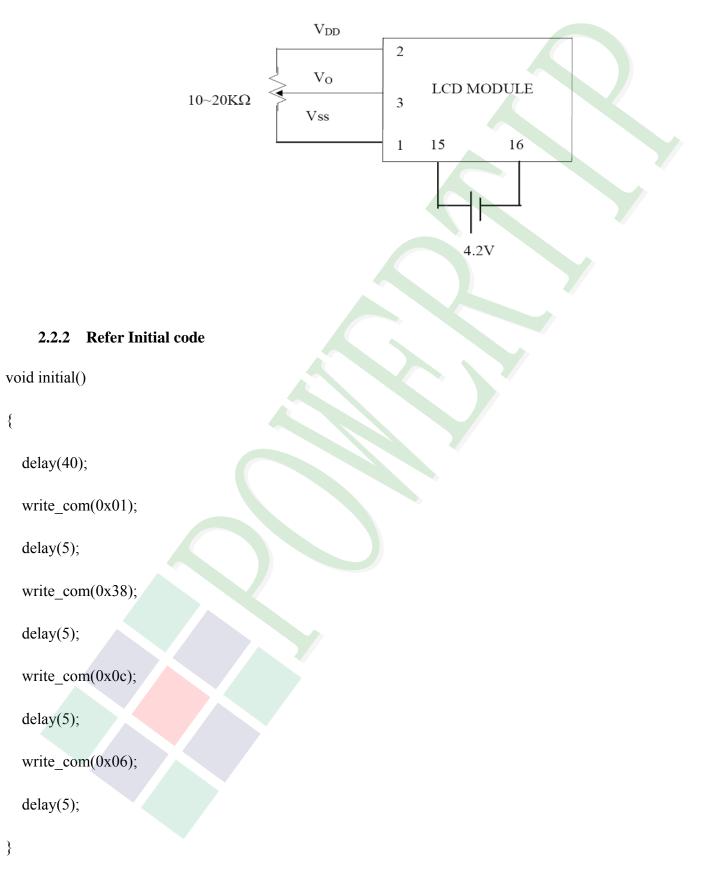
## 2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	V <sub>SS</sub>	Power Supply (Vss=0)
2	$V_{DD}$	Power Supply (5V)
3	Vo	Operating voltage for LCD
		Register Selection input
4	RS	High = Data register
4	RS	Low = Instruction register (for write)
		Busy flag address counter (for read)
5	R/W	Read/Write signal input is used to select the read/write mode
5 R/W		High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7	DB0	
8	DB1	Four low order bi-directional three-state data bus lines. Use for
9	DB2	data transfer between the MPU and the LCD module.
10	DB3	—These four are not used during 4-bit operation.
11	DB4	
12	DB5	Four high order bi-directional three-state data bus lines. Used
13	DB6	for data transfer between the MPU and the LCD module.
14	DB7	_DB7 can be used as a busy flag.
15	А	LED+
16	К	LED-



#### 2.2.1 Application Notes

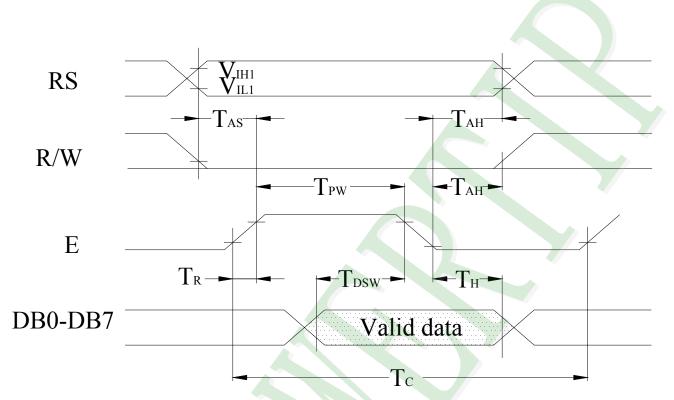
Contrast Adjust



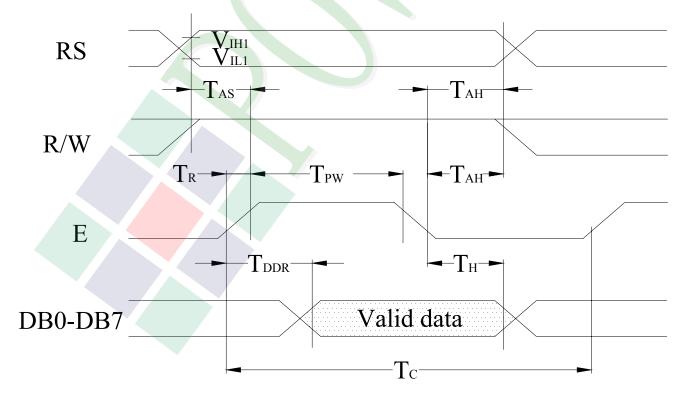


#### 2.3 Timing Characteristics

• Writing data from MPU to ST7066U



• Reading data from ST7066U to MPU





• Write Mode (Writing data from MPU to ST7066U)

				(\	/DD = 5V	,Ta=25°C)
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
T <sub>C</sub>	Enable Cycle Time	Pin E	1200	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns
$T_R$ , $T_F$	Enable Rise / Fall Time	Pin E	-	- (	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T <sub>DSW</sub>	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
Т <sub>Н</sub>	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

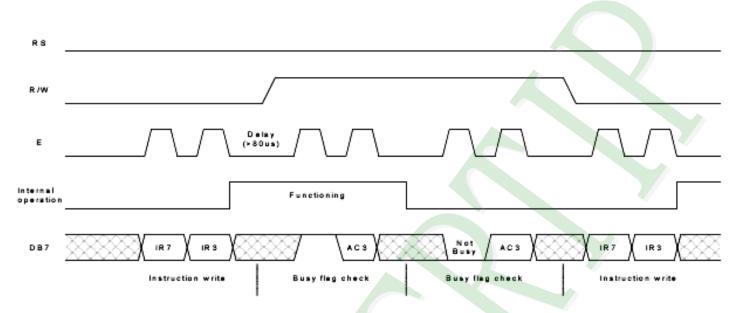
Read Mode (Reading data from ST7066U to MPU)

					VDD = 5\	/,Ta=25°C)
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	-	-	ns
T <sub>PW</sub>	Enable Pulse Width	Pin E	140	-	-	ns
$T_R, T_F$	Enable Rise / Fall Time	Pin E	-	-	25	ns
T <sub>AS</sub>	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T <sub>AH</sub>	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T <sub>DDR</sub>	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
Τ <sub>Η</sub>	Data Hold Time	Pins:DB0~DB7	10	-	-	ns



For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .

Exa	mple of busy flag check timing sequence
RS	
R <i>I</i> W	
E	D e la y (> 80 u s)
internal operation	Functioning
DB7	Data         Busy         Busy         Not Busy         Data           Instruction write         Busy flag check         Busy flag check         Busy flag check         Instruction write



## 2.4 Display Command

		Instruction Code									Description		
Instructions		R/	DB	DB	DB	DB	DB	DB	DB	DB	Description	Time	
	RS	W	7	6	5	4	3	2	1	0		(270KHz)	
01											Write "20H" to DDRAM. and set		
Clear	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms	
Display											AC.		
											Set DDRAM address to "00H"		
Deture									1		from AC and return cursor to it's		
Return	0	0	0 0	0	0	0	0	0		×	original position if shifted.	1.52ms	
Home											The contents of DDRAM		
											are not changed.		
											Sets cursor move direction and		
Entry Mode	~	~	~	0	0	•				0	specifies display shift. These	07.	
Set	0	0	0	0	0	0	0	1	I/D	S	operations are performed	37µs	
											during data write and read .		
Display											D=1 : entire display on		
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1 : cursor on	<b>37</b> μ <b>s</b>	
											B=1 : cursor position on		
Cureer or											Set cursor moving and display		
Cursor or	0	0	0	0	0	1	S/C	R/L	×	×	shift control bit, and the	270	
Display Shift	0		0	0	0	0	0		3/0	R/L			the direction, without changing
Shint											of DDRAM data.		
Function											DL: interface data is 8/4 bits		
Set	0	0	0	0	1	DL	Ν	F	×	×	NL: number of line is 2/1	<b>37</b> μ <b>s</b>	
Sei											F: font size is 5×11/5×8		
Set					AC	AC	AC	AC	AC	<u>، د</u>	Set CGRAM address		
CGRAM	0	0	0	1	AC 5	4 4	3	2	AC 1		in address counter.	<b>37</b> μ <b>s</b>	
Address					5	4	5	~ 2	1	0			
Set				AC	AC	AC	AC	AC	AC		Set DDRAM address		
DDRAM	0	0	1	6	5	4	3	2	1	0	in address counter.	<b>37</b> μ <b>s</b>	
Address				0	5	-	5	2		0			
											Whether during internal		
Read Busy			в	AC	AC	AC	AC	AC	AC	AC	operation or not can be		
Flag and	0	1	F	6	5	4	3	2	1	0	known by reading BF.	<b>0</b> μ <b>s</b>	
Address				0	5	-		2		U	The contents of address		
											counter can also be read.		



Write Data	1	0 D D6 D5 D4 D3 D2 D1 D0		D0	Write data into internal RAM	37µs								
to RAM	to RAM		0 7 D						וט	DU	(DDRAM/CGRAM).	57µ8		
Read Data			1	1	D	D6	DE	D4	2	50			Read data from internal RAM	270
from RAM		1 1		' ' 7	7	00	D5	D4	DS	DZ	וט	D0	(DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF Refer to Instruction Table for the list of each instruction execution time .

## 2.5 Character Pattern

## NO.7066-0A

<u>140.7</u>	000-	07														
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)															
0001	(2)															
0010	(3)															
0011	(4)															
0100	(5)															
0101	(6)															
0110	$\sigma$															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	ග															
1101	(6)															
1110	0															
1111	(8)															

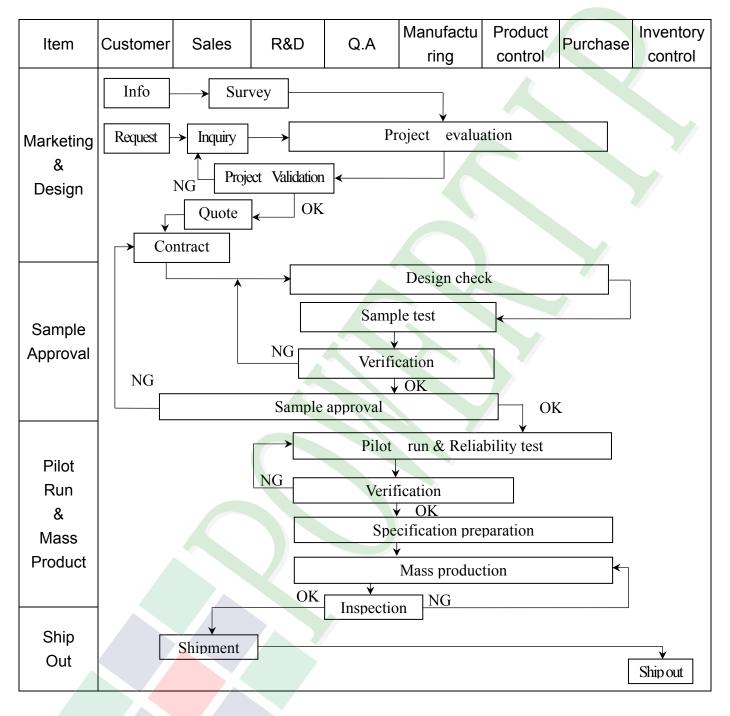
# 2.6 JUMPER (Setting different use)

J1 \ J4



# **3. QUALITY ASSURANCE SYSTEM**

## 3.1 Quality Assurance Flow Chart





Item	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control
Sales Service	Info Analys	→ Claim -	[	Trackin	Failure an Corrective			
Q.A Activity	1. ISO 900 3. Equipme 5. Standare	ent calibrat	ion	4	Process in . Education			es

## 3.2 Inspection Specification

Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).

◆Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.

(100% tested before shipment)

**•**Equipment : Gauge • MIL-STD • Powertip Tester • Sample

◆Defect Level: Major Defect AQL: 0.4 ; Minor Defect: AQL: 1.5.

**OUT Going Defect Level : Sampling .** 

#### ◆Manner of appearance test :

- (1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.
- (2). Standard of inspection : (Unit : mm)
- (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
- (4). Definition of area . (Fig. 2)

**B** area : Outside of viewing area



#### ♦ Specification:

NO	Item	Criterion	Level				
	Product condition	1. 1 The part number is inconsistent with work order of Production.	Major				
01		1. 2 Mixed production types.	Major				
		1.3 Assembled in inverse direction.	Major				
02	Quantity	. 1 The quantity is inconsistent with work order of production.					
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major				
		4.1 Missing line character and icon.	Major				
		4. 2 No function or no display.	Major				
04	Electrical Testing	4. 3 Output data is error.	Major				
		4. 4 LCD viewing angle defect.	Major				
		4.5 Current consumption exceeds product specifications.	Major				



<b>◆</b> Spe	ecification For Mono	type and Color STN:				(	Ver.B01)	
NO	Item		Criteri	on			Level	
	Black or white dot < scratch < contamination	<ul> <li>5. 1 Round type:</li> <li>5. 1. 1 display only : <ul> <li>White and black spots on display ≤ 0. 30 mm, no more than 4 white or black spots present.</li> <li>Densely spaced : NO more than two spots or lines within 3 mm.</li> </ul> </li> <li>5. 1. 2 Non-display :</li> </ul>						
		Dimension	A	cceptance	(Q'ty)			
	Round type $X _{Y}$	(diameter : Φ)	A area B		B are	a		
		$\Phi \leq 0.10$	Accept	no dense				
05		$0.10 < \Phi \leq 0.20$		3	T		Minon	
00		$0.20 < \Phi \le 0.30$ 2		Ignore		Minor		
	$\Phi = (x+y)/2$	Total quantity		4				
	Line type	5. 1. 3 Line type: Dimension Acceptan				( <b>Q'ty</b> )		
		Length (L) Width	(W)	A ar	ea	B area		
	∕ ¥ W	W	<b>≦ 0.03</b>	Accept no	Accept no dense			
	$\rightarrow \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$	$ \begin{array}{c c} L \leq 3.0 & 0.03 < W \\ L \leq 2.5 & 0.05 < W \\ \end{array} $		<u> </u>				
			>0.075					
		Dimension			tance (Q't	-		
		$(\text{diameter}: \Phi)$ $\Phi \leq 0.20$		A area	<b>n</b> G o	B area		
				cept no de	lise			
06	Polarizer	$0.20 < \Phi \le 0.50$		3		T	Minor	
	Bubble	$0.50 < \Phi \le 1.00$		2		Ignore		
		$\Phi > 1.00$ Total quantity		0 4				
		Total quantity		4				

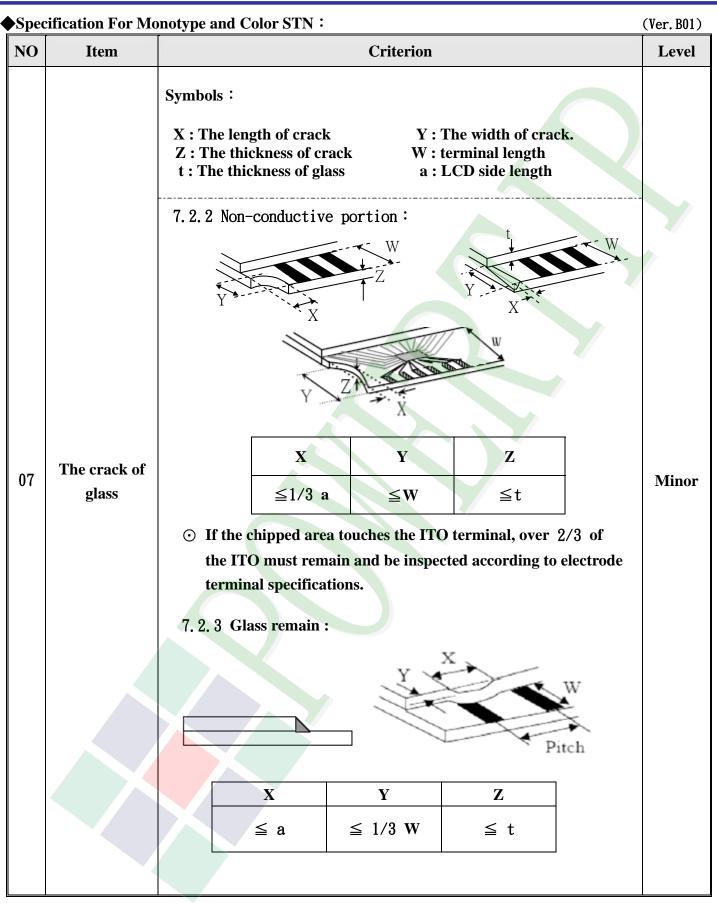


Item	Criterion Symbols : X : The length of erects		Level
	Z : The thickness of crack W :	The width of crack. terminal length LCD side length	
	7.1 General glass chip: 7.1.1 Chip on panel surface and crack	between panels:	
		Z X X X	
The crack of glass		ING J	Minor
	Seal width	Y	
	XY	Z	
	$\leq a \qquad \begin{array}{c} Crack \ can't \ enter \\ viewing \ area \end{array}$	$\leq 1/2 t$	
	$\leq a \qquad \begin{array}{c} Crack \ can't \ exceed \ the \\ half \ of \ SP \ width. \end{array}$	$1/2 t < Z \leq 2 t$	
		The crack of glass $I = \frac{X}{Y}$ $X = \frac{X}{$	The crack of glass $\frac{X \times X}{Seal width} = \frac{X}{Seal width} = \frac{X \times X}{Seal width} = \frac{X}{Seal width} = \frac{X \times X}{Seal width} = \frac{X \times X}{$



NO	Item	Criterion	Level					
		Symbols :X : The length of crackY : The width of crack.Z : The thickness of crackW : terminal lengtht : The thickness of glassa : LCD side length						
		7.1.2 Corner crack :						
		X Y Z						
		$ \leq 1/5 \text{ a}  \begin{array}{c} \text{Crack can't enter} \\ \text{viewing area} \end{array}  \mathbf{Z}  \leq 1/2 \text{ t} \end{array} $						
		$ \leq 1/5 \text{ a}  \begin{array}{c} \text{Crack can't exceed the} \\ \text{half of SP width.} \end{array}  1/2 \text{ t} < \text{Z}  \leq 2 \text{ t} \end{array} $						
07	The crack of glass	7.2 Protrusion over terminal:						
	8-400	7.2.1 Chip on electrode pad:						
		X						
		X Y Z						
		Front $\leq$ a $\leq$ 1/2 W $\leq$ t						
		Back Neglect						







<b>Specification For Monotype and Color STN</b> : (Ve										
NO	Item	Criterion	Level							
	<b>Backlight</b> elements	8. 1 Backlight can't work normally.	Major							
08		8. 2 Backlight doesn't light or color is wrong.	Major							
		8. 3 Illumination source flickers when lit.	Major							
	General appearance	9. 1 Pin type must match type in specification sheet.	Major							
		9. 2 No short circuits in components on PCB or FPC.	Major							
09		9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor							
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor							
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1.5 mm.	Minor							



## 4. RELIABILITY TEST

4.1 Reliability Test Condition

(Ver.B01)

4.1									
NO.	TEST ITEM		TEST CC	NDITION					
1	High Temperature Storage Test	Surroundin 4hrs.		storage at normal co	ndition				
2	Low Temperature Storage Test	-	±2℃ 96 hrs g temperature, then	storage at normal co	ndition				
3	High Temperature / High Humidity Storage Test	Surroundin 4hrs.	(Excluding the polarizer)						
			-30°C→ +25°C -	> 80°C → +25°C					
	Temperature Cycling	(	30mins) (5mins)	· · · · · · · · · · · · · · · · · · ·	ns)				
4	Storage Test			cycle					
		Surrounding temperature, then storage at normal condition							
		4hrs.							
			ge: with 5 times for each polarity	Contact Discharge: Apply 250 V with 5 til discharge for each p					
		1. Temperature ambiance : $15^{\circ}C \sim 35^{\circ}C$							
5	ESD Test	2. Humidity relative : $30\% \sim 60\%$							
		3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%							
		<ul> <li>4. Discharge Resistance(Rd) : 330 Ω±10%</li> <li>5. Discharge, mode of operation :</li> </ul>							
		Single Discharge (time between successive discharges at least							
		1 sec) (Tolerance if the output voltage indication : ±5%)							
	Vibration Test	1. Sine way	ve <mark>10~55</mark> Hz frequei	ncy (1 min/sweep)					
6	(Packaged)	2. The amplitude of vibration :1.5 mm							
		3. Each di	rection (X 、Y 、Z) du		1				
			Packing Weight (Kg						
			0 ~ 45.4	122	-				
7	Drop Test		45.4 ~ 90.8	76	-				
	(Packaged)		90.8 ~ 454	61					
		L	Over 454	46	J				
		Drop Direct	tion : ※1 corner / 3 e	dges / 6 sides each 1	time				

# **5. PRECAUTION RELATING PRODUCT HANDLING**

## 5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

## 5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is  $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

## **5.3 STORAGE**

- 5.3.1 Store the panel or module in a dark place where the temperature is  $25^{\circ}C \pm 5^{\circ}C$  and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

#### **5.4 TERMS OF WARRANTY**

5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.

#### 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

