



SPECIFICATIONS

CUSTOMER : PTC

SAMPLE CODE : NSC2004WRP-AWA-I

MASS PRODUCTION CODE : NPC2004WRP-AWA-I

SAMPLE VERSION : 01

SPECIFICATIONS EDITION : 003

DRAWING NO. (Ver.) : JLMD-NPC2004WRP-AWA-I_001

PACKAGING NO. (Ver.) : JPKG-NPC2004WRP-AWA-I_001

Customer Approved

Date:



Approved	Checked	Designer
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- Preliminary specification for design input
- Specification for sample approval

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Note : For detailed information please refer to IC data sheet : SITRONIX---ST7066U-0A

1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	20*4 Characters
LCD Type	PBT , Negative , Transmissive
Driver Condition	LCD Module : 1/16 Duty , 1/5 Bias
Viewing Direction	6 O'clock
Weight	71.4g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U
ROHS	THIS PRODUCT CONFORMS THE ROHS OF PTC Detail information please refer web side : http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	98.0(L) * 60.0(W) * 12.8(H)	mm
Viewing Area	76.0(L) * 25.2(W)	mm
Active Area	70.4(L) * 20.8(W)	mm
Dot Size	0.55(L) * 0.55(W)	mm
Dot Pitch	0.60(L) * 0.60(W)	mm

Note : For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V _{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	V _{LCD}	-	V _{DD} -10.0	V _{DD} +0.3	V
Input Voltage	V _{IN}	-	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	-	-20	70	°C
Storage Temperature	T _{ST}	-	-30	80	°C
Storage Humidity	H _D	Ta < 60 °C	-	90	%RH

1.4 DC Electrical Characteristics

VDD=5.0±0.5V, VSS=0V, Ta = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Logic Supply Voltage	V _{DD}	-	4.5	5.0	5.5	V
“H” Input Voltage	V _{IH}	-	0.7 VDD	-	VDD	V
“L” Input Voltage	V _{IL}	-	-0.3	-	0.6	V
“H” Output Voltage	V _{OH}	I _{OH} =-0.1mA	3.9	-	VDD	V
“L” Output Voltage	V _{OL}	I _{OL} =0.1mA	-	-	0.4	V
Excluded B/L	I _{DD} *1	V _{DD} =5.0V, V _o = 0.8 V	-	4	6	mA
LCM Driver Voltage	V _o	-20°C	-	0.8	-	V
		25°C	-	0.8	-	
		70°C	-	0.8	-	

NOTE: *1 The Maximum current display

1.5 Optical Characteristics

LCD Panel : 1/16Duty , 1/5Bias , $V_{LCD} = (8.3) V$, $T_a=25^{\circ}C$

Item		Symbol	Conditions	Min.	Typ.	Max.	Unit	Reference
Response Time	Rise	tr	-	-	40	60	ms	Note 2
	Fall	tf		-	50	75		
Viewing angle	Top	$\Theta Y+$	C>2.0	-	60	-	Deg	Note 1
	Bottom	$\Theta Y-$		-	25	-		
	Left	$\Theta X-$		-	60	-		
	Right	$\Theta X+$		-	60	-		
Contrast Ratio		CR	-	-	231	-	-	Note 3
Average Brightness (LCD & BL)		IV	IF= 180 mA	55	90	-	cd/m ²	Note 4
Color of CIE Coordinate (LCD & BL)		X		0.20	0.25	0.30	-	
		Y		0.23	0.28	0.33		
Uniformity		ΔB	-	70	-	-	%	

Note 4 :

1 : $\Delta B=B(\min) / B(\max) * 100\%$

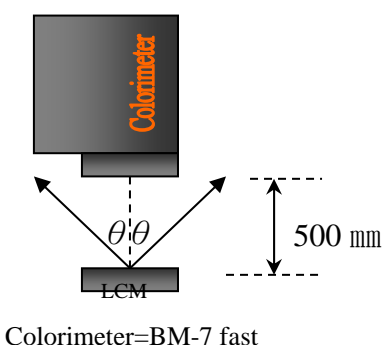
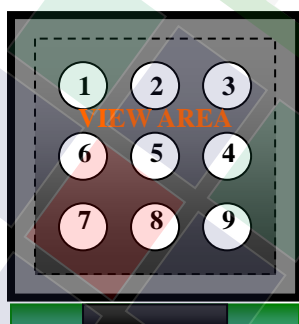
2 : Measurement Condition for Optical Characteristics:

a : Environment: $25^{\circ}C \pm 5^{\circ}C$ / $60 \pm 20\%R.H$, no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.

b : Measurement Distance: $500 \pm 50 \text{ mm}$, ($\theta= 0^{\circ}$)

c : Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.

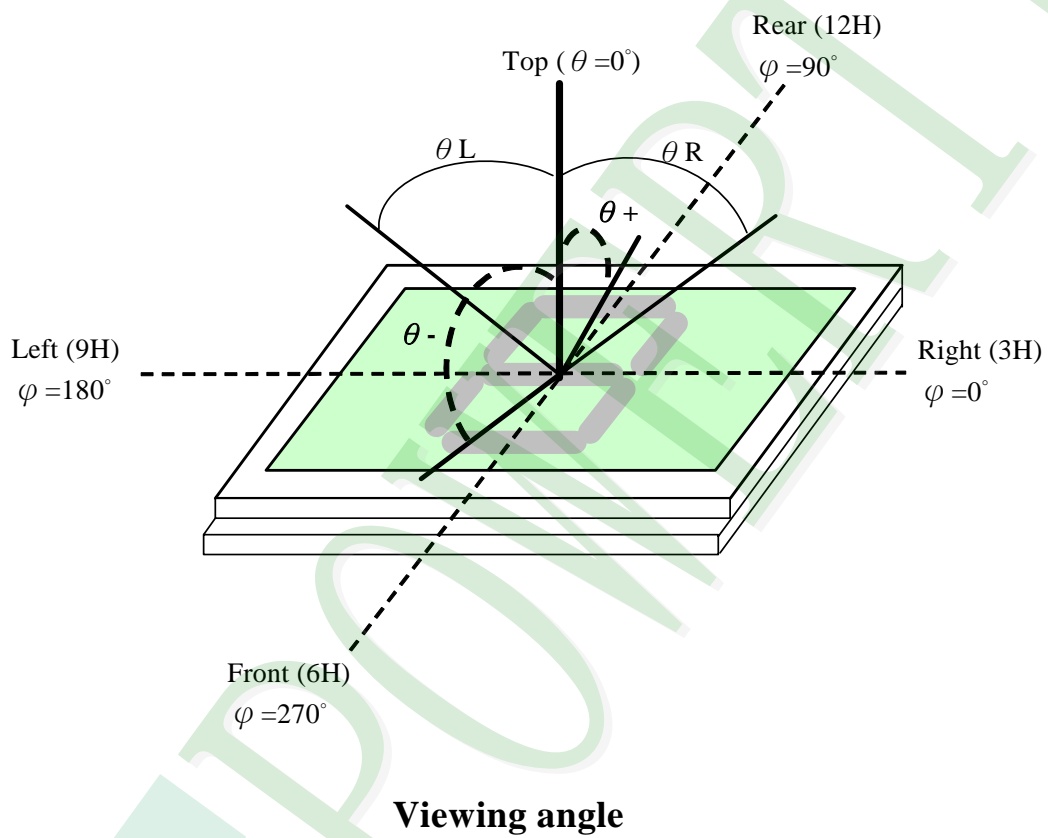
d : The uncertainty of the C.I.E coordinate measurement ± 0.01 , Average Brightness $\pm 4\%$



Note 1.

Optical characteristics-2

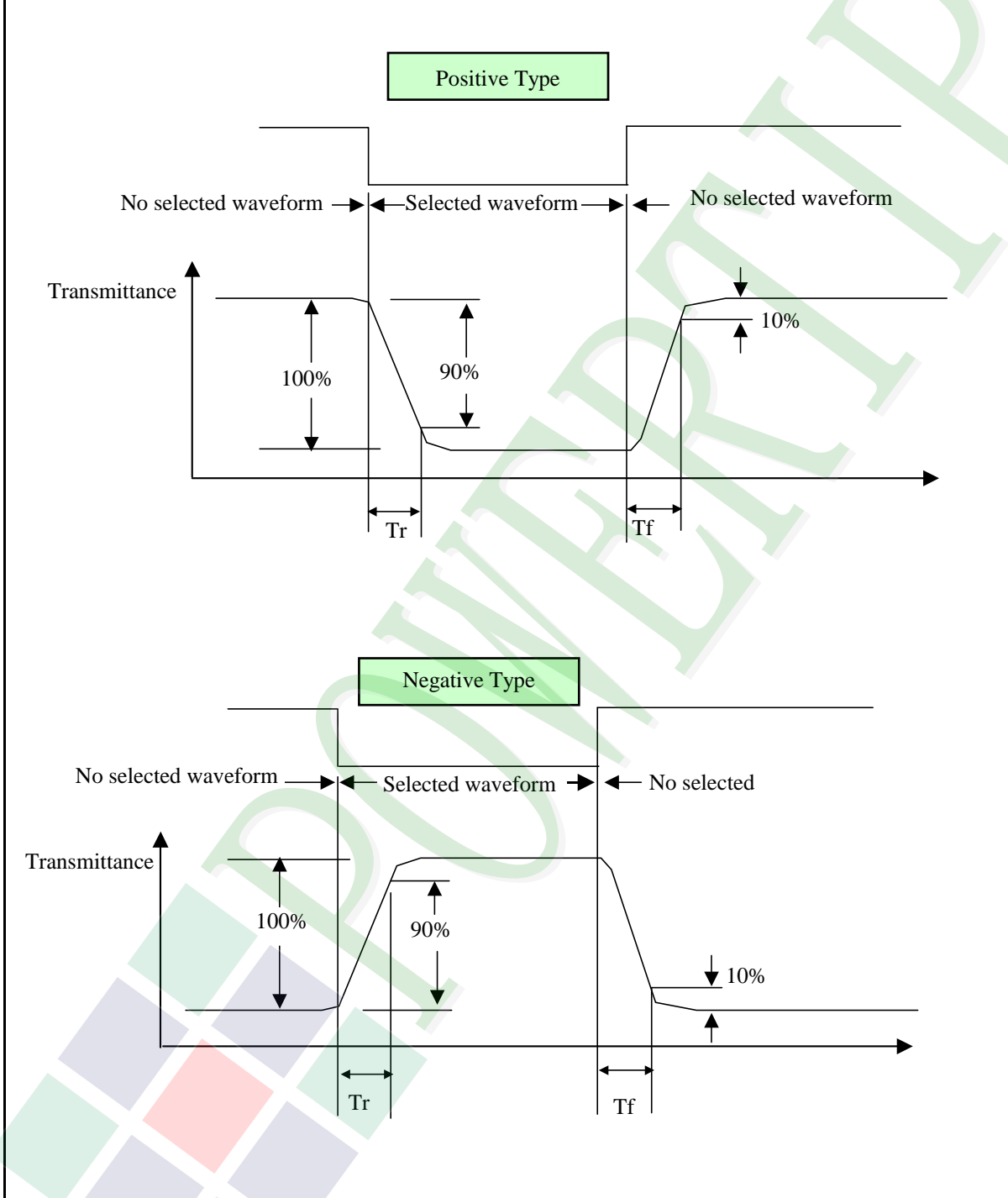
Viewing angle



Note 2.

Optical characteristics-3

Fig.2 Definition of response time



Electrical characteristics-2

※2 Drive waveform

 V_{op} : Drive voltage

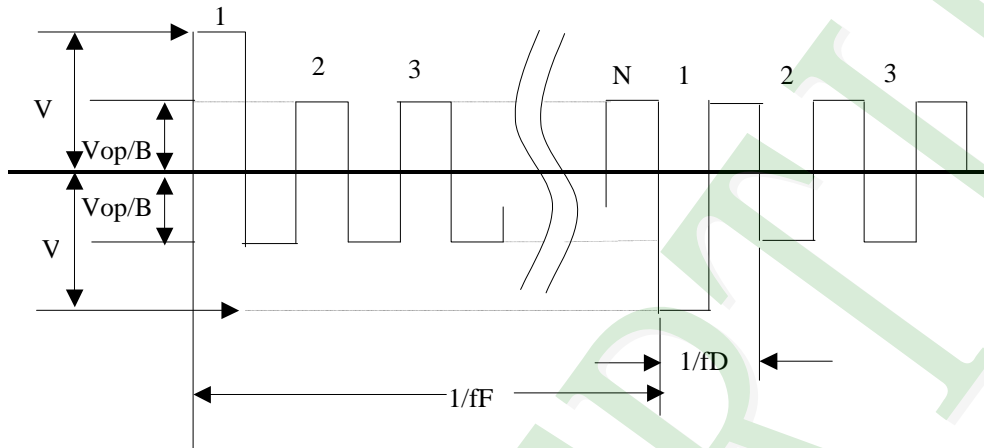
 f_F : Frame frequency

 $1/B$: Bias

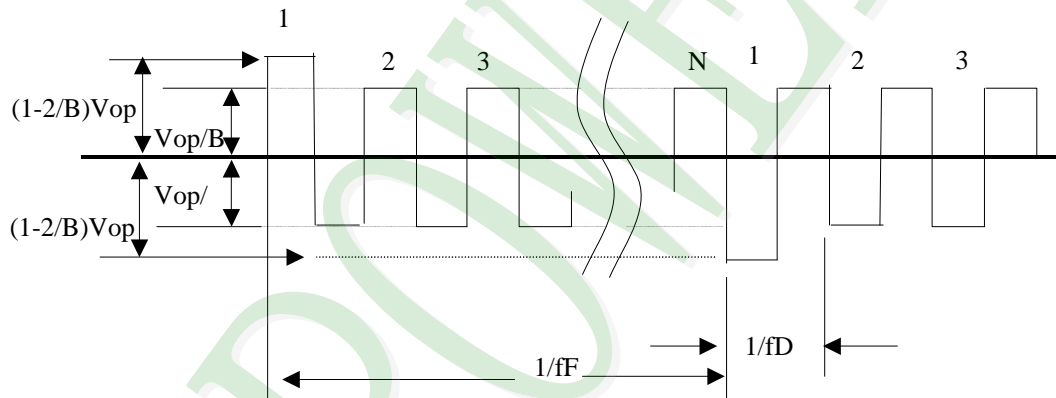
 f_D : Drive frequency

 N : Duty

(1) Selected waveform



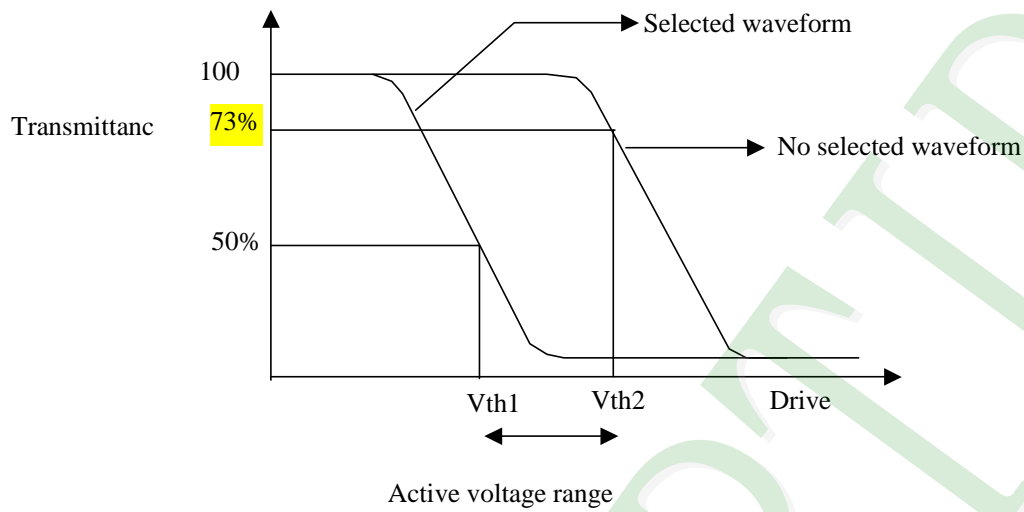
(2) Non- Selected wave form



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak / 2 = 1 period

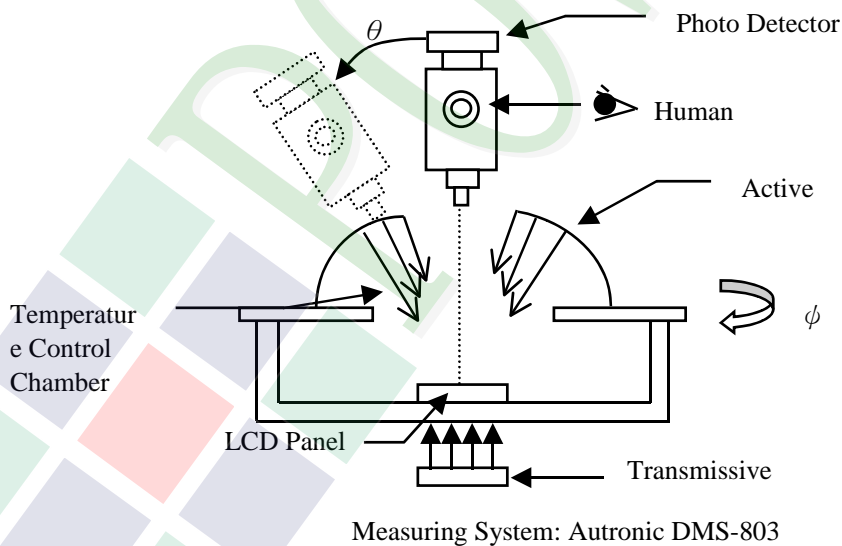
Note 3. : Definition of Vth



	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio
= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System



1.6 Backlight Characteristics

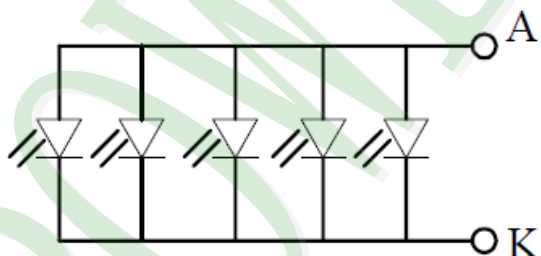
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	175	mA
Reverse Voltage	VR	Ta =25°C	-	5	V
Power Dissipation	PD	Ta =25°C	-	575	W

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Typ.	Max.	Unit
Forward Voltage	VF	IF= 180 mA	2.9	3.1	3.3	V
Average Brightness (without LCD)	IV		1875	2250	-	cd/m ²
CIE Color Coordinate (Without LCD)	X		0.25	0.28	0.32	-
	Y		0.25	0.28	0.32	
Color	White					

Internal Circuit Diagram:



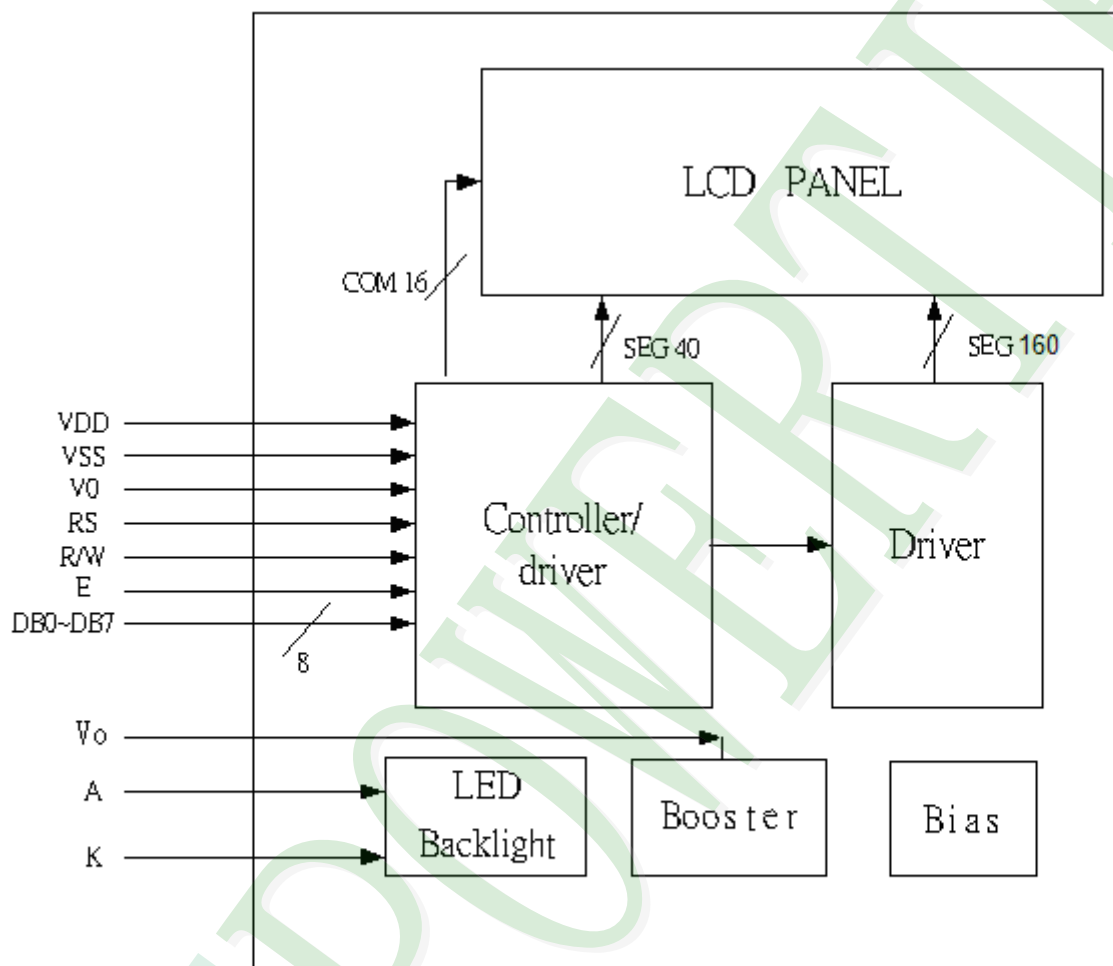
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram

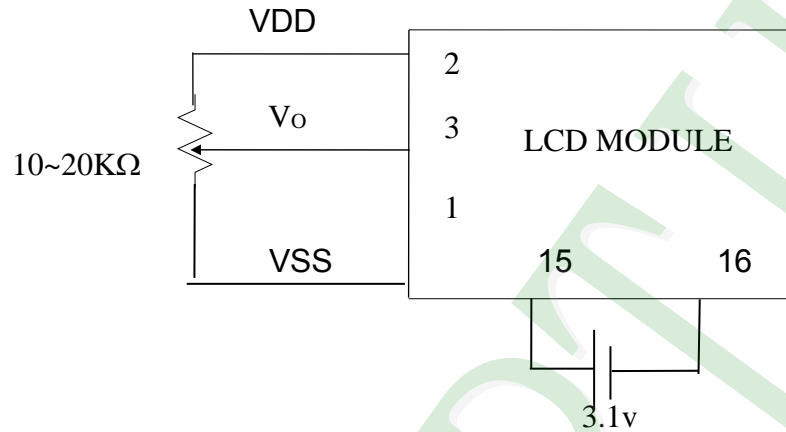


2.2 Interface Pin Description

Pin No	Symbol	Signal Description
1	Vss	Power Supply(Vss=0)
2	VDD	Power Supply (5V)
3	VO	Operating voltage for LCD
4	RS	Register Selection input High=Data register Low=Instruction register(for write) Busy flag address counter (for read)
5	R/W	Read /Write signal input is used to select the read/write mode High=Read mode; Low=Write mode
6	E	Star enable signal to read or write the data
7	DB0	Four low order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. These four are not used during 4-bit operation.
8	DB1	
9	DB2	
10	DB3	
11	DB4	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module. DB7 can be used as a busy flag.
12	DB5	
13	DB6	
14	DB7	
15	A	LED+
16	K	LED-

2.2.1 Application Notes

Contrast Adjust

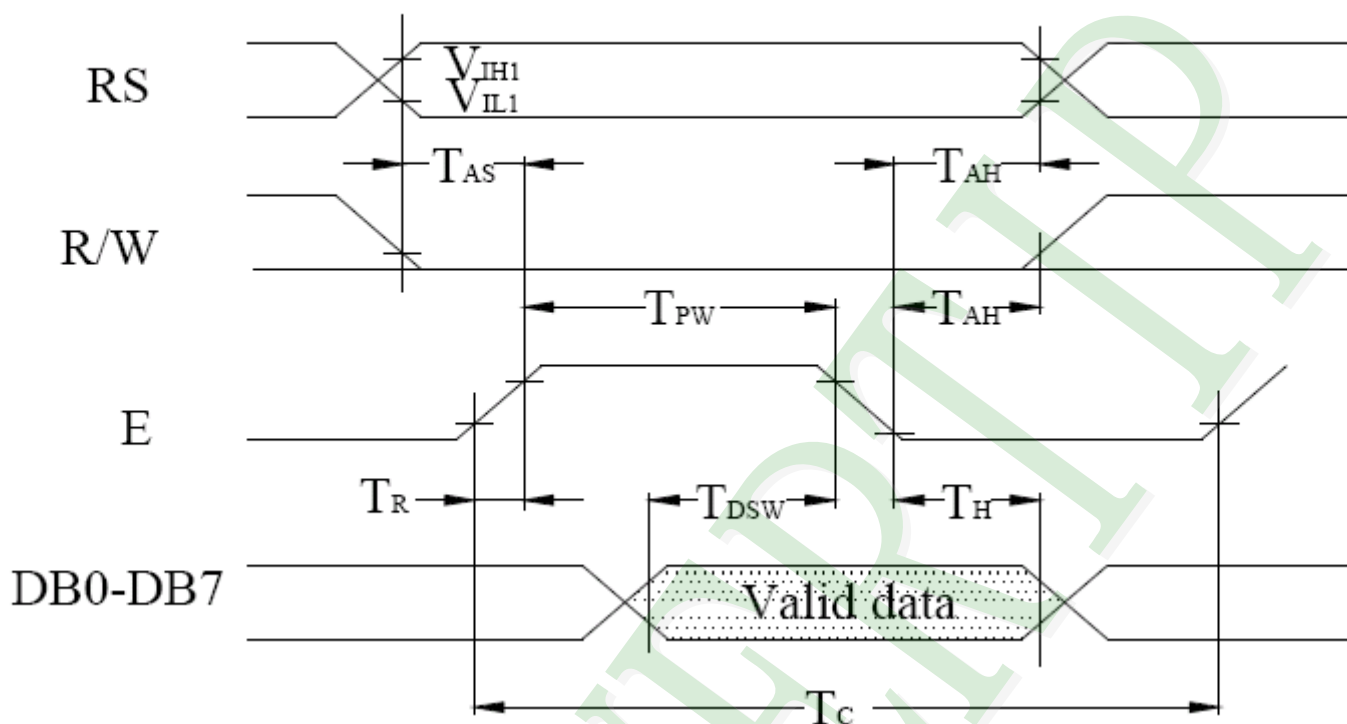


2.2.2 Refer Initial code

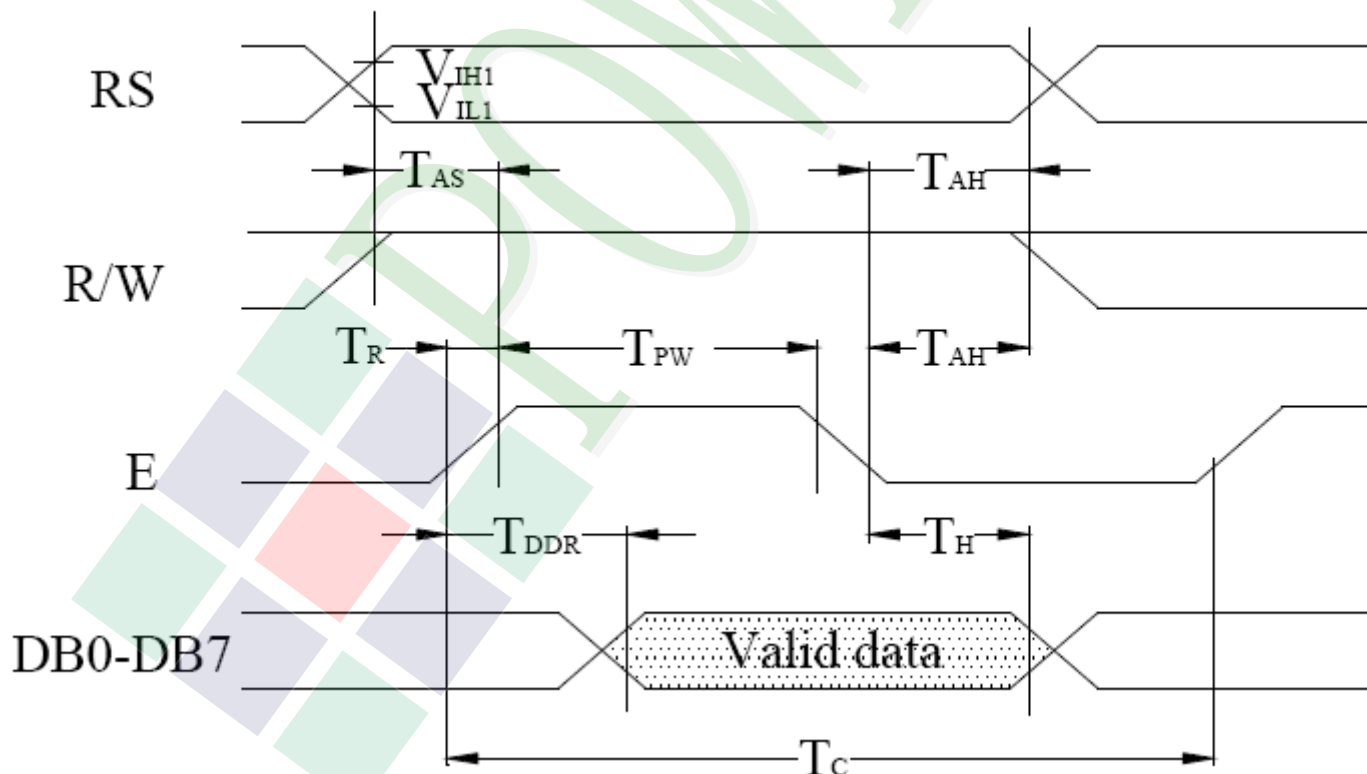
```
void initial()
{
    delay(40);
    write_com(0x01);
    delay(5);
    write_com(0x38);
    delay(5);
    write_com(0x0c);
    delay(5);
    write_com(0x06);
    delay(5);
}
```

2.3 Timing Characteristics

- Writing data from MPU to ST7066U



- Reading data from ST7066U to MPU



• Write Mode (Writing data from MPU to ST7066U)

(V_{cc} = +5V, T_a = 25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS, RW, E	0	-	-	ns
T _{AH}	Address Hold Time	Pins: RS, RW, E	10	-	-	ns
T _{DSW}	Data Setup Time	Pins: DB0~DB7	40	-	-	ns
T _H	Data Hold Time	Pins: DB0~DB7	10	-	-	ns

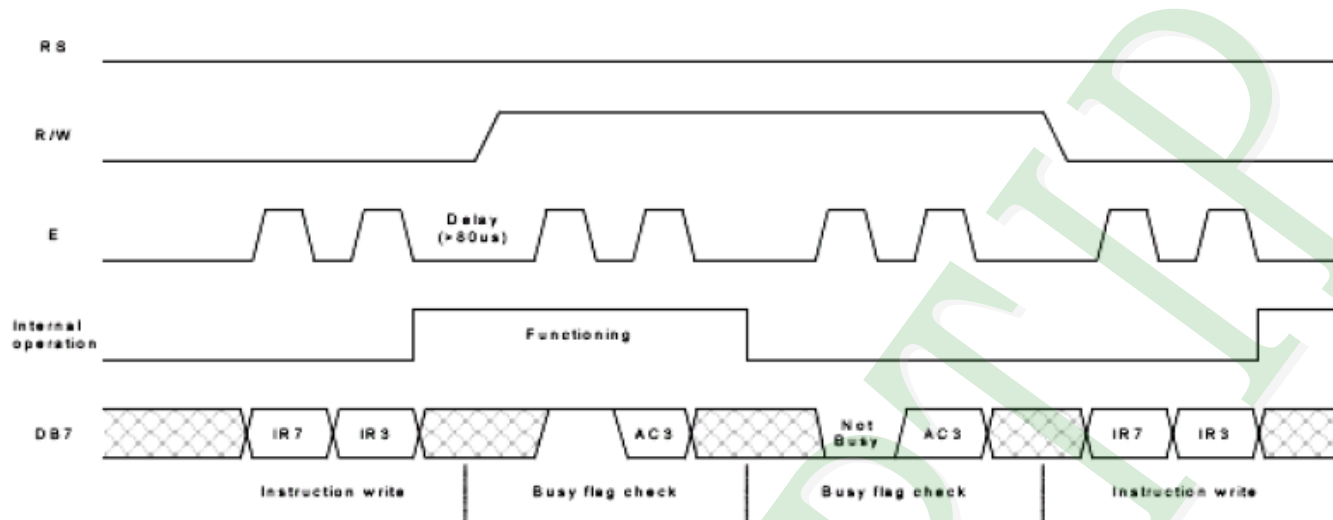
• Read Mode (Reading data from ST7066U to MPU)

(V_{cc} = +5V, T_a = 25°C)

Symbol	Characteristics	Test Condition	Min.	Typ.	Max.	Unit
T _C	Enable Cycle Time	Pin E	1200	-	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
T _{AS}	Address Setup Time	Pins: RS, RW, E	0	-	-	ns
T _{AH}	Address Hold Time	Pins: RS, RW, E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins: DB0~DB7	-	-	100	ns
T _H	Data Hold Time	Pins: DB0~DB7	10	-	-	ns

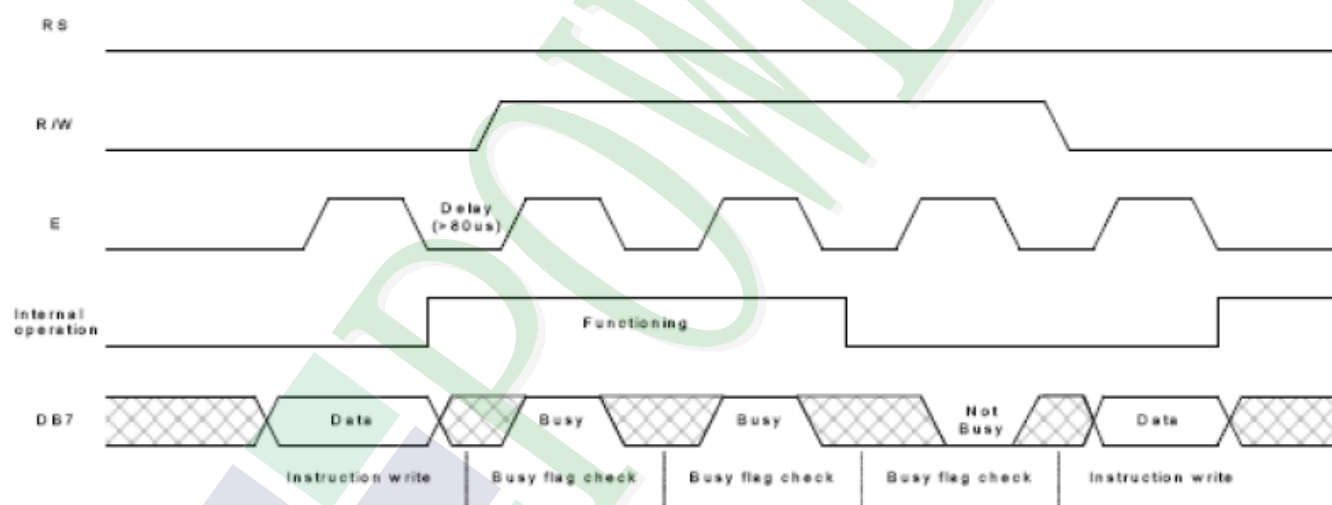
For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer.

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used .

Example of busy flag check timing sequence



2.4 Display Command

Instructions	Instruction Code										Description	Description Time (270KHz)
	RS	R/W	DB 7	DB 6	DB 5	DB 4	DB 3	DB 2	DB 1	DB 0		
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM. and set DDRAM address to "00H" from AC.	1.52ms
Return Home	0	0	0	0	0	0	0	0	1	x	Set DDRAM address to "00H" from AC and return cursor to it's original position if shifted. The contents of DDRAM are not changed.	1.52ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	S	Sets cursor move direction and specifies display shift. These operations are performed during data write and read .	37μs
Display ON/OFF	0	0	0	0	0	0	1	D	C	B	D=1 : entire display on C=1 : cursor on B=1 : cursor position on	37μs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	x	x	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	37μs
Function Set	0	0	0	0	1	DL	N	F	x	x	DL: interface data is 8/4 bits NL: number of line is 2/1 F: font size is 5×11/5×8	37μs
Set CGRAM Address	0	0	0	1	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set CGRAM address in address counter.	37μs
Set DDRAM Address	0	0	1	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Set DDRAM address in address counter.	37μs

Read Busy Flag and Address	0	1	BF	AC 6	AC 5	AC 4	AC 3	AC 2	AC 1	AC 0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 μ s
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37 μ s
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37 μ s

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80 μ s.. Do not keep "E" always "High" for checking BF.

Refer to Instruction Table for the list of each instruction execution time.

2.5 Character Pattern

NO.7066-0A

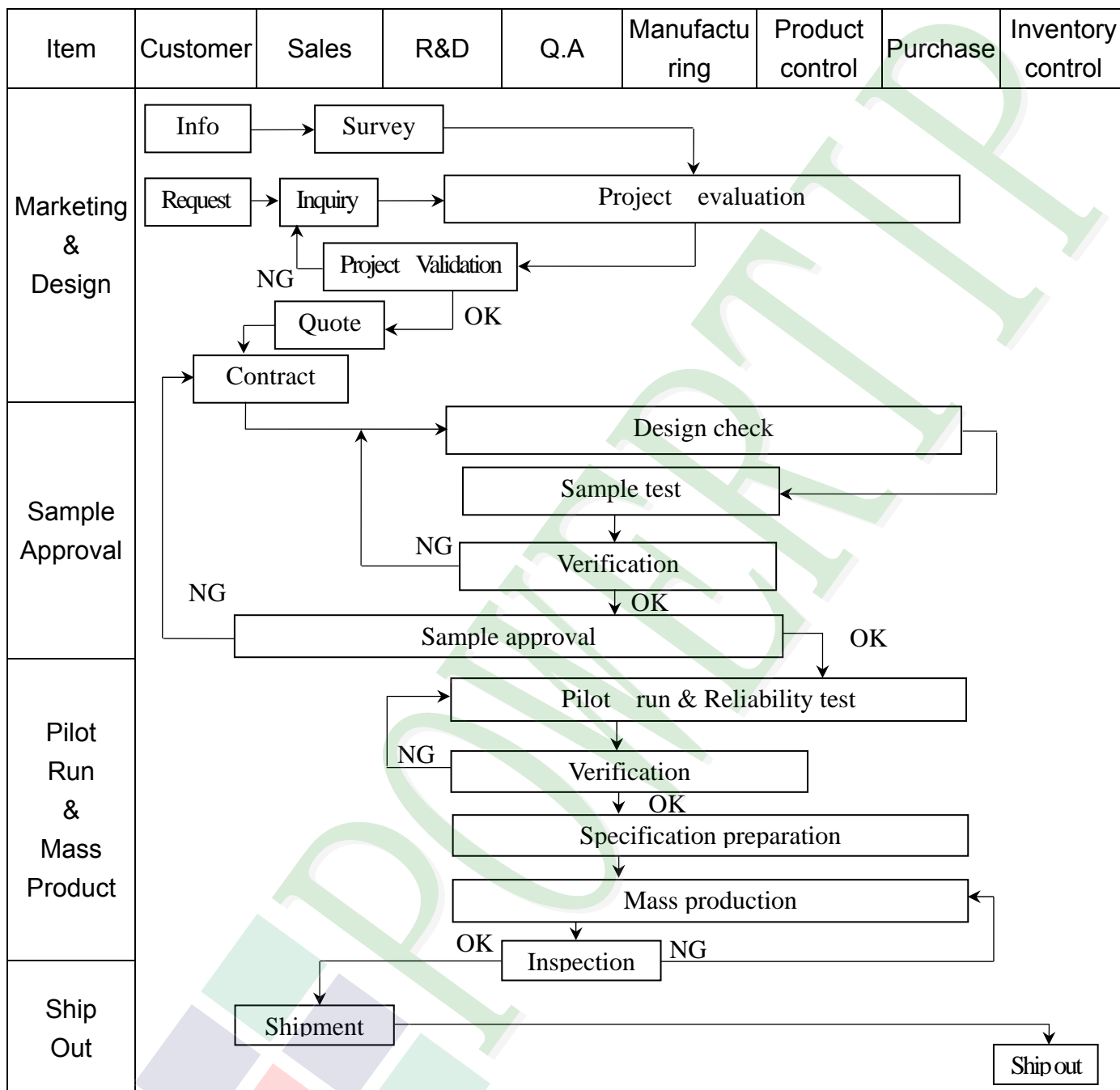
b7-b4 b3-b0	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
0000	CG RAM (1)			0	1	2	3	4	5	6	7	8	9	A	B	C
0001	(2)	.	!	1	A	0	a	9			.	7	*	6	3	4
0010	(3)	"	"	2	B	R	b	r			"	4	9	X	P	0
0011	(4)	#	#	3	O	S	o	s			#	0	7	E	8	0
0100	(5)	\$	\$	4	D	T	d	t			\$	1	k	P	u	0
0101	(6)	%	%	5	E	U	e	u			%	*	*	1	0	0
0110	(7)	&	&	6	F	V	f	v			&	0	2	0	P	0
0111	(8)	'	'	7	G	W	g	w			'	*	*	7	g	0
1000	(1)	((8	H	X	h	x			(0	*	U	r	0
1001	(2)))	9	I	Y	i	y)	7	U	U	Y	0
1010	(3)	*	*	0	J	Z	j	z			*	0	n	V	j	0
1011	(4)	+	+	1	K	C	k	c			+	9	E	0	*	0
1100	(5)	,	,	2	L	*	l	*			,	0	7	7	0	0
1101	(6)	=	=	3	M	I	m	i			=	0	0	U	0	0
1110	(7)	>	>	4	N	^	n	^			>	0	0	0	0	0
1111	(8)	/	/	5	O	_	o	_			/	0	0	0	0	0

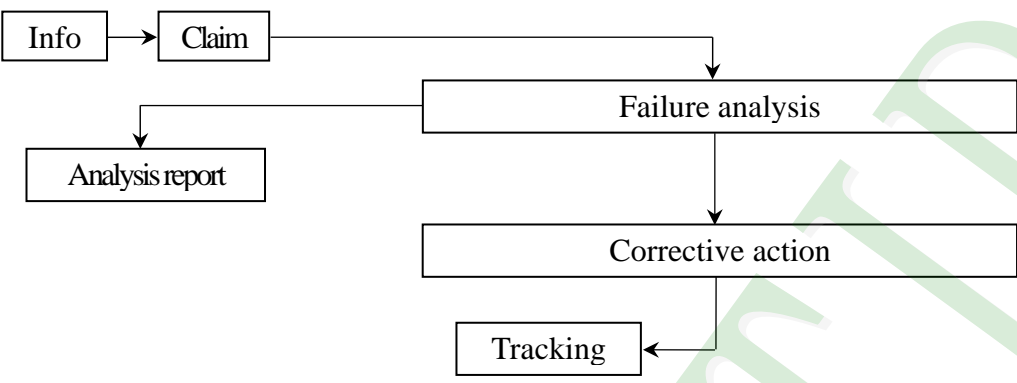
2.6 JUMPER (Setting different use)

J1,J3,J5

3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart



Item	Customer	Sales	R&D	Q.A	Manufacturing	Product control	Purchase	Inventory control
Sales Service	 <pre> graph TD Info[Info] --> Claim[Claim] Claim --> Failure[Failure analysis] Failure --> Report[Analysis report] Failure --> Action[Corrective action] Action --> Tracking[Tracking] </pre>							
Q.A Activity	1. ISO 9001 Maintenance Activities 3. Equipment calibration 5. Standardization Management				2. Process improvement proposal 4. Education And Training Activities			

3.2 Inspection Specification

◆ Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).

◆ Inspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level II .

◆ Equipment : Gauge 、 MIL-STD 、 Powertip Tester 、 Sample

◆ Defect Level : Major Defect AQL : 0.4 ; Minor Defect : AQL : 1.5 .

◆ OUT Going Defect Level : Sampling .

◆ Manner of appearance test :

(1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.

(2). Standard of inspection : (Unit : mm)

(3). The test direction is base on about around 45° of vertical line. (Fig. 1)

(4). Definition of area . (Fig. 2)

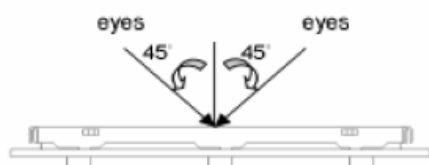


Fig.1

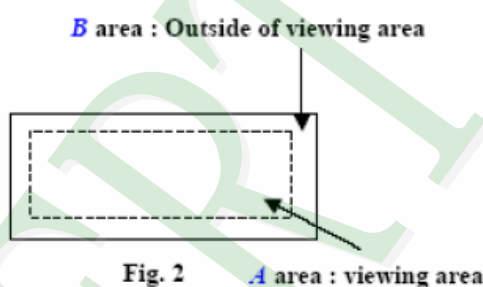
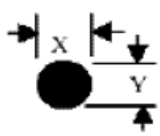
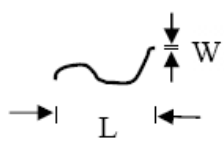
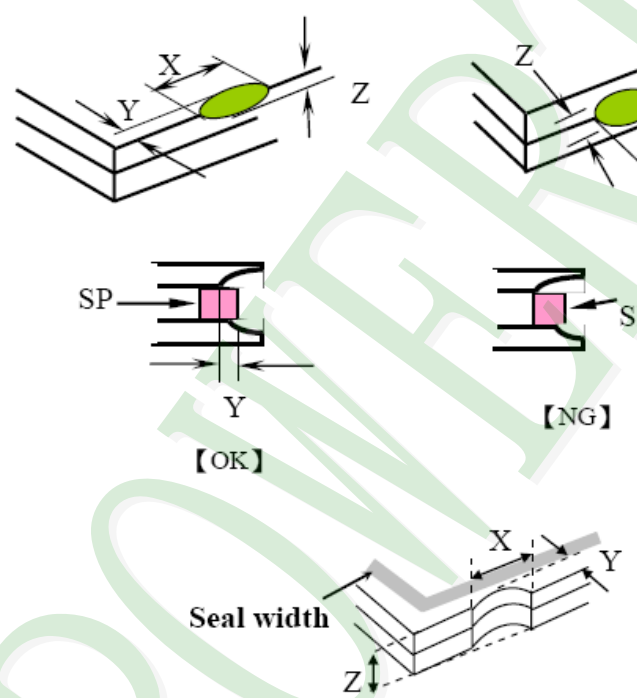


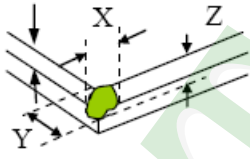
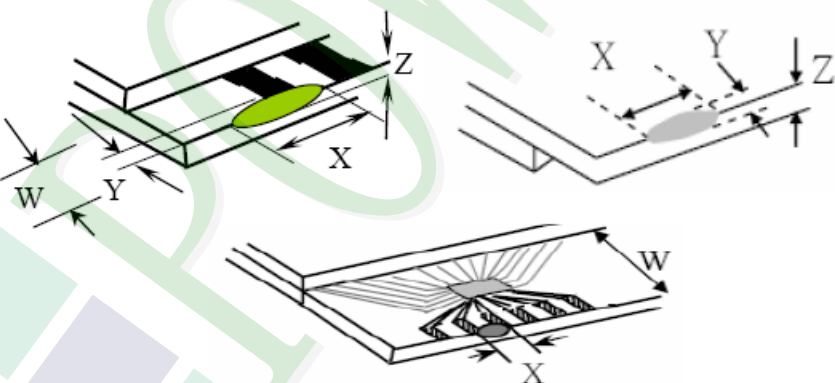
Fig. 2

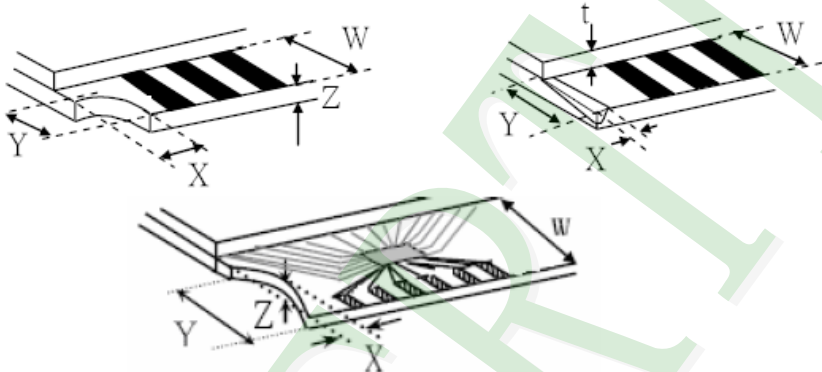
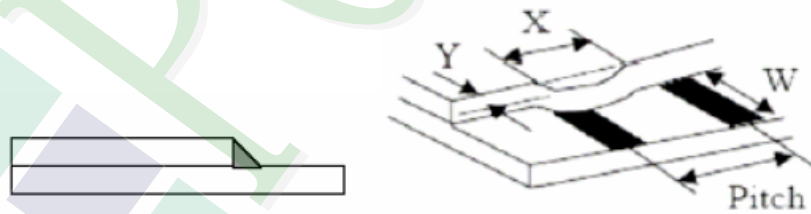
◆ Specification:

NO	Item	Criterion	Level
01	Product condition	1. 1 The part number is inconsistent with work order of Production.	Major
		1. 2 Mixed production types.	Major
		1. 3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3. 1 Product dimension and structure must conform to Structure diagram.	Major
04	Electrical Testing	4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
		4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major

NO	Item	Criterion	Level																																					
05	<p>Black or white dot、scratch、contamination</p> <p>Round type</p>  <p>$\Phi = (x+y)/2$</p> <p>Line type</p> 	<p>5. 1 Round type:</p> <p>5. 1. 1 display only :</p> <ul style="list-style-type: none"> • White and black spots on display ≤ 0.30 mm , no more than 4 white or black spots present. • Densely spaced : NO more than two spots or lines within 3 mm. <p>5. 1. 2 Non-display :</p> <table border="1"> <thead> <tr> <th rowspan="2">Dimension (diameter : Φ)</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td colspan="2">Accept no dense</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.20$</td> <td>3</td> <td rowspan="2">Ignore</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.30$</td> <td>2</td> </tr> <tr> <td>Total quantity</td> <td colspan="2">4</td> </tr> </tbody> </table> <p>5. 1. 3 Line type:</p> <table border="1"> <thead> <tr> <th colspan="2">Dimension</th> <th colspan="2">Acceptance (Q'ty)</th> </tr> <tr> <th>Length (L)</th> <th>Width (W)</th> <th>A area</th> <th>B area</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.03$</td> <td>Accept no dense</td> <td rowspan="3">Ignore</td> </tr> <tr> <td>$L \leq 3.0$</td> <td>$0.03 < W \leq 0.05$</td> <td rowspan="2">4</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.05 < W \leq 0.075$</td> </tr> <tr> <td>---</td> <td>$W > 0.075$</td> <td colspan="2">As round type</td> </tr> </tbody> </table>	Dimension (diameter : Φ)	Acceptance (Q'ty)		A area	B area	$\Phi \leq 0.10$	Accept no dense		$0.10 < \Phi \leq 0.20$	3	Ignore	$0.20 < \Phi \leq 0.30$	2	Total quantity	4		Dimension		Acceptance (Q'ty)		Length (L)	Width (W)	A area	B area	---	$W \leq 0.03$	Accept no dense	Ignore	$L \leq 3.0$	$0.03 < W \leq 0.05$	4	$L \leq 2.5$	$0.05 < W \leq 0.075$	---	$W > 0.075$	As round type		Minor
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NO	Item	Criterion	Level									
07	The crack of glass	<p>Symbols :</p> <p>X : The length of crack Y : The width of crack. Z : The thickness of crack W : terminal length t : The thickness of glass a : LCD side length</p> <hr/> <p>7.1 General glass chip :</p> <p>7.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="502 1444 1300 1736"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq a$</td> <td>Crack can't enter viewing area</td> <td>$\leq 1/2 t$</td> </tr> <tr> <td>$\leq a$</td> <td>Crack can't exceed the half of SP width.</td> <td>$1/2 t < Z \leq 2 t$</td> </tr> </tbody> </table>	X	Y	Z	$\leq a$	Crack can't enter viewing area	$\leq 1/2 t$	$\leq a$	Crack can't exceed the half of SP width.	$1/2 t < Z \leq 2 t$	Minor
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<p>7.2 Protrusion over terminal :</p> <p>7.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="466 1635 1252 1809"> <thead> <tr> <th></th> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>Front</td> <td>$\leq a$</td> <td>$\leq 1/2 W$</td> <td>$\leq t$</td> </tr> <tr> <td>Back</td> <td colspan="3">Neglect</td> </tr> </tbody> </table>		X	Y	Z	Front	$\leq a$	$\leq 1/2 W$	$\leq t$	Back	Neglect			
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		<p>7.2.2 Non-conductive portion :</p>  <table border="1" data-bbox="582 996 1204 1153"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq 1/3 a$</td> <td>$\leq W$</td> <td>$\leq t$</td> </tr> </tbody> </table> <p>⊙ If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>7.2.3 Glass remain :</p>  <table border="1" data-bbox="502 1668 1189 1814"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> </tr> </thead> <tbody> <tr> <td>$\leq a$</td> <td>$\leq 1/3 W$</td> <td>$\leq t$</td> </tr> </tbody> </table>		X	Y	Z	$\leq 1/3 a$	$\leq W$	$\leq t$	X	Y	Z
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X	Y	Z										
$\leq a$	$\leq 1/3 W$	$\leq t$										

NO	Item	Criterion	Level
08	Backlight elements	8. 1 Backlight can't work normally.	Major
		8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
09	General appearance	9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
		9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤ 1.5 mm.	Minor

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes , please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm 10^{\circ}\text{C}$ and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

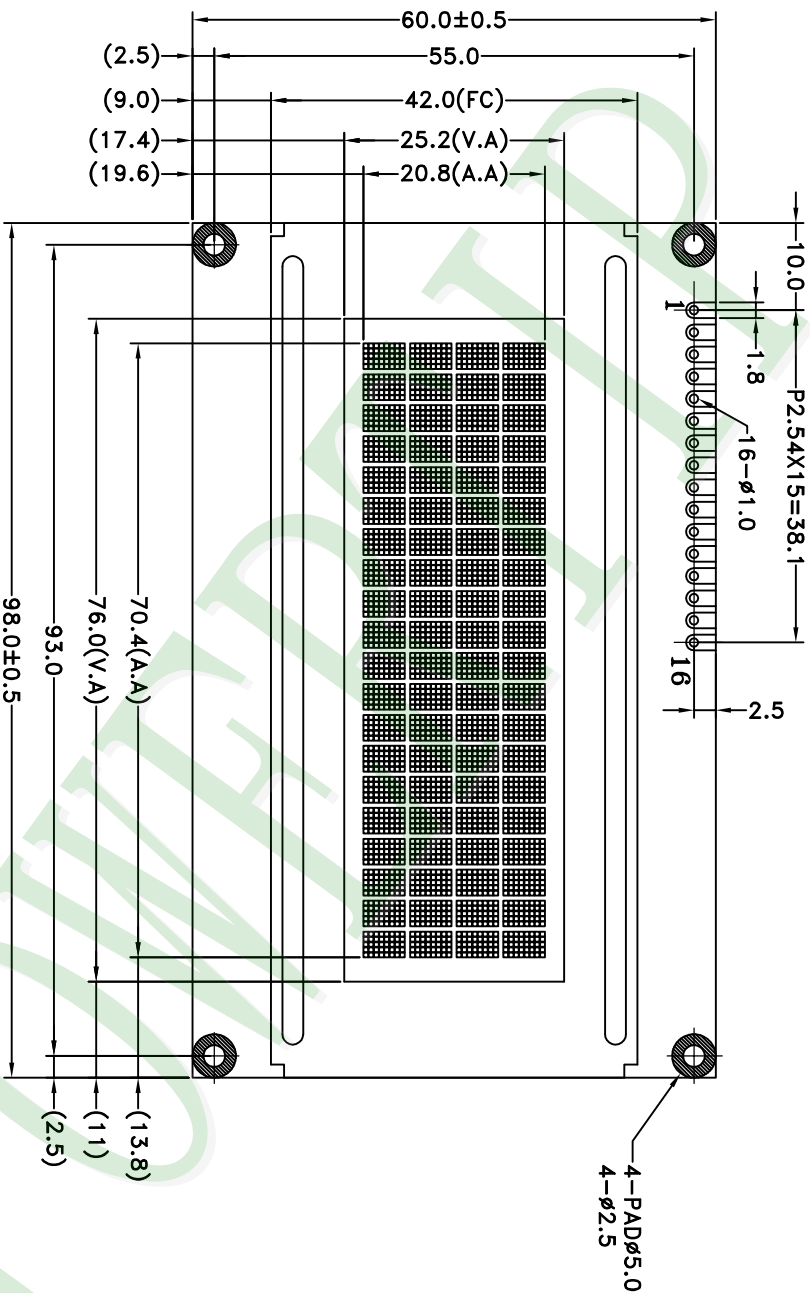
5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}\text{C} \pm 5^{\circ}\text{C}$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

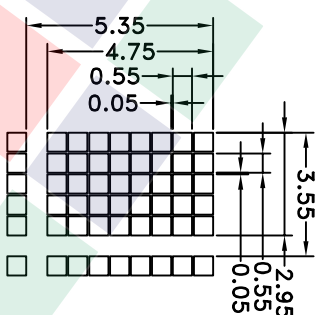
5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period
The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility
This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment , we cannot take responsibility if the product is used in nuclear power control equipment , aerospace equipment , fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

A B C D E F G H



- NOTES:
- 1.LCD TYPE: PBT,Negative/Transmissive
 - 2.Top=-20°C~70°C, Tst=-30°C~80°C
 - 3.VIEW DIRECTION: 6 O'CLOCK
 - 4.THE TOLERANCE UNLESS CLASSIFIED ±0.3
 - 5.THIS PRODUCT CONFORMS ROHS



007		PART NO:	NPC2004WRP-AWA-I		久正光电股份有限公司 POWER TIP TECHNOLOGY CORPORATION	Design	Sally	(3)	Surface		公称厚度 最小厚度 精度
006		DRAWING NAME:	JLMD-NPC2004WRP-AWA-I			Check	Terry		Unit	MMI	
005		TITLE:	LCD Module Drawing		Approve	Ryan	Scale	1:1	Thickness	4 ~ 16	
004							Page	1/1	Quantity	63 ~ 250	
003										250 ~ 1000	
002											
001	NEW DRAWING	REV BY	Sally	2015/07/02							
REV			REVISER	DATE							

1. 包裝材料規格表 (Packaging Material) : (per carton)

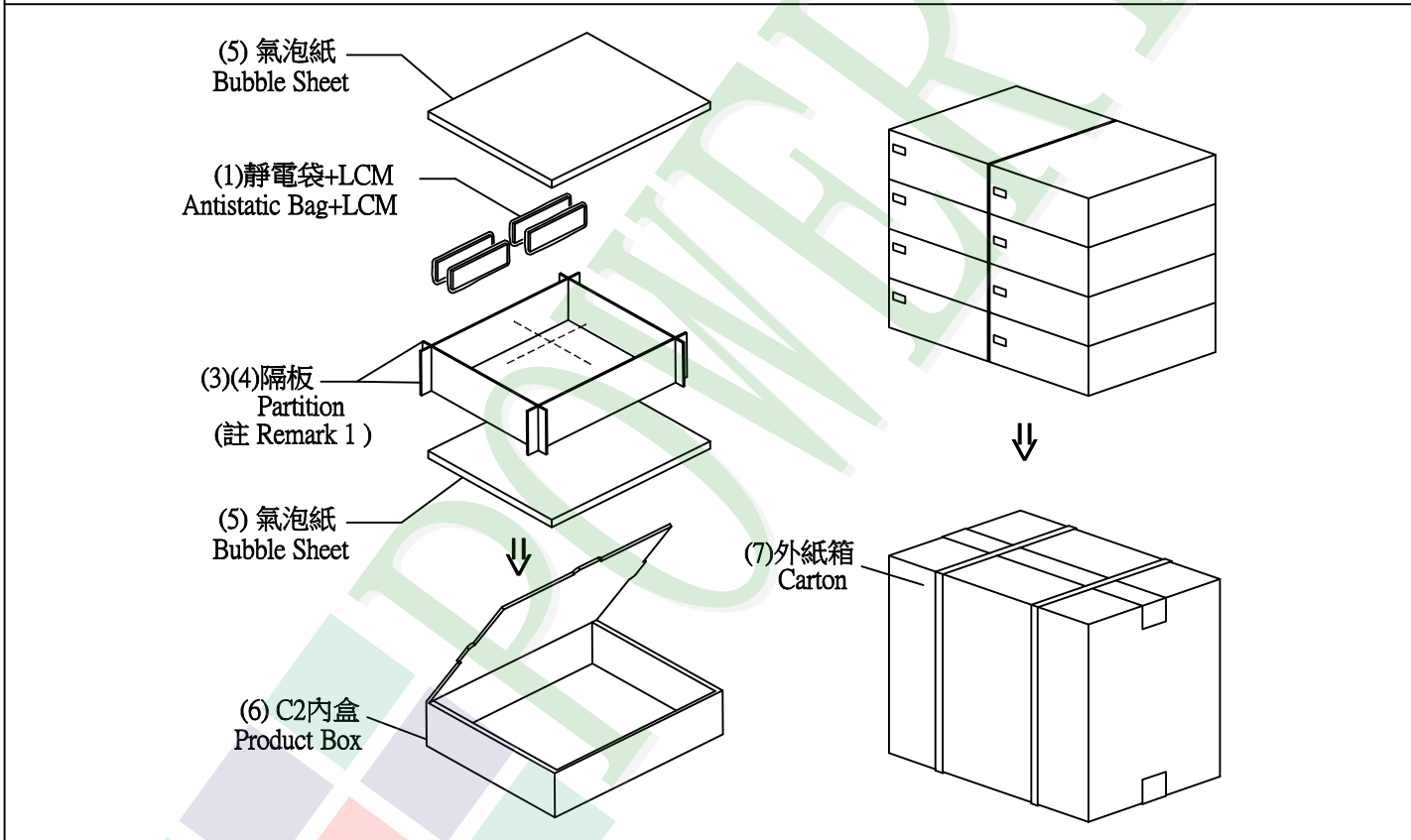
No.	Item	Model	Dimensions (mm)	1Pcs Weight	Quantity	Total Weight
1	成品 (LCM)	NPC2004WRP-AWA-I	98.0 X 60.0 X 12.8	0.0714	192	13.7088
2	靜電袋(1)Antistatic Bag	BAG150120ARABA	150 X 120	0.0018	192	0.3456
3	A2-1隔板(3)A2-1 Partition	BX29500072BZBA	295 X 72 X 3.0	0.0109	104	1.1336
4	B2-1隔板(4)B2-1 Partition	BX24500072BZBA	245 X 72 X 3.0	0.0094	24	0.2256
5	氣泡紙(5)Bubble Sheet	BAG280240BWABA	280 X 240	0.006	16	0.096
6	C2內盒(6)Product Box	BX31025580AABA	310 X 255 X 86	0.16	8	1.28
7	外紙箱(7)Carton	BX52732536CCBA	527 X 325 X 360	0.83	1	0.83
8						
9						

2. 一整箱總重量 (Total LCD Weight in carton) : 17.62 Kg±10%

3. 單箱數量規格表 (Packaging Specifications and Quantity) :

(1)Quantity Of Spacer : A2-1隔板 X 13 , B2-1隔板 X 3

(2)Total LCM quantity in carton : quantity per box 24 x no of boxes 8 = 192



特 記 事 項 (REMARK)

1. LCM排放示意圖(前後間隔不放置):

1. LCM placed as figure showing:
(First and last slot should be empty)

▨ 模組(LCM) X 1pcs.