TITLE:

HV460FH2-601 Product Specification

BEIJING BOE DISPLAY TECHNOLOGY

REVISION HISTORY

REV.	ECN NO.	DESCRIPTION OF CHANGES	DATE	PREPARED
P0	-	Initial Release	2013.07.22	Wang Jianming

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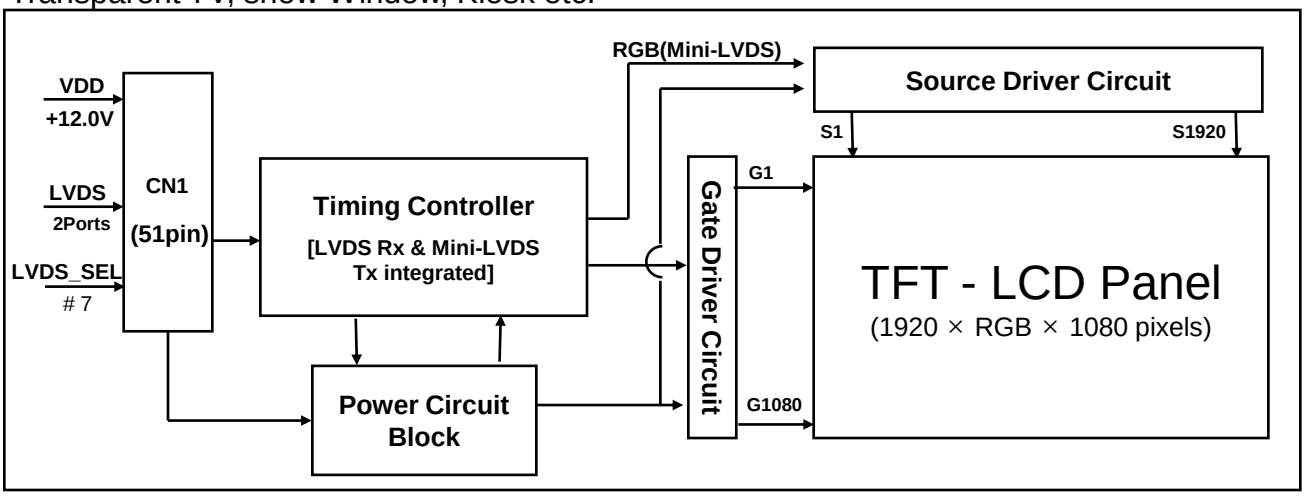
1.0 GENERAL DESCRIPTION

1.1 Introduction

HV460WU2-601 is a color active matrix TFT LCD module using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This module has a 46.00 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel is intended to support applications to provide an excellent performance for Flat Panel Display, such as Transparent TV, show Window, Kiosk etc.



1.2 Features

- LVDS interface with 2 pixel / clock
- High-speed response
- Low color shift image quality
- 8-bit color depth, display 16.7M colors
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only mode
- ADS technology is applied for high display quality
- RoHS compliant

1.3 Application Information for DID (Digital Information Display)

A long-term display like DID application may cause uneven display including image retention. To optimize module's lifetime and function, several operating usages are required.

1. Normal operating condition

– Temperature: 20 ± 15℃

– Humidity: 55 ± 20%

– Display pattern: moving picture or regular switchover display

Note) Long-term static information image may cause uneven display.

2. Operating usages under abnormal operating condition.

a. Ambient condition

– Well-ventilated place is recommended to set up DID system.

b. Power off and screen saver

– Periodical power-off or screen saver is needed after long-term static display.

3. Operating usages to protect uneven display due to long-term static information display

a. Suitable operating time for E-DID: under 20 hours a day

b. Periodical display contents change from static image to moving picture

– Liquid crystal refresh time required

c. Periodical background color and character (image) color change.

– Use different colors for background and character (image), respectively.

– Change colors periodically

d. Avoid combination of background and character with large different luminance.

4. Lifetime in this spec is guaranteed only when DID is used under right operating usages.

1.4 General Specification

< Table 1. General Specifications >

Parameter	Specification	Unit	Remark
Active area	1018.08(H) × 572.67(V)	mm	
Number of pixels	1920(H) ×1080(V)	pixels	
Pixel pitch	176.75(H) ×RGB×530.25(V)	㎛	
Pixel arrangement	Pixels RGB Vertical stripe		
Display colors	16.7M(8bits-true)	colors	
Display mode	Transmission mode, Normally Black		
Module Transmittance	9.0 (typ.)	%	Center point
Weight	4200	gram	
Power Consumption	9.0(Typ.)	Watt	

2.0 ABSOLUTE MAXIMUM RATINGS

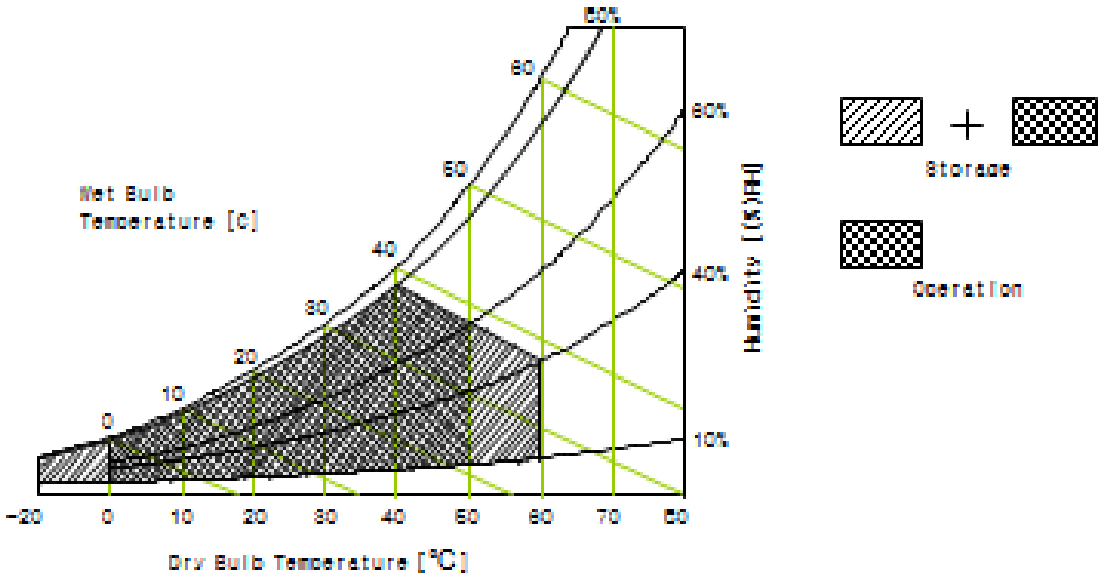
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Module Electrical Specifications >

[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remark
Power Supply Voltage	VDD	VSS-0.3	13.5	V	Ta = 25 ℃
Operating Temperature	T_{OP}	0	+50	℃	Note 1
	T_{SUR}	0	+60	℃	
Storage Temperature	T_{ST}	-20	+60	℃	
Operating Ambient Humidity	Hop	10	80	%RH	
Storage Humidity	Hst	10	80	%RH	

Note 1 : Temperature and relative humidity range are shown in the figure below.
Wet bulb temperature should be 39 ℃ max. and no condensation of water.



3.0 ELECTRICAL SPECIFICATIONS

3.1 TFT LCD Module

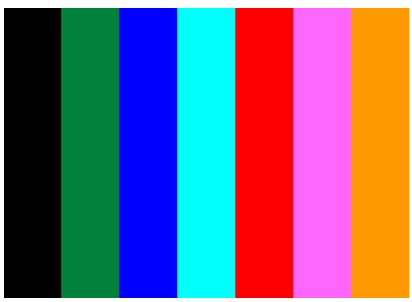
< Table 3. Module Electrical Specifications >

[Ta =25±2 ℃]

Parameter		Symbol	Values			Unit	Remark
			Min	Typ	Max		
Power Supply Input Voltage		VDD	10.8	12	13.2	Vdc	
Power Supply Ripple Voltage		VRP			300	mV	
Power Supply Current		IDD	-	750	850	mA	Note 1
Power Consumption		PDD		9.0	10.2	Watt	
Rush current		IRUSH	-	-	3.0	A	Note 2
LVDS Interface	Differential Input High Threshold Voltage	VLVTH	+100		+300	mV	
	Differential Input Low Threshold Voltage	VLVTL	-300		-100	mV	
	Common Input Voltage	VLVC	1.0	1.2	1.4	V	
CMOS Interface	Input High Threshold Voltage	VIH	2.7	-	3.3	V	
	Input Low Threshold Voltage	VIL	0	-	0.6	V	

Note 1 : The supply voltage is measured and specified at the interface connector of LCM.
The current draw and power consumption specified is for VDD=12.0V,
Frame rate f_V=60Hz and Clock frequency = 75.4MHz.
Test Pattern of power supply current

a) Typ : Color Test (L0/L255)



b) Max : Horizontal 1 Line (L0/L255)

Note 2 : The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

4.0 INTERFACE CONNECTION

4.1 Module Input Signal & Power

- Connector : IS050-C51B-C39-S (UJU) / FI-RE51S-HF-R1500 (JAE) or Equivalent.

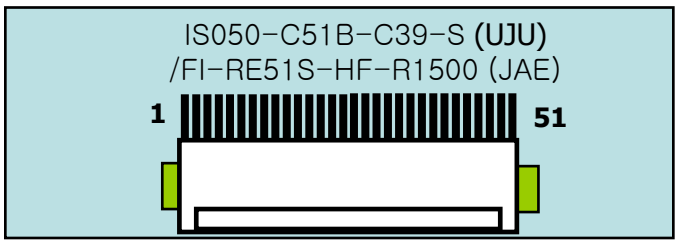
< Table 4. Module Input Connector Pin Configuration >

Pin No	Symbol	Description	Pin No	Symbol	Description
1	NC	No Connection	21	GND	Ground
2	SDA	I^2C Data	22	CH1[3]-	First pixel negative LVDS differential data input. Pair3
3	SCL	I^2C Clock	23	CH1[3]+	First pixel positive LVDS differential data input. Pair3
4	NC	Not Connected	24	CH1[4]-/NC	First pixel negative LVDS differential data input. Pair4
5	NC	Not Connected	25	CH1[4]+/NC	First pixel positive LVDS differential data input. Pair4
6	NC	Not Connected	26	NC	Not Connected
7	SELLVDS	High: JEIDA Low or Open: NS	27	NC	Not Connected
8	NC	Not Connected	28	CH2[0]-	Second pixel negative LVDS differential data input. Pair0
9	NC	Not Connected	29	CH2[0]+	Second pixel positive LVDS differential data input. Pair0
10	NC	Not Connected	30	CH2[1]-	Second pixel negative LVDS differential data input. Pair1
11	GND	Ground	31	CH2[1]+	Second pixel positive LVDS differential data input. Pair1
12	CH1[0]-	First pixel negative LVDS differential data input. Pair0	32	CH2[2]-	Second pixel negative LVDS differential data input. Pair2
13	CH1[0]+	First pixel positive LVDS differential data input. Pair0	33	CH2[2]+	Second pixel positive LVDS differential data input. Pair2
14	CH1[1]-	First pixel negative LVDS differential data input. Pair1	34	GND	Ground
15	CH1[1]+	First pixel positive LVDS differential data input. Pair1	35	CH2CLK-	First pixel negative LVDS clock
16	CH1[2]-	First pixel negative LVDS differential data input. Pair2	36	CH2CLK+	First pixel positive LVDS clock
17	CH1[2]+	First pixel positive LVDS differential data input. Pair2	37	GND	Ground
18	GND	Ground	38	CH2[3]-	Second pixel negative LVDS differential data input. Pair3
19	CH1CLK-	First pixel negative LVDS clock	39	CH2[3]+	Second pixel positive LVDS differential data input. Pair3
20	CH1CLK+	First pixel positive LVDS clock			

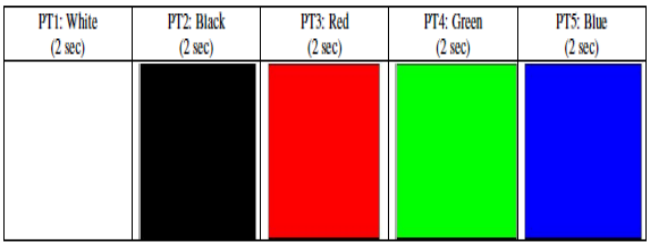
Pin No	Symbol	Description	Pin No	Symbol	Description
40	CH2[4]-/NC	Second pixel negative LVDS differential data input. Pair4	46	GND	Ground
41	CH2[4]+/NC	Second pixel positive LVDS differential data input. Pair4	47	NC	Not Connected
42	NC	Not Connected	48	VCC	Input Voltage
43	NC	Not Connected	49	VCC	Input Voltage
44	GND	Ground	50	VCC	Input Voltage
45	GND	Ground	51	VCC	Input Voltage

Notes : 1. NC(Not Connected) : This pins are only used for BOE internal operations.
2. Input Level of LVDS signal is based on the IEA 664 Standard.
3. LVDS_SEL : This pin is used for selecting LVDS signal data format.
If this Pin : High (3.3V) →JEIDA LVDS format
Otherwise : Low (GND) or Open (NC) → Normal NS LVDS format

Rear view of LCM



BIST Pattern



4.0 INTERFACE CONNECTION

4.2 X-PCBA-LEFT Interface

- Connector : PF050-O68B-C15 (UJU) or Equivalent.

< Table 5. Module Input Connector Pin Configuration >

Pin No	Symbol	Description	Pin No	Symbol	Description
1	GMEF	GMA REFERANCE	27	NC	Not Connected
2	VCOM_OUT	VCOM	28	NC	Not Connected
3	VCOM_OUT	VCOM	29	GND	Ground
4	HAVDD	HALF AVDD	30	GMA1	GAMMA
5	HAVDD	HALF AVDD	31	GMA2	GAMMA
6	NC	Not Connected	32	GMA3	GAMMA
7	AVDD	AVDD	33	GMA4	GAMMA
8	AVDD	AVDD	34	GMA5	GAMMA
9	NC	Not Connected	35	GMA6	GAMMA
10	DVDD	DVDD	36	GMA7	GAMMA
11	DVDD	DVDD	37	GMA8	GAMMA
12	GND	GROUND	38	GMA9	GAMMA
13	CH1[0]-	First pixel negative mini-LVDS differential data input. Pair0	39	GMA10	GAMMA
14	CH1[0]+	First pixel positive mini-LVDS differential data input. Pair0	40	GMA11	GAMMA
15	CH1[1]-	First pixel negative mini-LVDS differential data input. Pair1	41	GMA12	GAMMA
16	CH1[1]+	First pixel positive mini-LVDS differential data input. Pair1	42	GMA13	GAMMA
17	CH1[2]-	First pixel negative mini-LVDS differential data input. Pair2	43	GMA14	GAMMA
18	CH1[2]+	First pixel positive mini-LVDS differential data input. Pair2	44	GMA15	GAMMA
19	GND	Ground	45	GMA16	GAMMA
20	CH1CLK-	First pixel negative mini-LVDS clock	46	GMA17	GAMMA
21	CH1CLK+	First pixel positive mini-LVDS clock	47	GMA18	GAMMA
22	GND	Ground	48	GND	Ground
23	NC	Not Connected	49	POL	POL
24	NC	Not Connected	50	LD	LOAD
25	SDA_PGMA	I2C for PGMA	51	SHL_R	SHL_R
26	SCL_PGMA	I2C for PGMA	52	PAIR_R	PAIR_R

Pin No	Symbol	Description	Pin No	Symbol	Description
53	GND	Ground	61	XON	XON
54	VON	VON	62	UD_R	UD_R
55	VON	VON	63	STV2_R	STV2_R
56	VOFF	VOFF	64	FEED_L	FEED_L
57	VOFF	VOFF	65	FEED_R	FEED_R
58	STV1_R	STV1_R	66	NC	Not Connected
59	CPV	CPV	67	GND	Ground
60	OE1	OE1	68	GND	Ground

Notes : 1. NC(Not Connected) : This pins are only used for BOE internal operations.
2.Pin map of X-PCB-RIGHT is reverse by this one(X-PCB-LEFT).

4.3 LVDS Interface

- LVDS Receiver : Timing Controller (LVDS Rx merged) / LVDS Data : Pixel Data

< Table 6. Module Input Connector Pin Configuration >

Channel No.	Data No.	8-bit LVDS Type	
		NS	JEIDA
0	Bit-0	R0	R2
	Bit-1	R1	R3
	Bit-2	R2	R4
	Bit-3	R3	R5
	Bit-4	R4	R6
	Bit-5	R5	R7
	Bit-6	G0	G2
1	Bit-0	G1	G3
	Bit-1	G2	G4
	Bit-2	G3	G5
	Bit-3	G4	G6
	Bit-4	G5	G7
	Bit-5	B0	B2
	Bit-6	B1	B3
2	Bit-0	B2	B4
	Bit-1	B3	B5
	Bit-2	B4	B6
	Bit-3	B5	B7
	Bit-4	HS	HS
	Bit-5	VS	VS
	Bit-6	DE	DE
3	Bit-0	R6	R0
	Bit-1	R7	R1
	Bit-2	G6	G0
	Bit-3	G7	G1
	Bit-4	B6	B0
	Bit-5	B7	B1
	Bit-6	-	

5.0 SIGNAL TIMING SPECIFICATION

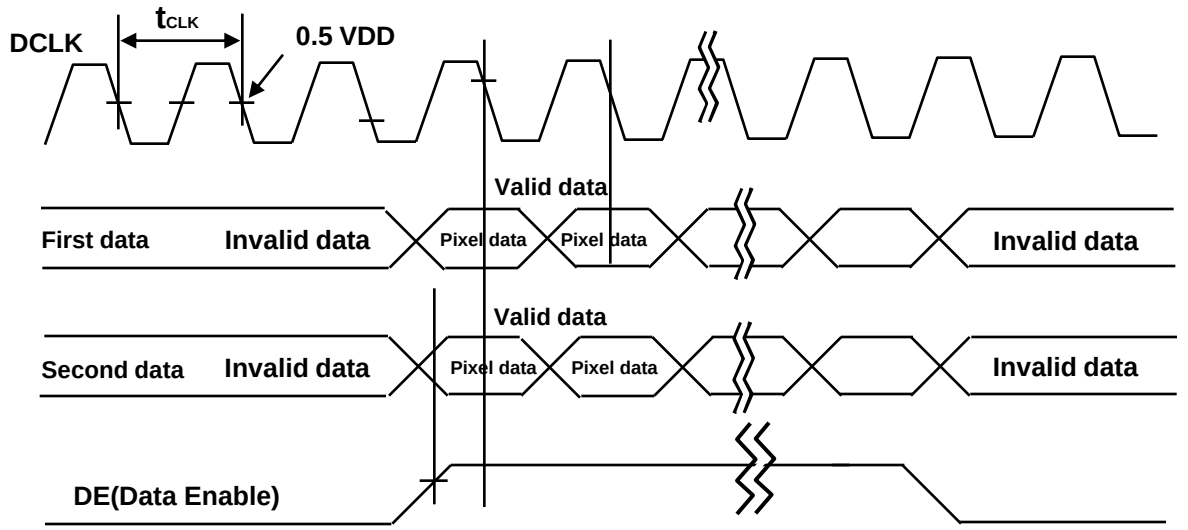
5.1 Timing Parameters (DE only mode)

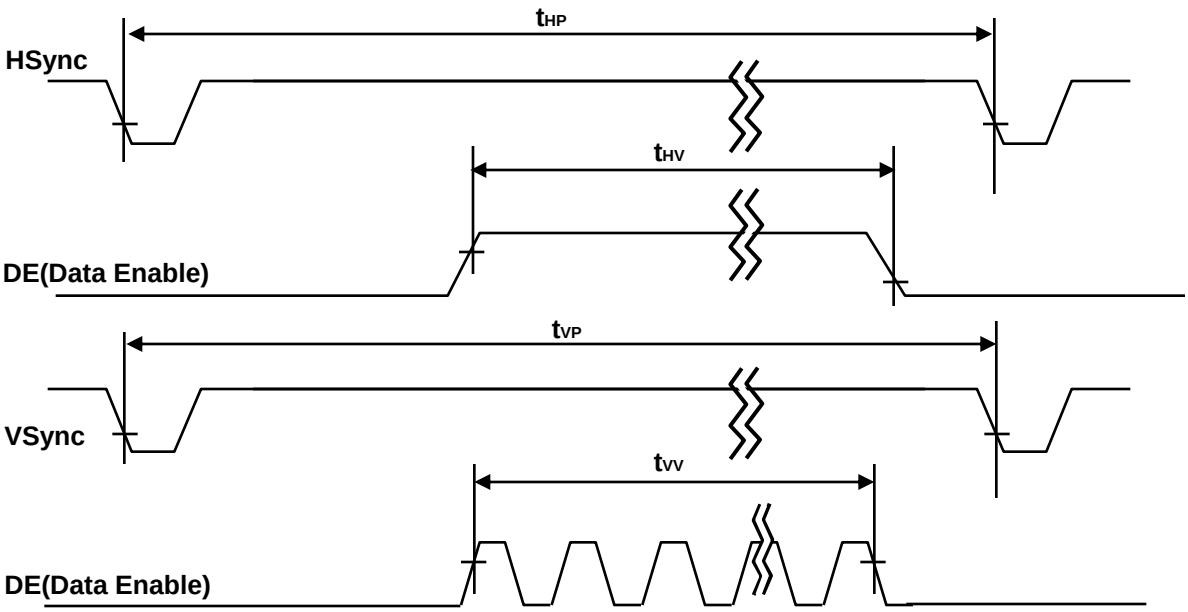
< Table 7. Timing Table >

Item		Symbols	Min	Typ	Max	Unit
Clock	Frequency	1/Tc	66.00	74.25	82	MHz
	High Time	Tch	-	4/7Tc	-	
	Low Time	Tcl	-	4/7Tc	-	
Frame Period		Tv	1116	1126	1150	lines
			56.32	60	62.77	Hz
Vertical Display Period		Tvd	-	1080	-	lines
One line Scanning Period		Th	1050	1100	1150	clocks
Horizontal Display Period		Thd	960	960	960	clocks

Notes: This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

5.2 Signal Timing Waveform



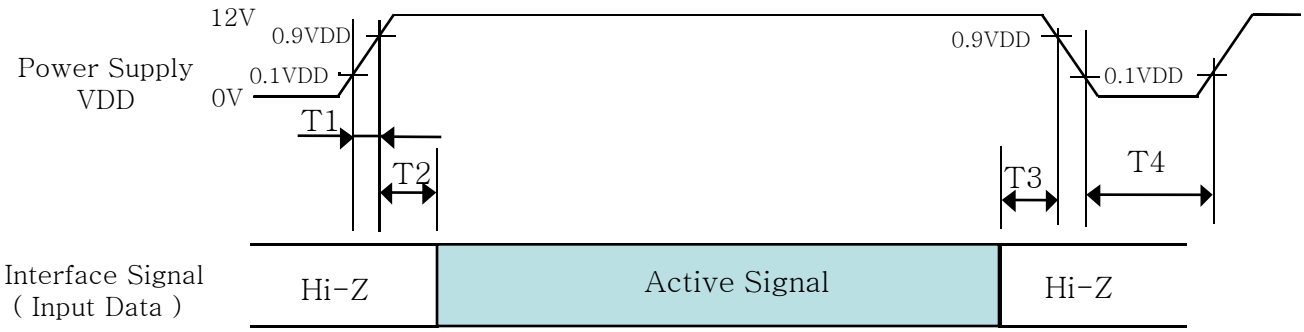


5.3 Input Signals, Basic Display Colors and Gray Scale of Colors

< Table 8. Input Signal and Display Color Table >

Color & Gray Scale		Input Data Signal																							
		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Colors	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	▽	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	▽	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Gray Scale of Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	▽	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Gray Scale of White	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	△	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
	△	↑								↑								↑							
	▽	↓								↓								↓							
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
	▽	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

5.4 Power Sequence



< Table 9. Sequence Table >

Parameter	Values			Units
	Min	Typ	Max	
T1	0.5	-	10	ms
T2	0	-	50	ms
T3	0	-	50	ms
T4	1	-	-	s

Notes: 1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.

6.0 OPTICAL SPECIFICATIONS

The test of optical specifications shall be measured in a dark room (ambient luminance≤1 lux and temperature=25±2℃) with the equipment of Luminance meter system (Goniometer system and PR730) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta_{Ø=0}$ (=θ_3) as the 3 o'clock direction (the "right"), $\theta_{Ø=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{Ø=180}$ (= θ_9) as the 9 o'clock direction ("left") and $\theta_{Ø=270}$(= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or Ø, the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 12.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

< Table 10. Optical Table >

[VDD = 12.0V, Frame rate = 60Hz, Ta =25±2 ℃]

Parameter		Symbol	Condition	Min	Typ	Max	Unit	Remark
Viewing Angle	Horizontal	Θ_3	CR > 10		89		Deg.	Note 1
		Θ_9			89		Deg.	
	Vertical	Θ_{12}			89		Deg.	
		Θ_6			89		Deg.	
Contrast ratio		CR	Θ = 0° (Center) Normal Viewing Angle	900:1	1200:1	-		Note 2
Color Gamut					36		%	
Response Time	G to G	T_g		-	8	10	ms	Note 4
Gamma Scale				1.8	2.2	2.6		
Cell Transmittance					9.0		%	Note 5

Note :

1. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
2. Contrast measurements shall be made at viewing angle of $\theta = 0°$ and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See Figure 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

$$CR = \frac{\text{Luminance when displaying a white raster}}{\text{Luminance when displaying a black raster}}$$

3. The color chromaticity coordinates specified in Table 9.shall be calculated from the spectral data measured with all pixels first in red, green, blue and white. Measurements shall be made at the center of the panel. The BLU is used by BOE.
4. Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV =60Hz to optimize.
   Each time in below table is defined as Figure 2 and shall be measured by switching the

5. Definition of Transmittance (T%) :
   Module is with white(L255) signal input

$$\text{Transmittance} = \frac{\text{Luminance of LCD Module}}{\text{Luminance of BLU}} \times 100\ \%$$

7.0 MECHANICAL CHARACTERISTICS

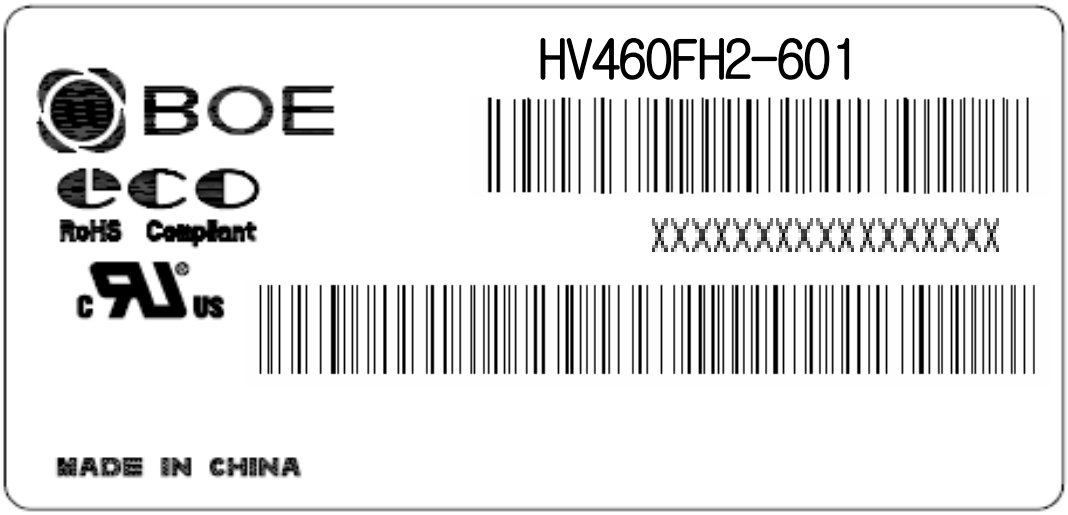
Dimensional Requirements

Figure 3(located in Appendix) shows mechanical outlines for the model HV460WU2-600. Other parameters are shown in Table 10.

< Table 11. Dimensional Parameters >

Parameter	Specification	Unit
Active area	1018.08 (H) ×572.67(V)	mm
Pixel pitch	0.53025(H) ×0.53025(V)	mm
Number of pixels	1920(H) ×1080(V) (1 pixel = R + G + B dots)	pixels
Weight	6100	gram

8.0 PRODCUT SERIAL NUMBER



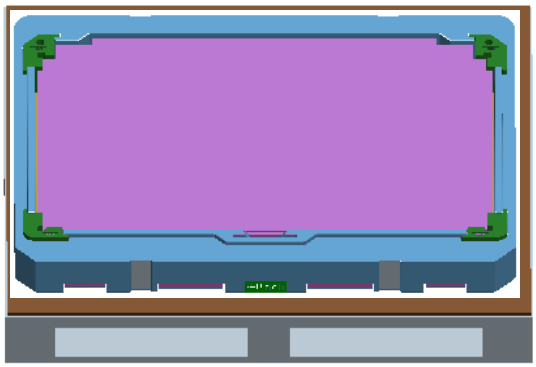
1	2	3	4	5	6	7
X X	X	X	X X	X	X X X X	X X X X X X

1. Control Number
2. Rank / Grade
3. Line Classification
4. Year (2011 : 11, 2012 : 12, …)
5. Month (1,2,3, … , 9, X, Y, Z)
6. Internal Use
7. Serial Number

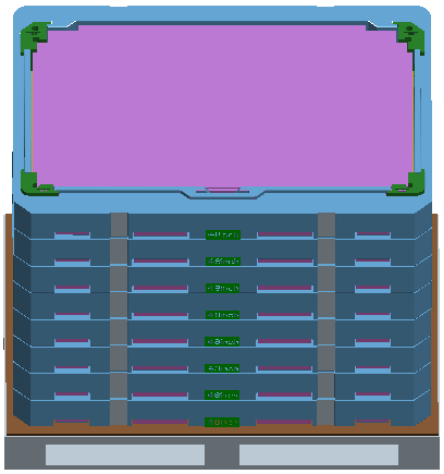
9.0 PACKING INFORMATION

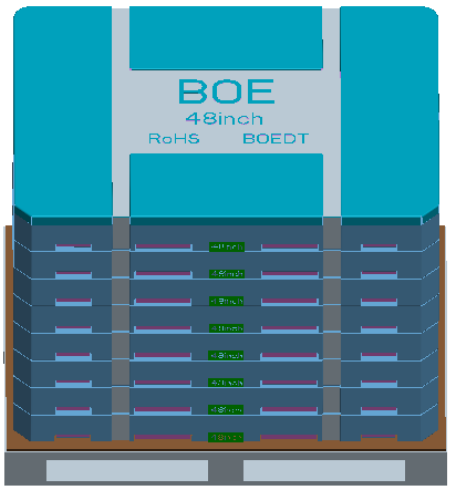
BOE provides the standard shipping container for customers, unless customer specifies their packing information. The standard packing method and Barcode information are shown in below.

9.1 Packing Order

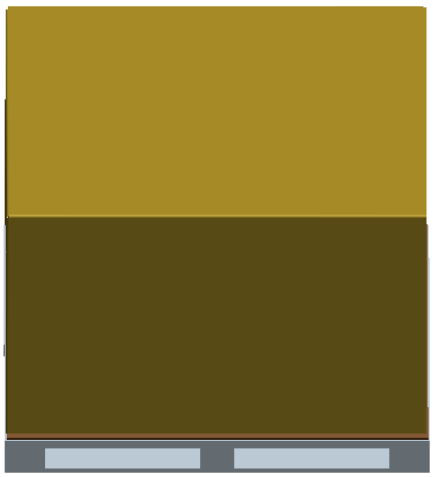


1 Box: 3 Panel +8 EPE Pad + 1 Drier+3 PE Bag





1 Pallet:: 8 Box + 1Cover



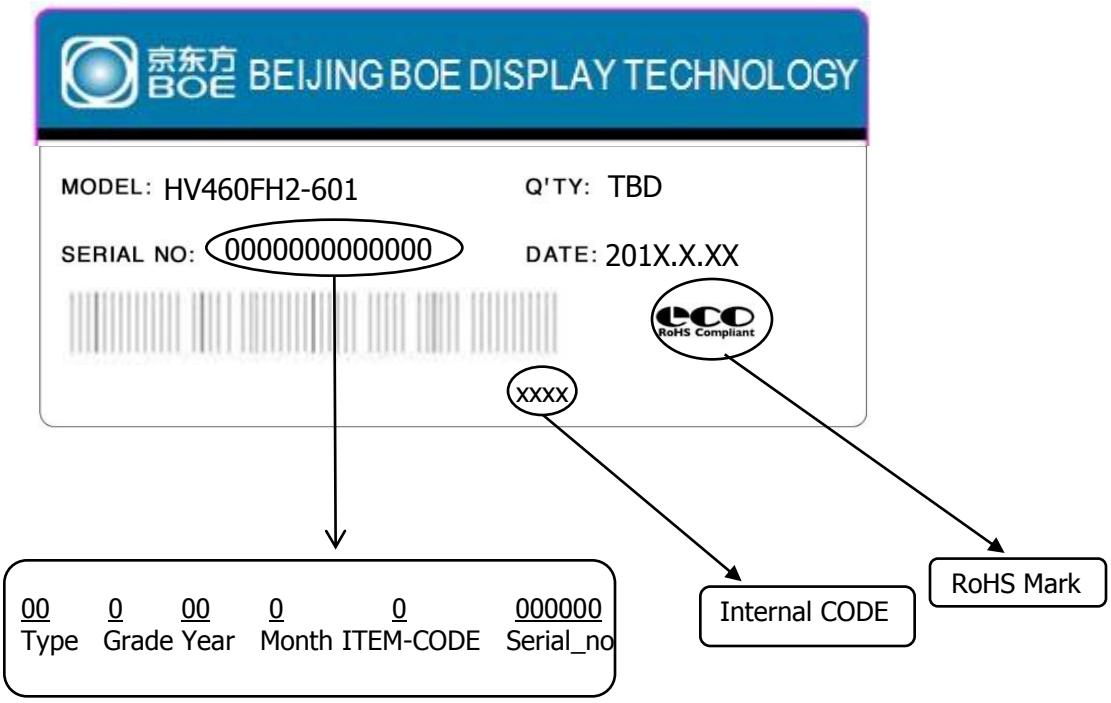
9.2Packing Note

- Box Dimension : 1383mmL×890mmW×132mmH
- Package Quantity in one Box : 3pcs

9.3 Box Label

- Label Size : 110 mm (L) × 55 mm (W)
- Contents

  Model : HV460DH2-601
  Serial No. : Box Serial No. See next page for detail description.
  Date : Packing Date
  FG Code : FG Code of Product



10.0 HANDLING & CAUTIONS

(1) Cautions when taking out the module
- Pick the pouch only, when taking out module from a shipping package.

(2) Cautions for handling the module
- As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
- As the LCD panel and back - light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
- As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
- Do not pull the interface connector in or out while the LCD module is operating.
- Put the module display side down on a flat horizontal plane.
- Handle connectors and cables with care.

(3) Cautions for the operation
- When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.

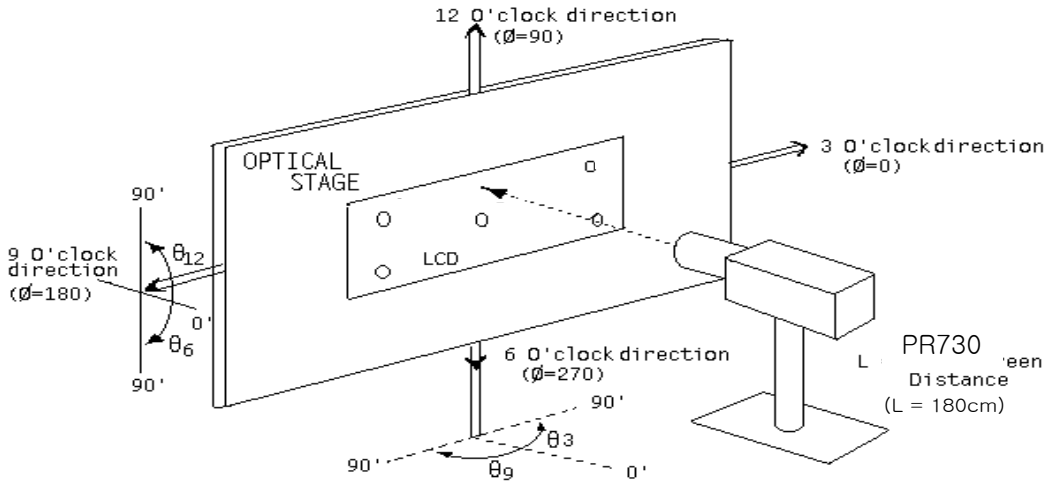
(4) Cautions for the atmosphere
- Dew drop atmosphere should be avoided.
- Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.

(5) Cautions for the module characteristics
- Do not apply fixed pattern data signal to the LCD module at product aging.
- Applying fixed pattern for a long time may cause image sticking.

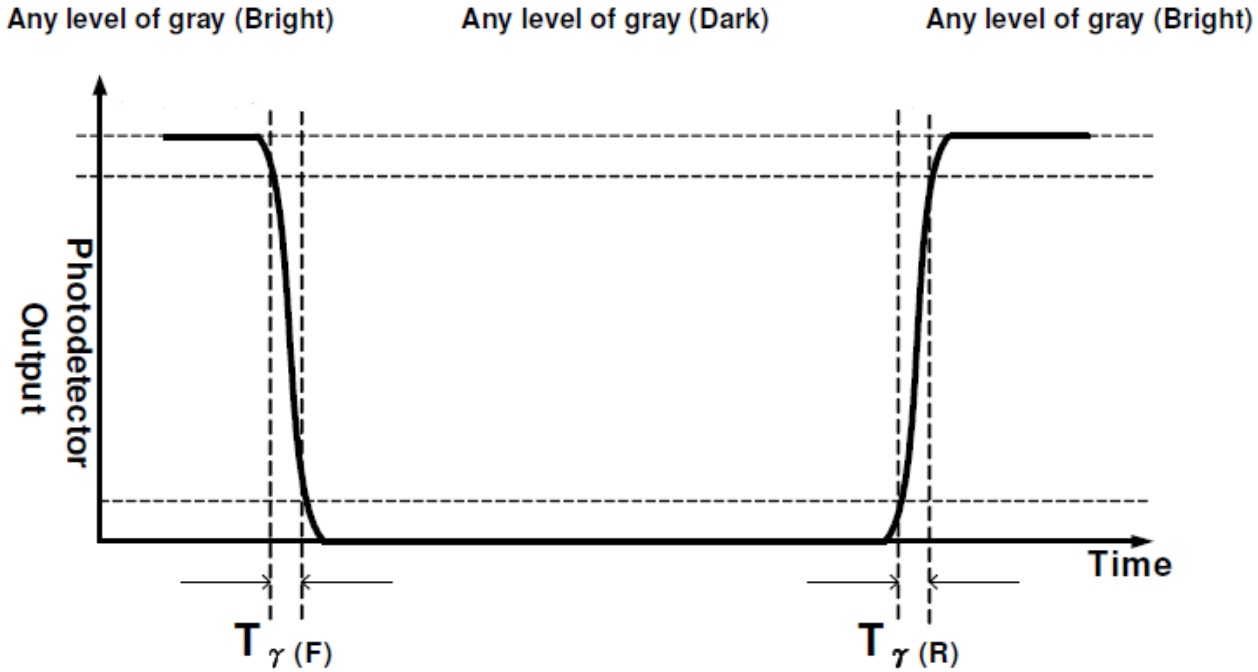
(6) Other cautions
- Do not disassemble and/or re-assemble LCD module.
- Do not re-adjust variable resistor or switch etc.
- When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

11.0 APPENDIX

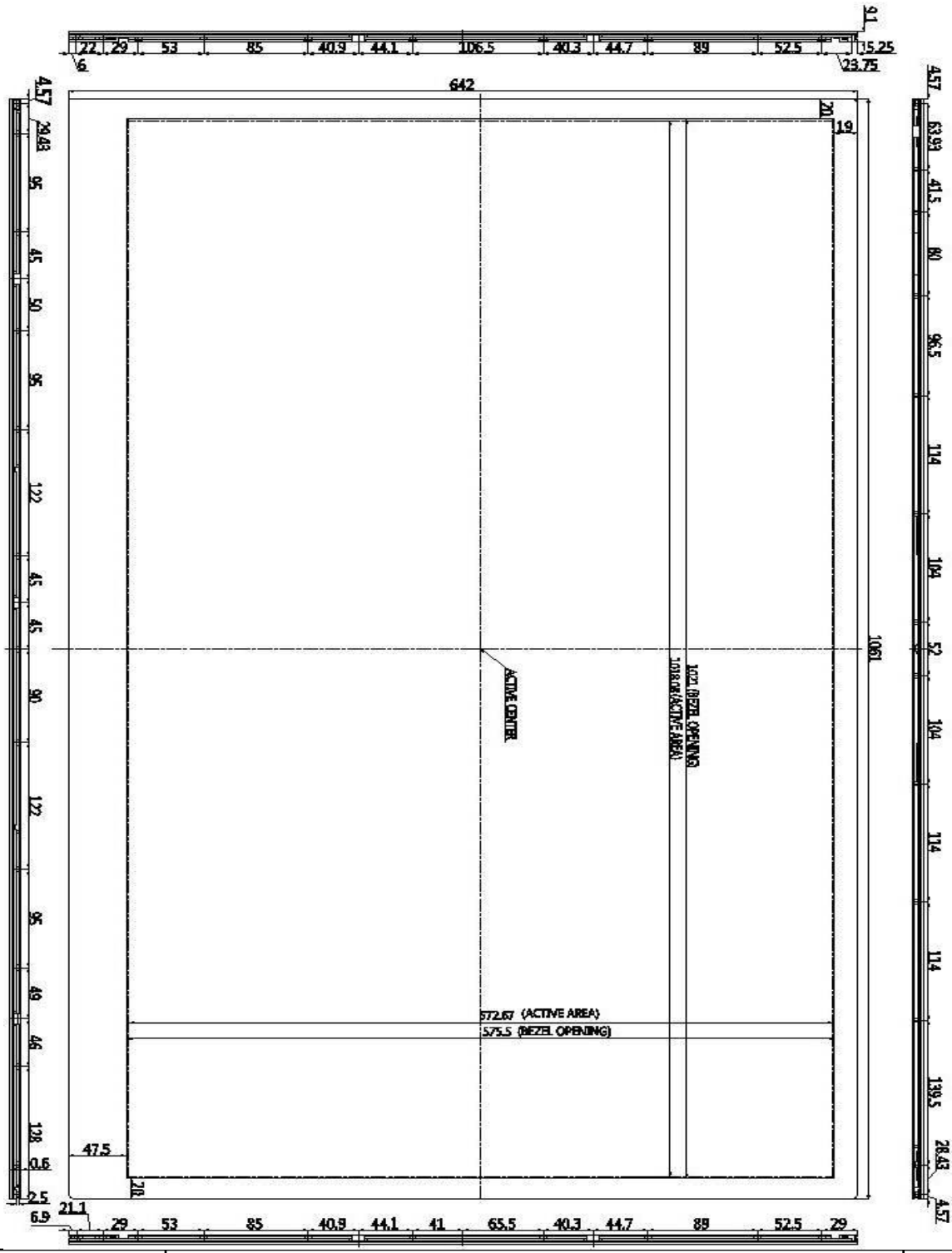
< Figure 1. Measurement Set Up >



< Figure 2. Response Time Testing >



< Figure 3. TFT-LCD Module Outline Dimensions (Front View) >



< Figure 4. TFT-LCD Module Outline Dimensions (Rear View) >