



Table of Contents

1.0	SCOPE	3
2.0	 PRODUCT DESCRIPTION 2.1 Product Name and Series Numbers 2.2 Dimensions, Materials, Plating and Markings 2.3 Safety Agency Approvals 	3 3 3 3
3.0	 APPLICABLE DOCUMENTS AND SPECIFICATIONS 3.1 Molex Documents 3.2 Industry Documents 	4 4 4
4.0	 ELECTRICAL PERFORMANCE RATINGS 4.1 Voltage 4.2 Applicable Wires 4.3 Maximum Current Rating 4.4 Temperature 4.5 Durability 4.6 Current Interruption 	4 5 5 6 7
5.0	QUALIFICATION	7
6.0	PERFORMANCE6.1Electrical Performance6.2Mechanical Performance6.3Environmental Performance	7 7 8 10
7.0	TEST SEQUENCE GROUPS	12
8.0	SOLDER INFORMATION8.1 Solder Process Temperature8.2 Reflow Solder Profile	14 14 14

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	T SPECIFICATION	FOR	SHEET No.				
A5	EC No:UCP2016-0922	ULTRA-F	IT™ WIRE TO BO	ARD	3 of 16				
AJ	DATE: 08/28/2015	CON	CONNECTOR SYSTEM						
DOCUMENT	NUMBER:	CREATED / REVISED BY:	CHECKED BY:	<u>APPROV</u>	/ED BY:				
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1.0 SCOPE

This Product Specification covers Ultra-Fit® 3.50 mm pitch wire to board connector systems with gold and tin plating. Receptacles are terminated with 22 to 16 AWG wire using crimp technology.

2.0 PRODUCT DESCRIPTION

2.1 PRODUCT NAME AND SERIES NUMBER (S)

Table 1 – WIRE-TO-BOARD							
Description	Series Number						
Receptacle Crimp Terminal	172253						
Receptacle Housing, Single Row	172256/172260						
Receptacle Housing, Single Row	172258/172262						
ТРА	172264/172268						
Vertical Header Single Row, Kinked Pins	172286						
Vertical Header Single Row, Solder Clips	172287						
Vertical Header Dual Row, Kinked Pins	172298						
Vertical Header Dual Row, Solder Clips	172299						
Right Angle Header Single Row	172310						
Right Angle Header Dual Row	172316						

2.2 DIMENSIONS, MATERIALS, PLATING AND MARKINGS

Dimensions & Plating: See individual sales drawings. Material: RoHS compliant materials.

2.3 SAFETY AGENCY APPROVALS

2.3.1 UL File Number: E29179

UL (fully loaded) NON-current interruption	Current interruption per UL1977
14 Amps @ 400V (16 AWG wire)	14 Amps @ 48V AC (16 AWG wire)

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	SHEET No.		
A5	EC No: UCP2016-0922		IT [™] WIRE TO BO	_	4 of 16
AJ	<u>DATE:</u> 08/28/2015	CON	Λ	4 01 10	
DOCUMENT	NUMBER:	CREATED / REVISED BY:	CHECKED BY:	<u>APPRO\</u>	/ED BY:
PS	172323-0001	PPEREZ	RHODGE	RHO	DGE

molex®	PRC	DUCT SPEC	FICATION	
2.	3.2 IEC License Nur	nber per IEC / EN 61984: Ti	BD	
		IEC (fully loaded) NON-current interrup		
2.3		mber*: 70022376 (LR19980) eets following standards/test		
		t to CSA signifies that the pro n Canada and US respective		to the applicable CSA and
		CSA (single circu NON-current interru 14 Amps @ 400V (16 AV	ption	
3.0 API				
3.1	MOLEX DOCUMENTS			
	referenced documents ar Ultra-Fit Test Summary T Molex Solderability Spec		ions of this specifications	for the necessary
3.2	INDUSTRY DOCUMENT EIA-364-1000.01 UL-60950-1 UL-1977 CSA STD. C22.2 NO. 18 IEC / EN 61984 USCAR-2 REV.6			
REVISION:	ECR/ECN INFORMATION:			J FOR SHEET No.
A5	<u>EC No:</u> UCP2016-0922	ULTRA-F	T SPECIFICATION IT™ WIRE TO BC	DARD 5 of 16
DOCUMENT	DATE: 08/28/2015	CON CREATED / REVISED BY:	NECTOR SYSTEI CHECKED BY:	APPROVED BY:
	172323-0001	PPEREZ	RHODGE	RHODGE
				FILENAME: PS75431r0.DOC



4.0 ELECTRICAL PERFORMANCE RATINGS

4.1 VOLTAGE *

400 Volts AC (RMS) or 400 Volts DC max.

* This connector voltage meets the connector level provided by the safety agency. For application voltage requirements per UL-60950 or other standards, the creepage & clearance also needs to be determined based upon pads/traces on the PCB.

4.2 APPLICABLE WIRES

Maximum Insulation Diameter and	Stranded copper 16 AWG: 2.39mm MAXIMUM Stranded copper 18 AWG: 2.03mm MAXIMUM
	Stranded copper 20 AWG: 1.78mm MAXIMUM
Applicable Wire Gauges	Stranded copper 22 AWG: 1.57mm MAXIMUM

4.3 MAXIMUM CURRENT RATING

Current rating is application dependent and may be affected by the wire rating such as listed in UL-60950-1. Each application should be evaluated by the end user for compliance to specific safety agency requirements. The ratings listed in the chart below are per Molex test method based on a 30° C maximum temperature rise over ambient temperature and are provided as a guideline. Appropriate de-rating is required based on circuit size, ambient temperature, copper trace size on the PCB, gross heating from adjacent modules/components and other factors that influence connector performance. Wire size, insulation thickness, stranding, tin coated or bare copper, wire length & crimp quality are other factors that influence current rating.

Wire to Board Current Rating (Amp Max.) (Tested with TIN plated terminals)														
			Conn	ector f	ully loa	ded wi	th all c	circuits	power	ed				
AWG Wire		Cir	cuit Si	ze (Sir	ngle Ro	w)			С	ircuit S	ize (D	ual Rov	w)	
Size	2	3	4	5	6	7	8	4	6	8	10	12	14	16
16	14	12.8*	12.1*	11.5*	11.1*	10.7*	11	12	11.1*	11*	10.5*	10.3*	10*	10
18	12.6*	11.6*	10.9*	10.4*	9.9*	9.5*	9.2*	10.9*	9.9*	9.2*	8.6*	8.2*	7.8*	7.5*
20	11.5*	10.5*	9.8*	9.2*	8.8*	8.4*	8.1*	9.8*	8.8*	8.1*	7.5*	7*	6.7*	6.3*
22	9	8.8*	8.6*	8.1*	7.6*	7.3*	7	8	7.6*	6.9*	6.4*	5.9*	5.5*	5

Temperature Rise vs. Current per EIA-364-70

Tested with UL1061 Tinned Wire and PCB with 2oz. Copper Traces of 1.8mm width and 3.5mm length. *Extrapolated from test data.

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	SHEET No.		
A5	EC No: UCP2016-0922		IT [™] WIRE TO BO	-	6 of 16
AJ	DATE: 08/28/2015	CON	NECTOR SYSTEM	Λ	
DOCUMENT	<u>NUMBER:</u>	CREATED / REVISED BY:	CHECKED BY:	<u>APPRO\</u>	/ED BY:
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	Wire to Board Current Rating (Amp Max.) (Tested with GOLD plated terminals)													
		Conr	ector	fully l	oaded	with a	ll circ	uits p	ower	ed				
		Circ	cuit S	ize (Si	ngle R	low)		Circuit Size (Dual Row)						
AWG Wire Size	2	3	4	5	6	7	8	4	6	8	10	12	14	16
16	12	11.2*	11	10.7*	10.5*	10.3*	10	11	9.6*	9	8.5*	8*	7.7*	7
18	11*	10.1*	9.5*	9*	8.6*	8.2*	7.9*	9.5*	8.6*	7.9*	7.4*	7*	6.6*	6.3*
20	10*	9.1*	8.4*	7.9*	7.5*	7.2*	6.9*	8.4*	7.5*	6.9*	6.4*	6*	5.6*	5.3*
22	8	7.7*	7.4*	6.9*	6.5*	6.1*	6	6	5.8*	5.5*	5.3*	5.2*	5.1*	5

Temperature Rise vs. Current per EIA-364-70

Tested with UL1061 Tinned Wire and PCB with 2oz. Copper Traces of 1.8mm width and 3.5mm length. *Extrapolated from test data.

4.4 **TEMPERATURE**

TIN plated

Max. operating temperature range (including T-rise from applied current) is -40°C to 105°C. Field temperatures and field life: Tested per EIA 364-1000.01 to meet field temperature of 65°C for 10 years life per table-8.

GOLD plated

Max. operating temperature range (including T-rise from applied current) is -40°C to 120°C, thermal aging at 120°C for 1000 hours.

Field temperatures and field life: Tested per EIA 364-1000.01 to meet field temperature of 85°C for 10 years or 95°C for 7 years life per table-8.

4.5 DURABILITY

Tin plated: 25 mating cycles Gold plated: 200 mating cycles

As tested in accordance with EIA-364-1000.01 test method (see Sec. 7.0 of this specification). Durability per EIA-364-09.

5.0 QUALIFICATION

Laboratory conditions and sample selection are in accordance with EIA-364-1000.01

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	T SPECIFICATION	I FOR	SHEET No.				
A5	EC No:UCP2016-0922	ULTRA-F	ULTRA-FIT™ WIRE TO BOARD						
AJ	<u>DATE:</u> 08/28/2015	CON	CONNECTOR SYSTEM						
DOCUMEN	<u> NUMBER:</u>	CREATED / REVISED BY:	CHECKED BY:	<u>APPRO\</u>	/ED BY:				
PS-172323-0001		PPEREZ	RHODGE	RHO	DGE				



6.0 PERFORMANCE

6.1 ELECTRICAL PERFORMANCE

DESCRIPTION	TEST CONDITION	REQUIREMENT
Initial Contact Resistance (Low Level)	Mate connectors, apply a maximum voltage of 20 mV and a current of 100 mA (measurement locations shown) Per EIA-364-23 Wire resistance and traces shall be removed from the measured value.	Maximum (Initial): Tin: 2 mΩ 15μ" & 30μ" Gold: 3 mΩ
Contact Resistance @Rated Current (Voltage Drop)	Mate connectors; apply the rated current. Per EIA-364-70	Maximum: Tin: 5 mΩ 15μ" & 30μ" Gold: 7 mΩ
Insulation Resistance	Apply 500 VDC between adjacent terminals or ground. Per EIA-364-21	1,000 M Ω minimum
Dielectric Withstanding Voltage	Apply 1800 VAC for 1 minute between adjacent terminals. Per EIA-364-20	No breakdown Current leakage <5mA
Temperature Rise	Mate connectors, measure T- Rise @ Rated Current Per EIA-364-70	Temperature rise: 30° C maximum (see chart) PASS

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	T SPECIFICATION	I FOR	SHEET No.
A5	EC No:UCP2016-0922	ULTRA-FIT™ WIRE TO BO			8 of 16
AJ	<u>DATE:</u> 08/28/2015	CONNECTOR SYSTEM			0010
DOCUMENT NUMBER:		CREATED / REVISED BY: CHECKED BY: APPROVED B		/ED BY:	
PS-172323-0001		PPEREZ	RHODGE	RHO	DGE
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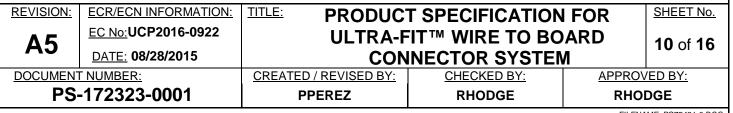
6.2 MECHANICAL PERFORMANCE

ITEM	TEST CONDITION	REQUIREMENT	
Connector Mating Force Without Latches	Mate connectors at a rate of 25.4 +/- 6 mm per minute. Per EIA-364- 37	Tin plated: 4.5 N MAX. initial mate force per circuit 15µ" & 30µ" Gold plated: 2.8 N MAX. per circuit	
Connector Un-mating Force Without Latches	Un-mate connectors with latch disabled at a rate of 25.4 +/- 6 mm per minute. Per EIA-364-37	Tin plated: 4.0 N MAX. initial un-mate force per circuit 15µ" & 30µ" Gold plated: 2.3 N MAX. per circuit	
Connector Mating Force Without Terminals	Mate connectors at a rate of 25.4 +/- 6 mm per minute. Per EIA-364- 37	8 N MAX.	
Thumb Latch Yield Strength	connectors apart at a rate of 25.4 89 NI MIN		
Durability	Mate connectors 25 cycles for tin plated and 200 cycles for gold plated connectors at a maximum rate of 10 cycles per minute. Per EIA-364-09	Maximum change from initial: Tin: 7 mΩ 15μ" & 30μ" Gold: 3 mΩ	

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	T SPECIFICATION	FOR	SHEET No.
A5	EC No:UCP2016-0922	ULTRA-F	ULTRA-FIT™ WIRE TO BOARD		
AJ	<u>DATE:</u> 08/28/2015	CON	9 of 16		
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	APPRO\	/ED BY:
PS-172323-0001		PPEREZ	RHODGE	RHO	DGE
				FILENA	ME: PS75431r0.DOC



Header Pin Retention Force in Housing	Axial pull force on the vertical header housing away from the PCB at a rate of 25.4 +/- 6 mm per minute.	Push from mating side: 50N MIN. Push from PCB side: 30N MIN.
6.2 MECHANICAL PER	FORMANCE (CONT.)	
ITEM	TEST CONDITION	REQUIREMENT
PCB Peg Insertion Force into the PCB (Right Angle Header)	Insert a header at a rate of 25.4±6 mm/minute. (Applies to parts with PCB retention pegs only)	Header with 2 pegs: 35 N MAX insertion force Headers with 1 peg: 23 N MAX insertion force
PCB Peg Retention Force to the PCB (Right Angle Header)	Insert a header at a rate of 25.4±6 mm/minute. (Applies to parts with PCB retention pegs only)	Header with 2 pegs: 0.2 N MIN retention force Headers with 1 peg: 0.1 N MIN retention force
Header Insertion Force into the PCB (Vertical Header)	Insert a header at a rate of 25.4±6 mm/minute.	With Kinked Pins: 35 N MAX. With Solder Clip: 25 N MAX.
Header Retention Force to the PCB (Vertical Header)	Remove a header at a rate of 25.4±6 mm/minute.	With Kinked Pins: 1 N MIN. With Solder Fork: 1 N MIN.
Crimp Terminal Retention Force (in housing)	Axial pullout force on the terminal in the housing at a rate of 25 ± 6 mm per minute. Per EIA-364-29	27 N MINIMUM retention force





Wire Pull Out Force From Terminal (Axial)	Apply an axial pullout force on the wire at a rate of 25 ± 6 mm per minute.	16AWG – 68.4N MIN 18AWG – 68.4N MIN 20AWG – 57.9N MIN 22AWG – 35.6N MIN Reference Molex Application Tooling Specification for Molex crimp tooling being used.
Vibration (Random)	Mate connectors and vibrate per EIA-364-28 test condition VII-D Tin: 15 minutes each axis. Gold: 1.5 hours each axis.	Maximum Change from Initial: Tin: 7 mΩ 15μ" & 30μ" Gold: 3 mΩ Discontinuity < 1 microsecond
Vibration per USCAR-2 Class V1, S1, T1	Mate connectors, mounted and vibrate as per USCAR-2 Rev6: 5.4.6 Class V1, S1, T2. Random Duration: 8hrs/axis	Maximum Change from Initial: Tin: 7 mΩ 15μ" & 30μ" Gold: 3 mΩ
Reseating	Unmate/Mate connectors by hand three cycles	Maximum Change from Initial: Tin: 7 mΩ 15μ" & 30μ" Gold: 3 mΩ

6.3 ENVIRONMENTAL PERFORMANCE*

	ITEM	TEST CONDITION	REQUIRE	MENT	
Thermal Shock		Mate connectors, expose to 1 cycles from -55°C to 85°C Per EIA-364-32 method A, condition 1	0 Maximum Chang Tin: 7 15µ" & 30µ" G	mΩ	
<u>REVISI</u>	ON: ECR/ECN INFORMATIO		SPECIFICATION	FOR <u>SHEET No.</u>	
۸ <i>E</i>	EC No: UCP2016-0922	ULTRA-FIT™ WIRE TO BOARD		ARD 11 of 16	
A5			CONNECTOR SYSTEM		
DOCUMENT NUMBER:		CREATED / REVISED BY:	CREATED / REVISED BY: CHECKED BY: APPROV		
PS-172323-0001		PPEREZ	PPEREZ RHODGE RHODG		
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Thermal Aging	Mate Connectors, expose to 240 hours at 105°C Per EIA-364-17 Method A	Maximum Change from Initial: Tin: 7 mΩ 15μ" & 30μ" Gold: 3 mΩ
Cyclic Temperature And Humidity	Mate connectors: expose to 24 cycles from 25 °C / 80% RH to 65 °C / 50% RH ramp time: 0.5hr dwell time: 1hr Per EIA-364-31	Maximum Change from Initial: Tin: 7 mΩ 15μ" & 30μ" Gold: 3 mΩ
Solderability Dip Test	Per Molex test method: SMES-152	Solder area shall have MIN. of 95% solder coverage (PASS)
Reflow Solder Resistance	Convection reflow solder process 260°C Max per AS-40000-5013	Visual: No damage
Wave Solder Resistance	Dip header terminal tails in solder: Duration: 5±0.5 seconds Solder temperature: 260±5° C Per AS-40000-5013	Visual: No damage
Thermal Cycling Tin Plated Only	Per EIA-364-1000.01 Test Group 5: Cycle mated connector between 15°C±3°C and 85°C±3°C as measured on the part. Ramps should be a minimum of 2°C per minute, and dwell times should insure contacts reach the temperature extremes (minimum of 5 minutes). Humidity is not controlled. Perform 500 cycles.	Maximum Change from Initial: Tin: 7 mΩ

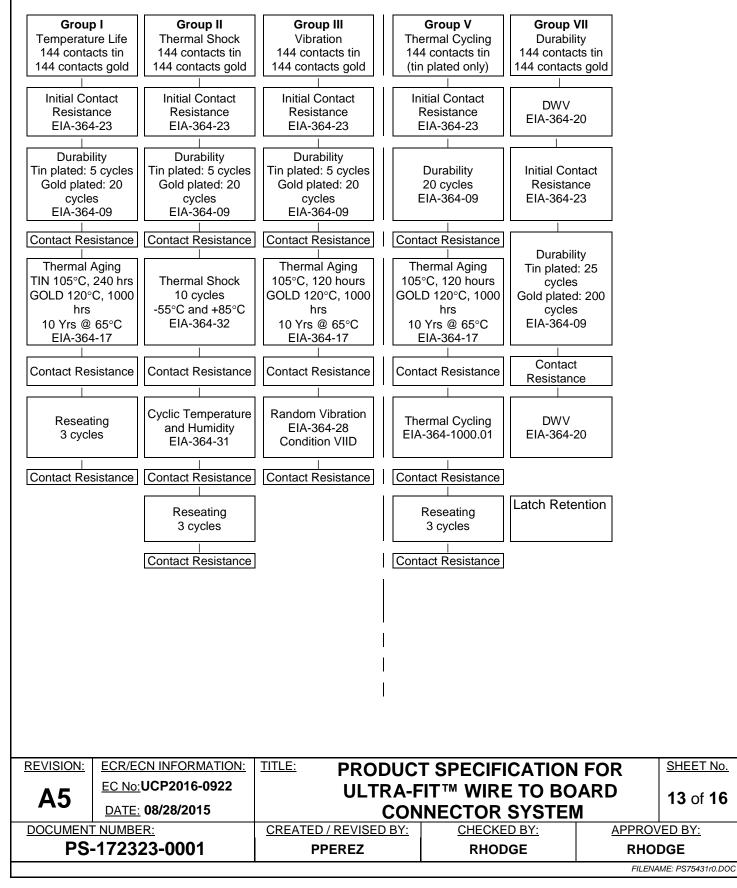
*Environmental tests have been performed per EIA-364-100.01 except where noted.

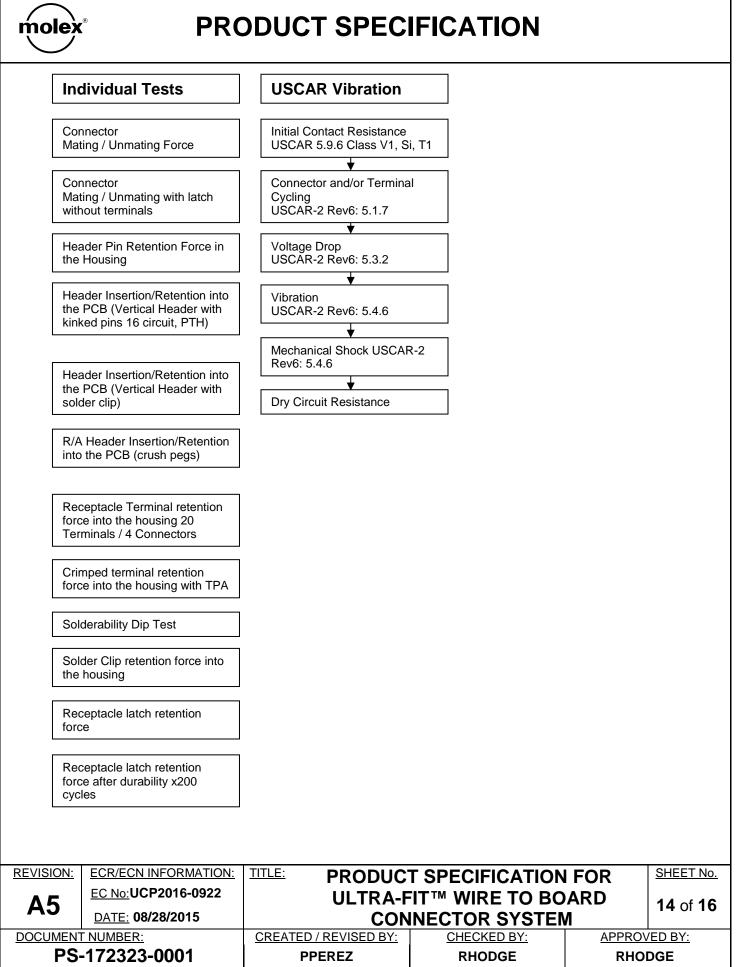
REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUCT SPECIFICATION FOR			SHEET No.	
A5	EC No:UCP2016-0922		ULTRA-FIT [™] WIRE TO BOARD 12 of 1			
AJ	DATE: 08/28/2015	CONNECTOR SYSTEM				
DOCUMENT NUMBER:		CREATED / REVISED BY: CHECKED BY: APPROVED BY		<u>'ED BY:</u>		
PS-172323-0001		PPEREZ	RHODGE	RHO	DGE	
				FILENA	ME: PS75431r0.DOC	



7.0 TEST SEQUENCE GROUPS

Reliability Test Sequences Per 364-1000.01





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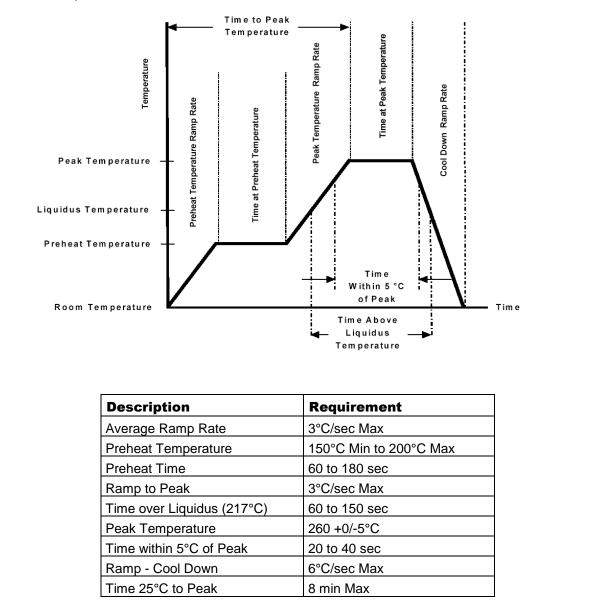
8.0 SOLDER INFORMATION

8.1 SOLDER PROCESS TEMPERATURES

Wave Solder: 265°C Max Reflow Solder: 260°C Max

8.2 REFLOW SOLDERING PROFILE

(This profile is per AS-40000-5013 and is provided as a guideline only. Please see notes for additional information)



REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	T SPECIFICATION	I FOR	SHEET No.
A5	EC No:UCP2016-0922	ULTRA-FIT™ WIRE TO BOARD			15 of 16
	<u>DATE:</u> 08/28/2015	CON			
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	<u>APPRO\</u>	/ED BY:
PS-172323-0001		PPEREZ	RHODGE	RHO	DGE



Notes:

- 1. Temperature indicated refers to the PCB surface temperature at solder tail area.
- 2. Connector can withstand 1 reflow cycle.
- 3. Actual reflow profile also depends on equipment, solder paste, PCB thickness, and other components on the board. Please consult your solder paste & reflow equipment manufacturer for their recommendations to adopt a suitable process.

REVISION:	ECR/ECN INFORMATION:	TITLE: PRODUC	T SPECIFICATION	FOR	SHEET No.
A5	EC No:UCP2016-0922	ULTRA-F	IT™ WIRE TO BO	ARD	16 of 16
	<u>DATE:</u> 08/28/2015	CON			
DOCUMENT NUMBER:		CREATED / REVISED BY:	CHECKED BY:	<u>APPRO\</u>	/ED BY:
PS-172323-0001		PPEREZ	RHODGE	RHO	DGE
				FILENA	ME: PS75431r0.DOC