



## L651\_Hardware Design

NB-IoT+GSM/GPRS+GNSS Module Series

Version: V1.0

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# **Version History**

Date	Version	Modify records	Author
2018-11-23	V1.0	First Release	Tp.Lin



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## 1. Introduction

L651 is a small NB-IoT+GSM/GPRS+GNSS module for LCC package, with low power consumption, high performance, stable performance, small size, light weight, high reliability, high cost-effective, it can be applied to a variety of compact products.

#### 1.1 Hardware Diagram

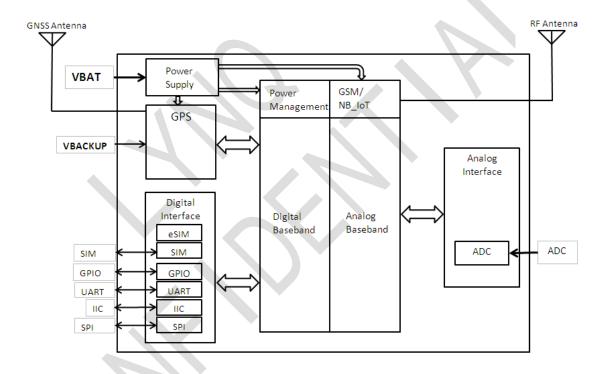


Figure 1-1 L651 Functional architecture

#### 1.2 Main features

#### CPU

MIPS processor@192MHz



#### Flash

PSRAM: 32Mb

Flash: 64Mb

#### Frequency bands

GSM:

GSM850

GSM900

DCS1800

PCS1900

NB-IoT:

Band3

Band5

Band8

#### Output Power

GSM:

GSM850/GSM900 33dBm  $\pm$ 2dB DCS1800/PCS1900 30dBm  $\pm$ 2dB

NB-IoT:

 Band3
 23dBm +/-2dB

 Band5
 23dBm +/-2dB

 Band8
 23dBm +/-2dB

#### Sensitivity

**TBD** 

#### • Ultra-low power consumption

PSM: 4uA@3.8V

GSM sleep mode:1.65mA@3.8V



#### Product series

L651: NB-IoT+GSM/GPRS

L651G: NB-IoT+GSM/GPRS+GNSS

#### 1.3 Specifications

■ Supply Voltage Range: 3.4~4.2V (type3.8V)

Dimensions: 24mm \* 20mm \* 2.4mm

Package: 52-pin LCC

Support GSM and NB-IoT (NB-IoT support PSM and eDRX mode)

GNSS: GPS+BEIDOU(Glonass)

Operation Temperature Range: -40℃~+85℃

Storage Temperature Range: -45℃~+90℃

Two antenna interface: GSM/NB-IoT antenna and GNSS antenna

#### 1.4 Interfaces

- ADC
- IIC
- GPIO
- SPK and MIC (alternative and reuse of GPS base)
- SPI
- SIM: Support 1.8V/3V or eSIM
- UART



## 2. Package Information

### 2.1 Pin Configuration

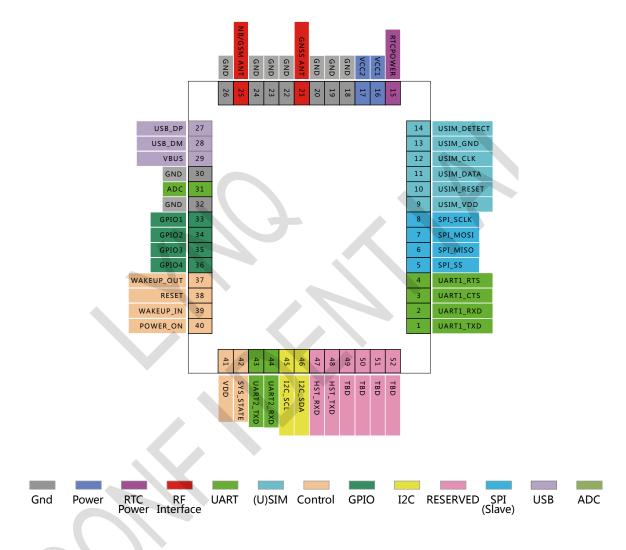


Figure 2-1 L651 Pin View

#### 2.2 Pin definition

Table 2-1 L651 Pin description

Pin	Pin name	Type	Function Description	Power domain	State
NO.	Pili liaille	Type	Function Description	Power domain	(1)



1.	UART1_TXD	O	UART1 transmit data output	VIO28	Open
2.	UART1_RXD	I	UART1 receive data input	VIO28	Open
3.	UART1_CTS	DO	UART1 clear to send	VIO28	Open
4.	UART1_RTS	DI	UART1 ready to receive	VIO28	Open
5.	SPI_SS	О	SPI chip-select	VIO28	Open
6.	SPI_MISO	I	SPI master in slave out	VIO28	Open
7.	SPI_MOSI	О	SPI master out slave in	VIO28	Open
8.	SPI_SCLK	O	SPI clock	VIO28	Open
9.	USIM_VDD	P	SIM output voltage	1.8V/2.8V	Open
10.	USIM_RESET	О	SIM reset	1.8V/2.8V	Open
11.	USIM_DATA	I/O	SIM data	1.8V/2.8V	Open
12.	USIM_CLK	0	SIM clock	1.8V/2.8V	Open
13.	USIM_GND	G	Ground		GND
14.	USIM_DETEC T	I	SIM detect pin	VIO28	Open
15.	RTCPOWER	P	GPS backup power		
16.	VCC1	D	Down ownsky	3.4V-4.2V	VBAT
17.	VCC2	P	Power supply	3.4V-4.2V	VBAI
18.	GND	G	Ground		GND
19.	GND	G	Ground		GND
20.	GND	G	Ground		GND
21.	GND	G	Ground		GND
22.	GNSS_ANT	ANT	GNSS Antenna		
23.	GND	G	Ground		GND
24.	GND	G	Ground		GND
25.	NB/GSM ANT	ANT	GSM/NB-IoT Antenna		



26.	GND	G	Ground		GND
27.	USB_DP	DIO	USB port differential data line		TBD
28.	USB_DM	DIO	USB port differential data line		TBD
29.	VBUS	P			TBD
30.	GND	G	Ground		GND
31.	ADC	I	ADC external input channel Ground	0V-1.8V	Open
32.	GND	G	Ground		GND
33.	GPIO1	I/O	General input/output PIN	VIO28	Open
34.	GPIO2	I/O	General input/output PIN	VIO28	Open
35.	GPIO3	I/O	General input/output PIN	VIO28	Open
36.	GPIO4	I/O	General input/output PIN	VIO28	Open
37.	WAKEUP_OU T	0	General input/output PIN. It can be used as wake signal to host from module	VIO28	Open
38.	RESET	I	System reset signal		Open
39.	WAKEUP_IN	I	Wake up module for exiting PSM	0V-1.4V	Open
40.	POWER_ON	I	Power key		Open
41.	VDD	P	2.8V output voltage	2.8V	Open
42.	SYS_STATE	0	Output PIN as LED control for network status	VIO28	Open
43.	UART2_TXD	0	UART2 transmit data output	VIO28	Open
44.	UART2_RXD	I	UART1 receive data input	VIO28	Open
45.	I2C_SCL	I/O	IIC clock	VIO28	Open
46.	I2C_SDA	I/O	IIC data	VIO28	Open
47.	HST_RXD	I	Download/debug UART receive data input	VIO28	Open
47.	HST_RXD HST_TXD	I O	•	VIO28 VIO28	Open Open



50.	TBD		
51.	TBD		
52.	TBD		

(1) Suggested status when not in use.

Table 2-2 Pin type description

P:Power	G:Ground
I:Input	DI:Digital input
O:Output	DO:Digital output
ANT:Antenna	DIO:Digital input output
NC:Not connect	AI:Analog input
TBD:To Be Determined	

## 2.3 Package Information

#### 2.3.1 Dimensions

The L651 mechanical dimensions are described as following figure (Top view, Back view, Side view).

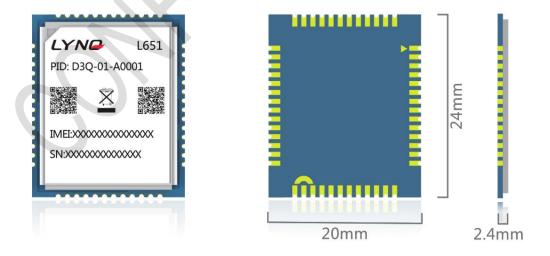


Figure 2-2 Mechanical Dimensions



#### 2.3.2 Product labeling

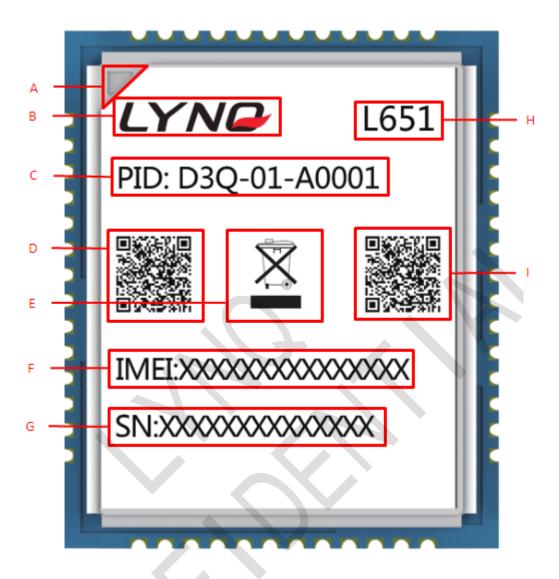


Figure 2-3 Label of L651

Table 2-3 Description of label

Item	Description
A	Pin1 mark
В	Logo of company
C	PIN number
D	QR codeinclude IMEI number
Е	WEEE



F	IMEI number
G	SN number
Н	SOM name
I	QR codeinclude SN number

#### 2.3.3 Module size

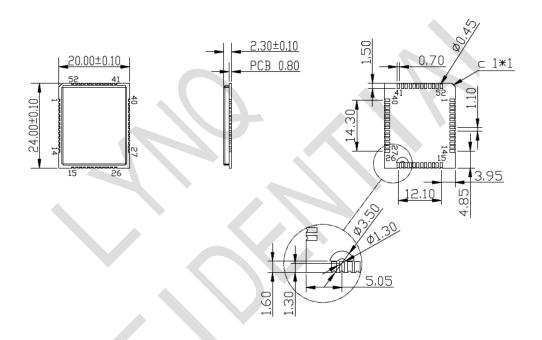


Figure 2-3 Module Size (Unit: mm)



#### 2.3.4 Recommend Pad

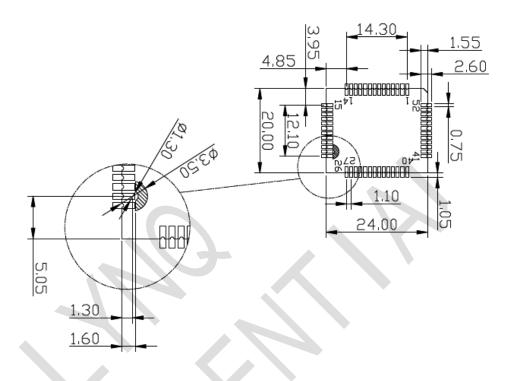


Figure 2-4 Recommend pad(Top view, Unit: mm)



## 3. Interface Circuit Design

#### 3.1 Power Section

#### 3.1.1 Power Supply interface

VBAT is the main power supply of the module, and the input voltage range is 3.4V to 4.2V. The recommended voltage is 3.8V. Because the module transmit burst may cause voltage drops, the highest peak will reach 2.0A. In NB-IoT mode, the module transient peak current may reach 1A. The peak current power supply capacity should be above 2.0A and the average current should be above 1.0A.A large capacitor voltage is recommended to use near VBAT pins, and the capacitor's value is the bigger the better. In order to improve the continued flow of large current, it is recommended to use a low-impedance tantalum 470uF or larger. During layout, the capacitors are close to the VBAT pins.

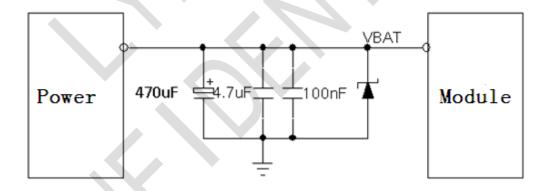


Figure 3-1 Power Supply circuit

Notes: According to the environment, please select capacitor as large value as possible; and add 100pF, 33pF capacitors if requiring.

Add Zener close to our module. The Zener should be 5.1V/500mW, Ir<100uA @Vr=4.2V. Ta=25 $^{\circ}$ C.



#### 3.1.2 Hardware Power On/Off

Module 40-pin is the Power on/off key. Pulling down the PWRKEY at least 460ms∼1s and then releasing, the module will boot. There is internally pulled-up resistor.

Module 40-pin can also be used for hardware shutdown. When this pin has been pulled down over  $2\sim6$  seconds, the module will shutdown.

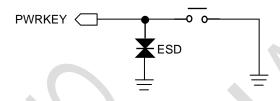


Figure 3-2 Turn on circuit

#### 3.1.3 Hardware reset

Module 38-pin is the hardware reset input. The module will reset hardware when it receives a 1s low level signal. It is internally pull-up to VBAT, and does not need to pull up externally.

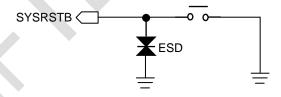


Figure 3-3 System Reset

#### 3.1.4 WAKEUP\_IN\_RTC Key

Module 39-pin is the WAKEUP\_IN\_RTC input. The module will exit the PSM mode when it receives a 1ms low level signal.





Figure 3-4 RTC\_EINT Key

#### 3.2 SIM Interface

#### 3.2.1 Pin Description

L651 supports and is able to automatically detect 3.0V and 1.8V SIM card. SIM card interface signal as shown in table 3-1.

Table 3-1 SIM Pin Description

Pin NO.	Pin Name	Signal definition	Function Description
9	USIM_VDD	VSIM output voltage	VSIM card power supply, output by the module
10	USIM_RESET	SIM card reset pin	SIM card reset signal, output by the module
11	USIM_DATA	SIM card data pin	SIM card DATA signal, I/O signal
12	USIM_CLK	SIM card clock pin	SIM card clock signal, output by the module
13	USIM_GND	Ground	Ground
14	USIM_DETRCT	SIM detect pin	SIM detect pin, input signal

#### 3.2.2 SIM application

Please note to increase the ESD components on SIM card signal group (PIN number: 9, 10, 11, 12, 13, 14), near the SIM card seat.

In order to meet the requirements of 3GPP TS 27.005 protocol and EMC certification, the proposed SIM card is arranged near the module SIM card interface, and avoid to layout too long resulting in



serious waveform distortion, affecting the signal integrity. SIM\_SCLK and SIM\_SIO signals are recommended to be protected. Paralleling a 1uF capacitor between GND and VSIM, that can filter out the interference of radio frequency signals.

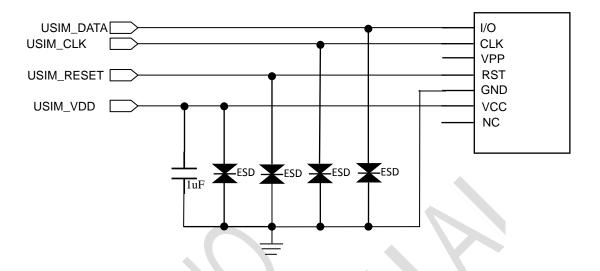


Figure 3-5 SIM Circuit

Notes: The capacity value of ESD components should be under 22pF.

#### 3.2.3 eSIM

The eSIM card is reserved inside the module. The eSIM card and external SIM card can only be selected one by one.

#### 3.3 IIC Interface

L651 module can support a group of IIC interface. You can connect peripherals that need to communicate using the IIC interface.



#### 3.4 UART Interface

#### 3.4.1 Pin Description

L651 module provides three serial communication interface UART: HST\_UART as the download and debug port of L651 module, the default baud rate is 921600 bps, and there is a means of grabbing logs in the process of software debug. We suggest that users retain this interface and reserve test points when designing. UART2 can be used as the download and debugging port of L651 module and the outside world. The default baud rate of the port for serial communication and AT instruction input is up to 57600 bps, and UART1 is the open port for customers to choose independently.

The pin signal is defined as shown in table 3-1.

Table 3-2 UART Pin Description

Pin NO.	Pin Name	1/0	Function Description
1	UART1_TXD	DO	UART1 transmit data output
2	UART2_RXD	DI	UART1 receive data input
3	UART1_CTS	DO	UART1 Clear to Send
4	UART3_RTS	DI	UART1 require to send
43	UART2_TXD	DO	UART1 transmit data output
44	UART2_RXD	DI	UART1 receive data input
47	HST_TXD	DO	Download/debug UART transmit data output
48	HST_RXD	DI	Download/debug UART receive data input

#### 3.4.2 UART application

If used UART1 in communication between the module and application processor, and the level is 1.8V, the connection mode is shown in Figure 3-6. You can use 4 wires or 2 wires mode connection. Module interface level is 1.8V. If the AP interface level does not match, you must increase the level



conversion circuit.

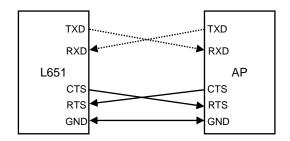


Figure 3-6 Connect to AP method(4lines)

#### 3.5 LED Interface

NETLIGHT (PIN42) is used as the enable pin. Table 3-3 lists the LED status.

Table 3-3 LED Status

LED Status	Module Status
OFF	Power off or PSM Mode
64ms ON/800ms OFF	Shut down network
64ms ON/3000ms OFF	Registered network

#### 3.6 ADC

The module provides an ADC for detecting light-sensitive resistors or other devices that require ADC detection. The Max voltage of ADC is 1.4V with 10-bit accuracy.

Table 3-4 ADC value

Parameter	Min.	Тур.	Max.	Unit
Input range	0.05		1.4	V



## 4. Electrical characteristics

#### 4.1 Electrical characteristic

Table 4-1 Electrical characteristic

Power	Min.	Nom.	Max	Unit
VBAT	3.4	3.8	4.2	V
Peak current	-0.3	-	1.5	A

Notes: The over-low voltage can't power on the module. Over-high voltage may be danger to damage the module.

### 4.2 Temperature characteristic

Table 4-2 Temperature characteristic

State	Min.	Nom.	Max	Unit
Working	-40	25	85	${\mathbb C}$
Storage	-45	25	90	${\mathbb C}$



Notes: When the temperature is over the range, the RF performance may be dropped. It also may cause power down or restart problem.

#### 4.3 Absolute Maximum Power

Table 4-3 Absolute maximum power rating

PIN Name	Description	Min.	Тур.	Max.	Unit
VDD	Digital power for IO	2.6	2.8	3	V

### 4.4 Recommended operating conditions

Table 4-4 Recommended operating range

PIN Name	Description	Min.	Тур.	Max.	Unit
VDD	Digital power for IO	2.6	2.8	3	V

Notes: All the GPIOs of module are 2.8V.

### 4.5 Power consumption

Table 4-5 Power Consumption

Parameter	Conditions	Min.	Average	Max.	Unit
Power off mode	VBAT=3.8V	-	7		uA



I	PSM	VBAT=3.8V	-	4	uA	
-						

Notes: The test value of power consumption is the value tested in laboratory condition.

#### 4.6 Power Sequence

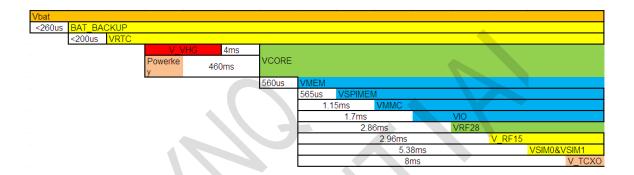


Figure 4-1 Power up time sequence diagram

### 4.7 Digital Interface Characteristics

Table 4-6 Digital IO Voltage

Parameter	Description	Min.	Typical	Max.	Unit
VIH	High level input voltage	2.6	-	3	V
VIL	Low level input voltage	0	-	0.7	V
VOH	High level output voltage	2.6	-	3	V
VOL	Low level output voltage	0	-	0.3	V

Notes: Suit to all GPIOs, IIC, UART interfaces.



#### 4.8 **ESD**

Because there is no special protection against electrostatic discharge in the module, it is necessary to pay attention to the protection of electrostatic protection in the production, assembly and operation module. The performance parameters of the module test are as follows.

ESD parameter (Tem: 25℃, humidity: 45%)

Table 4-7 ESD Performance

PIN Name	Contact discharge	Air discharge
VBAT	±4KV	±8KV
GND	±4KV	±8KV
RF_ANT	±4KV	±8KV
GNSS_ANT	±4KV	±8KV

Enhanced ESD performance method:

- 1. If a converted board is added, it should have enough GND pins and be equally distributed. And the Layout of GND should be enough wide.
- 2. Key (Powerkey, WAKEUP\_IN\_RTC key and Reset key) need to add ESD device. Reset key line can't be near the edge of the board.
- 3. UART and other plug connector need to add ESD devices, and the other control lines from the outside of the machine also need to add ESD devices.
- 4. SIM card should be added ESD protect.
- 5. External antenna, please add ESD device, ESD C<sub>pf</sub><0.5pF.

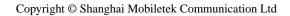




Notes: For ESD protect, please add ESD methods according to upper ways.

ESD components include varistors and TVS. For better performance, please use

TVS.





## 5. RF Features

#### 5.1 RF Main Features

The L651 module supports GSM and NB-IoT.

Table 5-1 GSM Frequency Band

Frequency	Uplink	Downlink
GSM850	824 MHz — 849 MHz	869 MHz — 894 MHz
E-GSM900	880 MHz — 915 MHz	925 MHz — 960 MHz
DCS1800	1710 MHz — 1785 MHz	1805 MHz — 1880 MHz
PCS1900	1850 MHz — 1910 MHz	1930 MHz — 1990 MHz

Table 5-2 GSM Output power

Frequency	Max output power	Min output power
GSM850	33dBm ±2dB	5dBm ±5dB
E-GSM900	33dBm ±2dB	5dBm ±5dB
DCS1800	30dBm ±2dB	0dBm ±5dB
PCS1900	30dBm ±2dB	0dBm ±5dB

Table 5-3 GSM Receive sensitivity

Frequency	Receive sensitivity
GSM850	TBD



E-GSM900	TBD
DCS1800	TBD
PCS1900	TBD

<sup>&</sup>quot;TBD" Under development.

Table 5-4 NB-IoT Frequency Band

Frequency	Uplink	Downlink
Band 3	1710 MHz—1785MHz	1805MHz —1880MHz
Band 5	824 MHz —849MHz	869MHz —894MHz
Band 8	880MHz —915MHz	925MHz —960MHz

Table 5-5 NB-IoT Output power

Frequency	Max output power	Min output power
Band 3	23dBm +/-2dB	<-45dBm
Band 5	23dBm +/-2dB	<-45dBm
Band 8	23dBm +/-2dB	<-45dBm

Table 5-6 NB-IoT Receive sensitivity

Frequency	Receive sensitivity
Band 3	TBD
Band 5	TBD
Band 8	TBD



#### 5.2 Data link

GSM:

L651 support GPRS CLASS12

NB-IoT:

Supports both single-tone and multi-tone transmission modes in uplink

Supports 200KHz bandwidth in downlink and uplink

Supports PSM and eDRX mode

Supports CP-CloT and UP-CloT

High mobility supported

### 5.3 GSM/NB-IoT Antenna Circuit Design

Figure R1, C1, C2 and R2 composition of the antenna matching network for antenna debugging, the default R1, R2 paste 0 ohm resistor and C2, C1 empty paste. The microstrip line or strip line is designed according to the characteristic impedance of 50 ohm, and the length of the wire is less than 10mm. Reserved  $\Pi$  matching network.

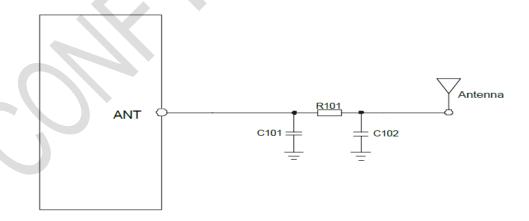


Figure 5-1 Main Antenna Design

Attention should be paid to component placement and RF routing:



- The antenna and matching circuit are placed close to the module pin;
- The RF line from the ANT pin of the module to the antenna must be controlled by 50 ohm impedance.
- To the antenna RF module ANT foot line must be far away from the high speed signal lines and strong interference sources, to avoid any cross line or flat line and the adjacent layer.





## 6. GNSS

#### 6.1 GNSS Characteristic

- Support GPS/Galileo/Glonass/BD2
- 64 channel tracking at the same time, the built-in LNA
- NMEA maximum 10 Hz update rate
- Type of Antenna: Active antenna + Passive antenna
- Position precision: TBD

#### 6.2 GNSS Performance

Table 6-1 GNSS Performance indicators

Desirat	Describe	Performance			
Project		Min.	Тур.	Max.	Unit
Horizontal Position Accuracy <sup>(1)</sup>	Autonomous		TBD		m
Velocity Accuracy <sup>(2)</sup>	Without Aid		TBD		m/s
velocity Accuracy	DGPS		TBD		m/s
Acceleration Accuracy	Without Aid		TBD		m/s <sup>2</sup>
	DGPS		TBD		m/s <sup>2</sup>
Timing Accuracy			TBD		ns
	Maximum Altitude			TBD	m
Dynamic Performance	Maximum Velocity			TBD	m/s
	Maximum Acceleration			TBD	G
GPSTime To First Fix <sup>(3)</sup>	Hot start		TBD		S
Warm start			TBD		S



	Cold start	TBD	s
	Hot start	TBD	S
BD2Time To First Fix <sup>(3)</sup>	Warm start	TBD	s
	Cold start	TBD	s
	Hot start	TBD	S
GPS+GLONASS Time To First Fix <sup>(3)</sup>	Warm start	TBD	S
	Cold start	TBD	S
	Hot start	TBD	S
GPS+BD2Time To First Fix <sup>(3)</sup>	Warm start	TBD	S
	Cold start	TBD	S
	Hot start	TBD	S
A-GPSTTFF(easy mode)	Warm start	TBD	S
	Cold start	TBD	S
	Hot start	TBD	S
A-GPSTTFF(EPO mode)	Warm start	TBD	S
	Cold start	TBD	S
GPS	Autonomous acquisition(cold start)	TBD	dBm
Sensitivity <sup>(5)</sup>	Re-acquisition	TBD	dBm
Schistivity	Tracking	TBD	dBm
	Autonomous acquisition(cold start)	TBD	dBm
BD2 Sensitivity <sup>(5)</sup>	Re-acquisition	TBD	dBm
Sensitivity	Tracking	TBD	dBm
GPS+BD2	Autonomous acquisition(cold start)	TBD	dBm
Sensitivity <sup>(5)</sup>	Re-acquisition	TBD	dBm
Belisitivity	Tracking	TBD	dBm
GPS+GLONASS	Autonomous acquisition(cold start)	TBD	dBm



Sensitivity <sup>(5)</sup>	Re-acquisition		TBD		dBm
	Tracking		TBD		dBm
	Channels		TBD		
	Update rate		TBD	TBD	Hz
Receiver	Tracking L1, CA Code				
	Protocol support NMEA,PMTK				
	Acquisition		TBD		mA
GPS	Continuous tracking		TBD		mA
Power consumption <sup>(4)</sup>	Sleep current		TBD		uA
	Backup current		TBD		uA
	Acquisition		TBD		mA
BD2	Continuous tracking		TBD		mA
Power consumption <sup>(4)</sup>	Sleep current		TBD		uA
	Backup current	1-7	TBD		uA
	Acquisition		TBD		mA
GPS+BD2	Continuous tracking		TBD		mA
Power consumption <sup>(4)</sup>	Sleep current		TBD		uA
	Backup current		TBD		uA
GPS+GLONASS	Acquisition		TBD		mA
	Continuous tracking		TBD		mA
Power consumption <sup>(4)</sup>	Sleep current		TBD		uA
	Backup current		TBD		uA

## 6.3 Antenna design and selection



#### 6.3.1 Antenna design

Table 6-2 Antenna selection

ТУРЕ	Specifications	
Passive Antenna	Frequency Range	TBD
	Polarization mode	TBD
	Gain	TBD
Active Antenna	Frtequency Range	TBD
	Polarization mode	TBD
	Noise Factor	TBD
	Gain	TBD

### 6.3.2 GNSS antenna design

TBD



## 7. Storage and Production

#### 7.1 Storage

The rank of moisture proof of the module is level 3. There is an obvious sign on the table of the internal and the external packaging.

In the vacuum sealed bag, the module can be stored for 180 days when the temperature is below 40°C and the humidity is below 90% under good air circulation.

Humidity level is described detail as follows:

Table 7-1 Humidity level

Rank	Factory Environment ≤ +30°C /60%RH
1	No controll < 30°C /85%RH
2	One year
2a	4 weeks
3	168 hours
4	72 hours
5	48 hours
5a	24 hours
6	Baking before using, SMT during the time table signs

Notes: Moving, storage, production of module must meet the demand of IPC/JEDEC J-STD-033.

#### 7.2 Production

The module is a humidity sensitive device. If the device needs reflow soldering, disassembly and



maintenance, we must strictly comply with the requirements of humidity sensitive device. If module is damp, a reflow soldering or using a hot-air gun maintenance will lead to internal damage, because the water vapor has the rapid expansion of the burst, causing physical injury to the device, like PCB foaming and BGA component fail. So customers should refer to the following recommendations.

#### 7.2.1 Module confirmation and moisture

The module in the production and packaging process should be strictly accordance with the humidity sensitive device operation. The factory packaging is vacuum bag, desiccant, and humidity indicator card. Please pay attention to the moisture control before SMT and the confirmation of the following aspects.

#### **Demand of Baking confirmation**

Smart module uses vacuum sealed bag, which can make it stored for 6 months under the condition of temp 30°C and humidity < 60%. The module should be baked before reflowing soldering if any of the conditions below happen.

- 1. Storage exceeds the time limit.
- 2. Package damages and vacuum bags have air leakage.
- 3. Humidity indicating card change the color at 10%.
- 4. Module is placed naked in the air over 168 hours.
- 5. Module is placed naked in the air under 168 hours but not temp 30°C and humidity < 60%.

#### **Baking condition confirmation**

The moisture proof level of the smart module is level 3. And the baking conditions are as follows.

Table 7-2 Baking conditions



Baking conditions	120°C / 5%RH
Baking time	4 hours
Description	not use the original tray

Notes: The original packaging of the module cannot bear the high temperature of baking. The packaging needs to be removed before baking, otherwise the packaging will be damaged.

The anti-ESD tray of the original packaging is only used for packaging, and can't be used as a SMT tray.

During taking and placing, please take notes of ESD and cannot be placed as overlay.

#### **Customer product maintenance**

If maintenance module after SMT, it is easy for damp module to damage when removing, so the module disassembly and other related maintenance operations should complete within 48 hours after SMT, or need to bake and then maintenance the module.

Because the module return from the field work can't ensure the dry state, it must be baked in accordance with the conditions of baking, then for disassembly and maintenance. If it has been exposed to the humid environment for a long time, please properly extend the baking time, such as 125°C /36 hours.

#### 7.2.2 SMT reflow attentions

The module has the BGA chips, chip resistances and capacitances internally, which will melt at high temperature. If module melt completely encountered a large shock, such as excessive vibration of reflux conveyor belt or hit the board, internal components will easily shift or be false welding. So, using intelligent modules over the furnace need to pay attention to:



- Modules can't be vibrate larger, namely customer requirements as far as possible in orbit (chain)
   furnace, furnace, avoid on the barbed wire furnace, in order to ensure smooth furnace.
- The highest temperature can't too high. In the condition that meet the welding quality of customer motherboard and module, the lower furnace temperature and the shorter maximum temperature time, the better.

Some customer's temperature curve in the line is not suitable, high temperature is too high, and customer motherboard melt good, but non-performing rate is on the high side. Through the analysis of the causes, it found that melt again of BGA components lead device offset and short circuit. After adjusting the temperature curve, it can ensure that the customer's motherboard the welding quality, and also improve the pass through rate. Non-performing rate is controlled below the 2/10000.

#### 7.2.3 SMT stencil design and the problem of less tin soldering

Part of customers found false welding or circuit short when reflowing. The main reason is module tin less, PCB distortion or tins too large. Suggestions are as follows:

- Suggest use ladder stencil, which means the depth of the region of module is thicker than other
  areas. Please adjust validation according to the measured thickness of solder paste, the actual
  company conditions and experience value. The products need to strictly test.
- Stencil: Reference module package, and the user can adjust according to their company experience; Outside of the module, the stencil extends outside. The GND pads use the net stencil.

#### 7.2.4 SMT attentions

If customer motherboard is thin and slender with a furnace deformation, warping risks, you will be suggested to create "a furnace vehicle" to ensure the welding quality. Other production proposals are as follows:

The solder pastes use brands like Alfa.



- The module must use the SMT machine mount (important), and do not recommend manually placed or manual welding.
- For SMT quality, Please ensure the necessary condition according to actual condition of factory before SMT, like SMT pressure, speed (very important), stencil ways.
- We must use the reflow oven more than 8 temperature zones, and strictly control the furnace temperature curve.

Recommended temperature:

- B. constant temperature zone: temperature 140-210°C, time: 60s-120s.
- E. recirculation zone: PEAK temperature 220-245°C, time: 45s-75s.

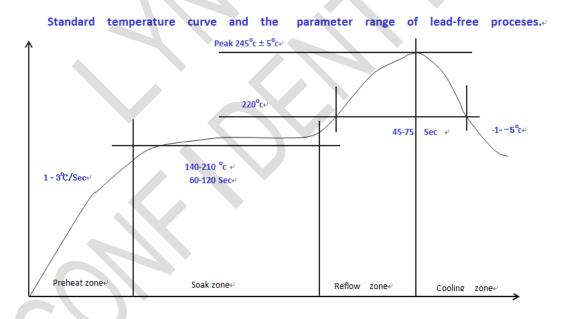


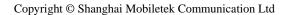
Figure 7-1 Temperature Curve





Notes: customer's board deformation must be controlled well. By reducing the number of imposition or increasing patch clamp to reduce the deformation.

Module thickness of the stencil is recommended to be thickened, and the rest position can be maintained by 0.1mm.





## 8. Safety Information

For the reasonable usage of the module, please comply with all these safety notices of this page. The product manufacturers should send followed safety information to user, operator or product's spec.



The devices using the module may disturb some electronic equipment. Put the module away from the phone, TV, radio and automation equipment to avoid the module and the equipment to interfere with each other.



Shut down the mobile device or change to flying mode before boarding. The Using of wireless appliances in an aircraft is forbidden to avoid the interference, or else cause to unsafe flying, even violate the law.



In hospital or health care center, switch off the mobile devices. RF interference may damage the medical devices, like hearing-aid, cochlear implant and heart pacemaker etc.



Mobile devices can't guarantee to connect in all conditions, like no fee or with an invalid SIM card. When you need emergent help, please remember using emergency calls and make sure your device power on in an area with well signal.



Put the module away from inflammable gases. Switch off the mobile device when close to gas station, oil depot, chemical plant etc.



The module is not water proof. Please don't use the module in the area with high humidity like bathroom, which will decelerate the physical performance, insulation resistance and mechanical strength.



Non-professionals can't teardown the module which will damage it. Refer to the specification or communicate the related staffs to repair and maintain it.



Please switch on the module before cleaning. The staffs should be equipped with anti-ESD clothing and gloves.

The users and product manufacturers should abide by the national law of wireless modules and devices. If not, Mobiletek will not respond the related damages.