

TFT COLOR LCD MODULE

NL8060BC31-50F

31cm (12.1 Type) SVGA



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INTRODUCTION

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Examples: Vehicle/train/ship control system, traffic signals system, traffic information control system, air traffic control system, surgery/operation equipment monitor, disaster/crime prevention system, etc.

The **Specific:** Applications as any failure, malfunction or error of the products might severe cause any damage to death, human bodily injury or other property (Products Safety Issue) and the safety of the public (Social Issues) and developed, designed and manufactured in accordance with the standards or quality assurance program designated by the customer who requires extremely high level reliability and quality.

Examples: Aerospace system (except seat entertainment monitor), nuclear control system, life support system, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.



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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL8060BC31-50F are composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATION

• For industrial use

1.3 FEATURES

- High luminance
- Wide viewing angle
- Wide temperature range
- 6-bit digital RGB signals
- Reversible-scan direction
- Long life LED backlight
- Replaceable lamp for backlight
- Compliant with the European RoHS directive (2011/65/EU) and Delegated Directive (2015/863/EU, Amending Annex II of 2011/65/EU)
- Acquisition product for UL60950-1 /CSA C22.2 No.60950-1-03 (File number: E170632)
- Acquisition product for UL62368-1 /CSA C22.2 No.62368-1-14 (File number: E170632)





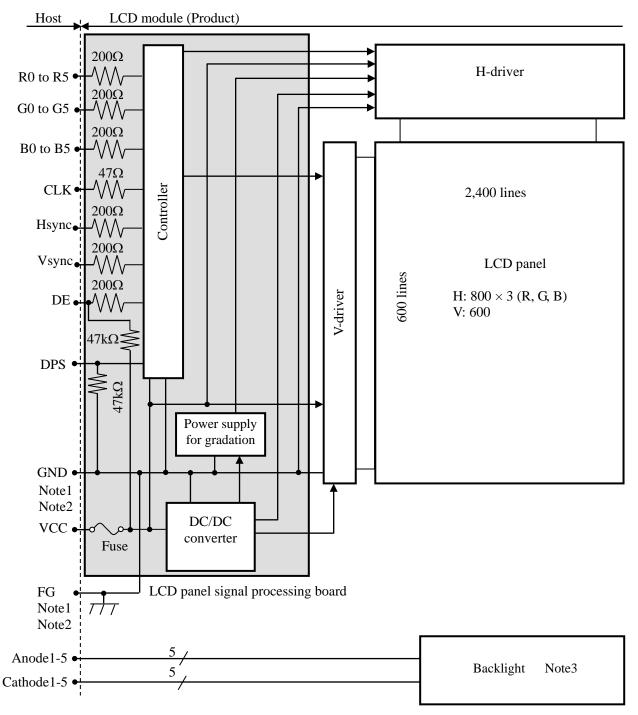


2. GENERAL SPECIFICATIONS

Display area	246.0 (H) × 184.5 (V) mm
Diagonal size of display	31cm (12.1 inches)
Drive system	a-Si TFT active matrix
Display color	262,144 colors
Pixel	$800 \text{ (H)} \times 600 \text{ (V)} \text{ pixels}$
	RGB (Red dot, Green dot, Blue dot) vertical stripe
Pixel arrangement	
Dot pitch	0.1025 (H) × 0.3075 (V) mm
Pixel pitch	0.3075 (H) × 0.3075 (V) mm
Module size	260.5 (W) × 203.0 (H) × 8.7 (D) mm (typ.)
Weight	490 g (typ.)
Contrast ratio	1000:1 (typ.)
Viewing angle	 At the contrast ratio ≥ 10:1 Horizontal: Right side 80° (typ.), Left side 80° (typ.) Vertical: Up side 80° (typ.), Down side 80° (typ.)
Designed viewing direction	 At DPS= Low or Open: Normal scan Viewing direction without image reversal: Up side (12 o'clock) Viewing direction with contrast peak: Down side (6 o'clock) Viewing angle with optimum grayscale (γ = 2.2): Normal axis (perpendicular)
Polarizer surface	Clear
Polarizer pencil-hardness	3H (min.) [by JIS K5600]
Color gamut	At LCD panel center 40 % (typ.) [against NTSC color space]
Response time	$Ton + Toff (10\% \longleftrightarrow 90\%)$ 18 ms (typ.)
Luminance	At IL= 50 mA/One circuit 900 cd/m ² (typ.)
Signal system	6-bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE), Horizontal synchronous signal (Hsync), Vertical synchronous signal (Vsync)
Power supply voltage	LCD panel signal processing board: 3.3V or 5.0V
Backlight	LED backlight: (Replaceable part • Lamp holder set: 121LHS32 (Recommended LED driver board (Option) • LED driver board: 104PW03F • Corresponding wiring harness: 121CBL03
Power consumption	At IL= 50 mA/One circuit, Checkered flag pattern 7.1 W (typ.)



3. BLOCK DIAGRAM



Note1: Relation between GND (Signal ground), FG (Frame ground) in the LCD module is as follows.

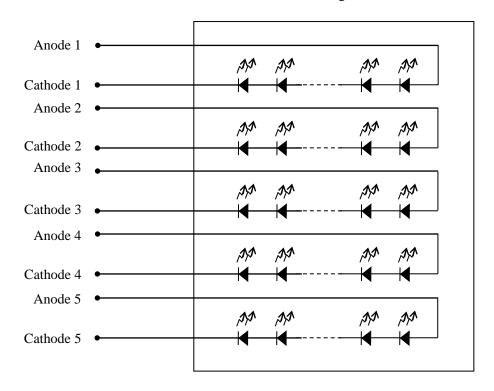
GND-FG Connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds to be connected together in customer equipment.



Note3: Backlight in detail

Backlight





4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$260.5 \pm 0.5 \text{ (W)} \times 203.0 \pm 0.5 \text{ (H)} \times 8.7 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	246.0 (H) × 184.5 (V)	Note1	mm
Weight	490 (typ.), 540 (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

	Paramete	r	Symbol	Rating	Unit	Remarks	
Power supply voltage	LCD panel si	ignal processing board	VCC	-0.3 to +6.5	V		
Input voltage	Dis	splay signals Note1	VD	-0.3 to VCC+0.3	V	Ta= 25°C	
for signals	Fui	nction signal Note2	VF	-0.3 to VCC+0.3	•		
Backlight	For	ward current	IL	60	mA	per one circuit	
	Storage tempe	rature	Tst	-30 to +80	°C	-	
0		Front surface	TopF	-30 to +80	°C	Note3	
Operating te	mperature	Rear surface	TopR	-30 to +80	°C	Note4	
				≤ 95	%	Ta ≤ 40°C	
				≤ 85	%	$40^{\circ}C < Ta \leq 50^{\circ}C$	
	Relative hum Note5	idity	RH	≤ 55	%	$50^{\circ}\text{C} < \text{Ta} \le 60^{\circ}\text{C}$	
	110163			≤ 36	%	60°C < Ta ≤ 70°C	
				≤ 24	%	70°C < Ta ≤ 80°C	
	Absolute hum Note5	nidity	АН	≤ 70 Note6	g/m ³	Ta= 80°C	

Note1: CLK, Hsync, Vsync, DE, DATA (R0 to R5, G0 to G5, B0 to B5)

Note2: DPS

Note3: Measured at LCD panel surface (including self-heat)

Note4: Measured at LCD module's rear shield surface (including self-heat)

Note5: No condensation

Note6: Water amount at Ta= 80°C and RH= 24%



4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

(Ta= 25°C, Note1)

Parameter	Parameter			typ.	max.	Unit	Remarks
Power supply voltage		VCC	3.0	3.3	3.6	V	at VCC= 3.3V
Tower suppry voltage		VCC	4.75	5.0	5.25	V	at VCC= 5.0V
Power supply current		ICC	-	310 Note2	460 Note3	mA	at VCC= 3.3V
Tower suppry current		icc	-	210 Note2	300 Note3	mA	at VCC= 5.0V
Logic input voltage for	High	VDH	0.7VCC	-	VCC	V	
display signals	Low	VDL	0	-	0.3VCC	V	CMOC level
I to be C DDC : 1	High	VFH	0.7VCC	-	VCC	V	CMOS level
Input voltage for DPS signal	Low	VFL	0	-	0.3VCC	V	

Note1: When designing of the power supply, take the measures for the prevention of surge voltage.

Note2: Checkered flag pattern [by IEC 61747-6]

Note3: Pattern for maximum current



4.3.2 Backlight

(Ta= 25°C, Note1, Note2, Note3)

Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	50.0	55.0	mA	-
		21.2	24.0	27.2		Ta= +25°C at IL= 50mA /One circuit
	VL	19.0	-	-		Ta= +80°C at IL= 50mA /One circuit
Forward Voltage		-	-	29.8	V	Ta= -30°C at IL= 50mA /One circuit
		-	-	30.1		Ta= -30°C at IL= 55mA /One circuit

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The Luminance uniformity may be changed depending on the current variation between 5 circuits. It is recommended that the current value difference among the circuits be less than 5%.

4.3.3 Power supply voltage ripple

This product works even if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power supp	ly voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3 V	≤ 100	mVp-p
VCC	5.0 V	≤ 100	mVp-p

Note1: The permissible ripple voltage includes spike noise.

4.3.4 Fuse

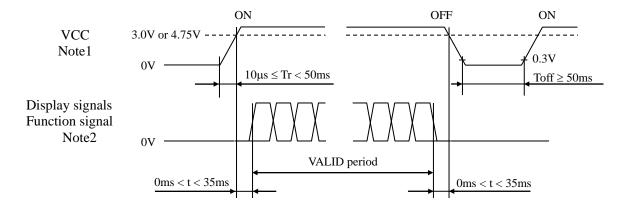
Doromatar		Fuse	Dating	Eusing ourrent	Remarks	
Parameter	Parameter Type Su		Rating	Fusing current	Remarks	
VCC	VOC FCC1C202AP KA		2.0A	4.0A	Note1	
VCC	FCC16202AB	Co., Ltd.	36V	4.0A	note1	

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.



4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board

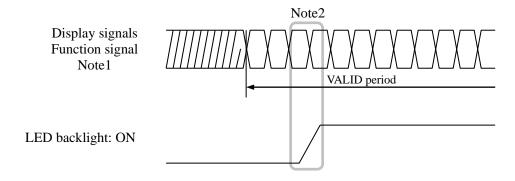


Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V in "VCC= 3.3V" or 4.75V in "VCC= 5.0V", there is a possibility that a product does not work due to a protection circuit.

Note2: Display signals (CLK, Hsync, Vsync, DE, DATA (R0 to R5, G0 to G5, B0 to B5)) and function signal (DPS) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.

If some of display and function signals of this product are cut while this product is working, even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

4.4.2 LED driver



Note1: These are the display and function signals for LCD panel signal processing board.

Note2: The backlight should be turned on within the VALID period of display and function signals, in order to avoid unstable data display.



4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): DF9C-41P-1V (2*) (Hirose Electric Co., Ltd. (HRS))
Adaptable plug: DF9-41S-1V (2*), DF9-41S-1V (3*) (Hirose Electric Co., Ltd. (HRS))

Pin No.	Symbol	Signal	Remarks
1	GND	Ground	Note1
2	CLK	Dot clock	-
3	GND	Ground	Note1
4	Hsync	Horizontal synchronous signal	
5	Vsync	Vertical synchronous signal	
6	GND	Ground	
7	GND	Ground	Note1
8	GND	Ground	
9	R0	Red data (LSB)	Least significant bit
10	R1	Red data	
11	R2	Red data	-
12	GND	Ground	Note1
13	R3	Red data	
14	R4	Red data	
15	R5	Red data (MSB)	Most significant bit
16	GND	Ground	
17	GND	Ground	Note1
18	GND	Ground	7
19	G0	Green data (LSB)	Least significant bit
20	G1	Green data	
21	G2	Green data	-
22	GND	Ground	Note1
23	G3	Green data	
24	G4	Green data	
25	G5	Green data (MSB)	Most significant bit
26	GND	Ground	
27	GND	Ground	Note1
28	GND	Ground	
29	В0	Blue data (LSB)	Least significant bit
30	B1	Blue data	
31	B2	Blue data	-
32	GND	Ground	Note1
33	В3	Blue data	
34	B4	Blue data	
35	B5	Blue data (MSB)	Most significant bit
36	GND	Ground	Note1
37	DE	Selection of DE / Fixed mode	High or Open: Fixed mode Data enable signal: DE mode
38	N. C.	-	Keep this pin Open.
39	VCC	Power supply	
40	VCC	Power supply	Note1
41	DPS	Selection of scan direction	High: Reverse scan Low or Open: Normal scan Note2

Note1: All VCC and GND terminals should be used without any non-connected lines.

Note2: See "4.8 SCANNING DIRECTIONS".

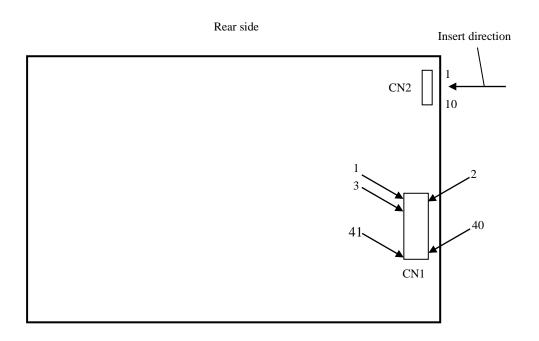


4.5.2 Backlight

CN2 socket (LCD module side): SM10B-SRSS-TB (J.S.T. Mfg. Co., Ltd.)
Adaptable plug: SHR-10V-S, SHR-10V-S-B (J.S.T. Mfg. Co., Ltd.)

Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	K1	Cathode1	-
3	A2	Anode2	-
4	K2	Cathode2	-
5	A3	Anode3	-
6	K3	Cathode3	-
7	A4	Anode4	-
8	K4	Cathode4	-
9	A5	Anode5	-
10	K5	Cathode5	-

4.5.3 Positions of socket





4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

This product can display 262,144 colors with 64 gray scales. Also the relation between display colors and input data signals is as follows.

D:1-	1						Data	a sigi	nal (0:	Low	level	, 1: H	igh le	evel)					
Dispia	y colors	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G4	G3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	В 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
Basic colors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
col	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
asic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
Be	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
le	, ,	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
sca	dark ↑	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	↑										:						:		
g ps	↓ bright	1	1	1	: 1	0	1	0	0	0	:	0	0	0	0	0	:	0	0
Re	bright	1 1	1	1 1	1	0 1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
4)	Diack	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
cal	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
ay s	\uparrow			:							:						:		
Green gray scale	\downarrow			:	:						:						:		
reer	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
Ü		0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
<u>e</u>		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
scal	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
Blue gray scale	↑			:	:						:						:		
e gī	\downarrow			:	:						:						:		
Blu	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1



4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel (See "4.8 SCANNING DIRECTIONS".).

C (0, 0) R G	В					
C(0, 0)	C(1, 0)	• • •	C(X, 0)	• • •	C(798, 0)	C(799, 0)
C(0, 1)	C(1, 1)	•••	C(X, 1)	• • •	C(798, 1)	C(799, 1)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	• • •
•	•	•	•	•	•	•
C(0, Y)	C(1, Y)	• • •	C(X, Y)	• • •	C(798, Y)	C(799, Y)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	•
•	•	•	•	•	•	•
C(0, 598)	C(1, 598)	•••	C(X, 598)	• • •	C(798, 598)	C(799, 598)
C(0, 599)	C(1, 599)	•••	C(X, 599)	•••	C(798, 599)	C(799, 599)

4.8 SCANNING DIRECTIONS

The following figures are seen from a front view.

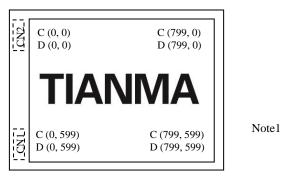


Figure 1. Normal scan (DPS: Low or Open)

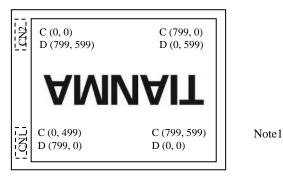


Figure 2. Reverse scan (DPS: High)

Note1: Meaning of C (X, Y) and D (X, Y)

C (X, Y): The coordinates of the display position (See "**4.7 DISPLAY POSITIONS**".) D (X, Y): The data number of input signal for LCD panel signal processing board

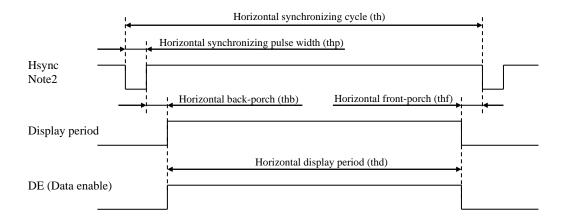


4.9 INPUT SIGNAL TIMINGS

4.9.1 Outline of input signal timings

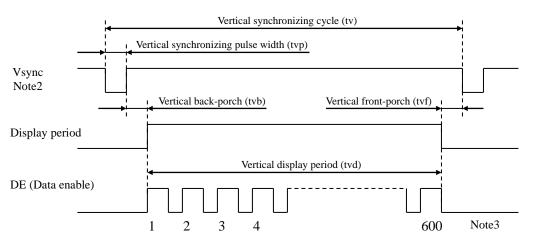
• Horizontal signal

Note1



• Vertical signal

Note1



Note1: This diagram indicates virtual signal for set up to timing.

Note2: Fixed mode cannot be used while working of DE mode.

Note3: See "4.9.3 Input signal timing chart" for the pulse number.



4.9.2 Timing characteristics

(a) Fixed mode

(Note1)

Parameter			Symbol	min.	typ.	max.	Unit	Remarks
CLK	Frequency		1/tc	34.0	38.362	42.0	MHz	26.067 ns (typ.)
	Duty ratio		tcd	0.4	0.5	0.6	-	
	Rise time	e, Fall time	terf	-	-	10	ns	-
DATA	CLV DATA	Setup time	tds	3	-	-	ns	
(R0-R5) (G0-G5)	CLK-DATA	Hold time	tdh	5	-	-	ns	-
(B0-B5)	Rise time	e, Fall time	tdrf	1	-	10	ns	
	C	ycle	th	24.0	26.693	30.1	μs	37.463 kHz (typ.)
	C	ycie	uii		1,024		CLK	
	Displa	y period	thd		800		CLK	
	Front-porch		thf	24		CLK	-	
Hsync	Pulse width		thp	12	72	1	CLK	
HSylic	Back-porch		thb	1	128	188	CLK	
	Total of pulse width and back-porch		thp + thb	200		CLK	Note2	
	CLK- Hsync	Setup time	ths	3	-	i	ns	
		Hold time	thh	5	-	1	ns	-
	Rise time, Fall time		thrf	1	-	10	ns	
	Cycle		tv	16.1	16.683	17.2	ms	59.94 Hz (typ.)
				625			Н	
	Display period		tvd	600			Н	
	Front-porch		tvf	1		Н	-	
Vsync	Pulse width		tvp	1	2	1	Н	
	Back-porch		tvb	1	22	23	Н	
	Total of pulse width and back-porch		tvp + tvb		24		Н	Note2
	Heyne, Veyne	Hsync-Vsync Setup time	Setup time	tvhs	3	-	-	ns
	113ync- v sync	Hold time	tvhh	5	-	ı	ns	-
	Rise time, Fall time		tvrf	-	-	10	ns	

Note1: Definition of parameters is as follows.

tc = 1CLK, tcd = tch/tc, th = 1H

Note2: Keep tvp + tvb and thp + thb within the table. If it is out of specification, display position will be shifted to right/left side or up/down.



(b) DE mode

(Note1, Note2, Note3)

Parameter			Symbol	min.	typ.	max.	Unit	Remarks		
	Frequency		1/tc	34.0	38.362	42.0	MHz	26.067 ns (typ.)		
CLK	Duty ratio		tcd	0.4	0.5	0.6	-			
	Rise time, Fall time		terf	-	-	10	ns	-		
DATA	CL II D ATTA	Setup time	tds	3	-	-	ns			
(R0-R5) (G0-G5)	CLK-DATA	Hold time	tdh	5	-	-	ns	-		
(B0-B5)	Rise time, Fall time		tdrf	-	-	10	ns			
	Horizontal	Cycle	th	24.0	26.693	30.1	μs	27.462.111.77		
				-	1,024	-	CLK	37.463 kHz (typ.)		
		Display period	thd		800		CLK	-		
	Vertical (One frame)	Cools	4	16.1	16.683	17.2	ms	50.04 H= (t)		
DE				tv	-	625	-	Н	59.94 Hz (typ.)	
		Display period	tvd		600		Н	-		
	CLK-DE	Setup time	tdes	3	-	-	ns			
		Hold time	tdeh	5	-	-	ns	-		
	Rise time	e, Fall time	tderf	-	-	10	ns			

Note1: Definition of parameters is as follows.

tc = 1CLK, tcd = tch/tc, th = 1H

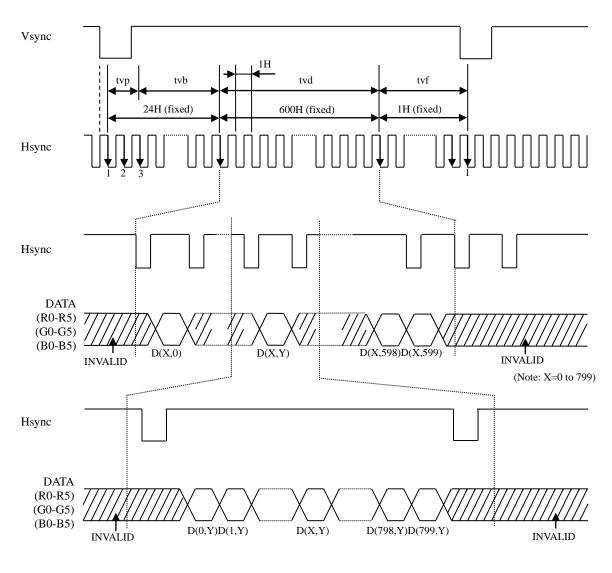
Note2: Hsync signal (Pin No.4 of CN1) and Vsync signal (Pin No.5 of CN1) are not used inside the product at DE mode. Do not keep these pins open to avoid noise problem.

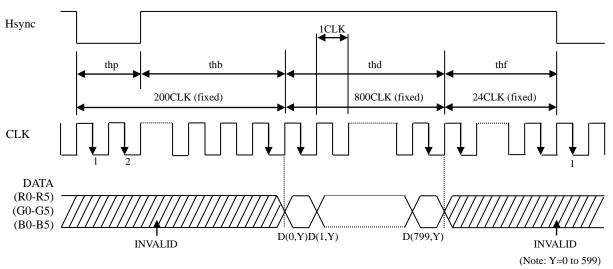
Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).



4.9.3 Input signal timing chart

(a) Fixed mode

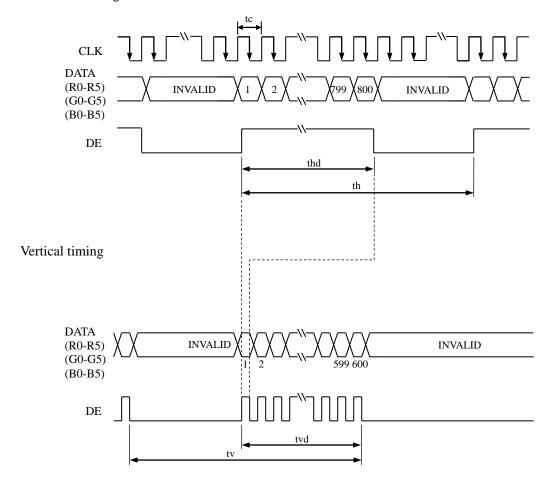






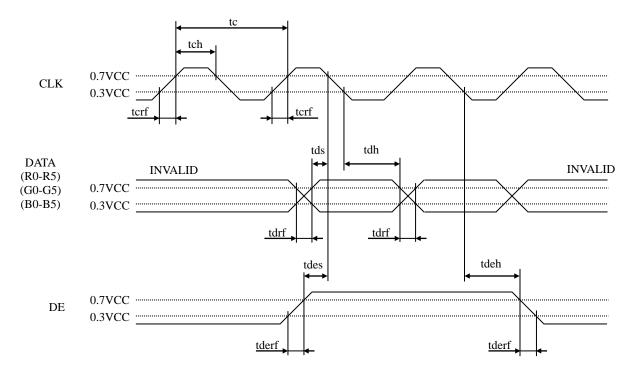
(b) DE mode

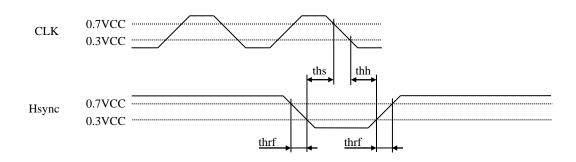
Horizontal timing

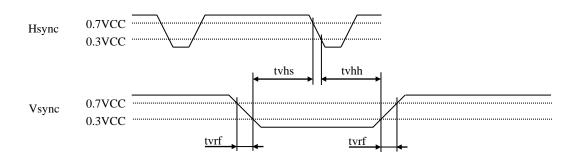




(c) Common item of Fixed mode and DE mode









4.10 OPTICS

4.10.1 Optical characteristics

(Note1, Note2)

Parameter		Condition	Symbol	min.	typ.	max.	Unit	Measuring instrument	Remarks
Luminance		White at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	L	550	900	-	cd/m ²	BM-5A or equivalent	-
Contrast ratio		White/Black at center $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	CR	500	1000	-	-	BM-5A or equivalent	Note3
Luminance uniformity		White $\theta R = 0^{\circ}$, $\theta L = 0^{\circ}$, $\theta U = 0^{\circ}$, $\theta D = 0^{\circ}$	LU	-	1.25	1.4	-	BM-5A or equivalent	Note4
	3371 14	x coordinate	Wx	0.263	0.313	0.363	-		
	White	y coordinate	Wy	0.279	0.329	0.379	-		
	Red	x coordinate	Rx	-	0.565	-	-		r N. 5
C1		y coordinate	Ry	-	0.340	-	-		
Chromaticity	Green	x coordinate	Gx	-	0.350	-	-	SR-3 or	
		y coordinate	Gy	-	0.540	-	-	equivalent	Note5
	DI	x coordinate	Bx	-	0.155	-	-		
	Blue	y coordinate	By	-	0.130	-	-		
Color gamut		θ R= 0°, θ L= 0°, θ U= 0°, θ D= 0° at center, against NTSC color space	С	35	40	-	%		
D 4		White to Black	Ton	1	3	5	ms	BM-5A or	Note6
Response time		Black to White	Toff	-	15	21	ms	equivalent	Note7
Viewing angle	Right	θ U= 0°, θ D= 0°, $CR \ge 10$	θR	70	80	-	0		
	Left	θ U= 0°, θ D= 0°, CR \geq 10	θL	70	80	-	0	EZ	Nota 9
viewing angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	0	Contrast	Note8
	Down	$\theta R=0^{\circ}, \theta L=0^{\circ}, CR \ge 10$	θD	70	80	1	0		

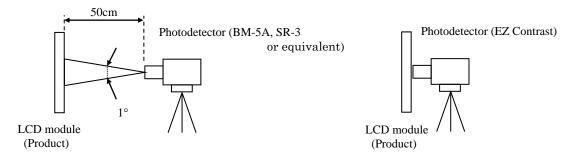
Note1: These are initial characteristics.

Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 50 mA/One circuit, Display mode: SVGA,

Horizontal cycle= 1/37.463kHz, Vertical cycle= 1/59.94Hz, DPS= Low or Open: Normal scan

Optical characteristics are measured at luminance saturation 20minutes after the product works in the dark room. Also measurement methods are as follows.



Note3: See "4.10.2 Definition of contrast ratio".

Note4: See "4.10.3 Definition of luminance uniformity".

Note5: These coordinates are found on CIE 1931 chromaticity diagram.

Note6: Product surface temperature: TopF= 29°C Note7: See "**4.10.4 Definition of response times**". Note8: See "**4.10.5 Definition of viewing angles**".



4.10.2 Definition of contrast ratio

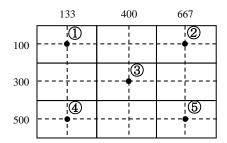
The contrast ratio is calculated by using the following formula.

4.10.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

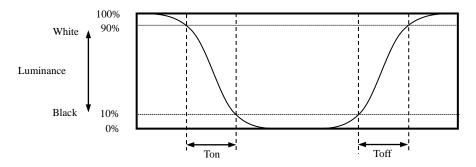
$$Luminance\ uniformity\ (LU) = \ \frac{Maximum\ luminance\ from\ \textcircled{1}\ to\ \textcircled{5}}{Minimum\ luminance\ from\ \textcircled{1}\ to\ \textcircled{5}}$$

The luminance is measured at near the 5 points shown below.

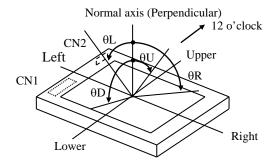


4.10.4 Definition of response times

Response time is measured at the time when the luminance changes from "white" to "black", or "black" to "white" on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).



4.10.5 Definition of viewing angles





5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

	Condition	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit
LED	25°C (Ambient temperature of the product) Continuous operation, IL= 50mA/One circuit	70,000	h
elementary substance	80°C (Surface temperature at screen) Continuous operation, IL= 50mA/One circuit		h

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.

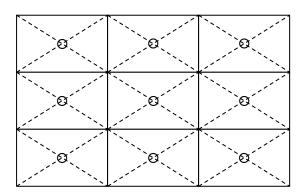


6. RELIABILITY TESTS

Test item	Condition	Judgment Note1		
High temperature and humidity (Operation)	 60 ± 2°C, RH= 90%, 240hours Display data is black. 			
High temperature (Operation)	 80 ± 3°C, 240hours Display data is black. 	No display malfunctions		
Heat cycle (Operation)	 -30 ± 3°C1hour 80 ± 3°C1hour 50cycles, 4 hours/cycle Display data is black. 			
Thermal shock (Non operation)	1 -30 ± 3°C30minutes 80 ± 3°C30minutes 2 100cycles, 1hour/cycle 3 Temperature transition time is within 5 minutes.			
ESD (Operation)	 150pF, 150Ω, ±10kV 9 places on a panel surface Note2 10 times each place at 1 sec interval 			
Dust (Operation)	 Sample dust: No. 15 (by JIS-Z8901) 15 seconds stir 8 times repeat at 1 hour interval 			
Vibration (Non operation)	 5 to 100Hz, 19.6m/s² 1 minute/cycle X, Y, Z directions 120 times each direction 	No display malfunctions		
Mechanical shock (Non operation)	 539m/ s², 11ms ±X, ±Y, ±Z directions 5 times each direction 	No physical damages		

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.





7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!**



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

7.2 CAUTIONS



* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 539m/s² and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N (ϕ 16mm jig))

7.3 ATTENTIONS



7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② When the product is put on the table temporarily, display surface must be placed downward.
- ③ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- ⓐ The torque for product mounting screws must never exceed 0.23N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be ≤ 2.0mm.
- ⑤ The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑥ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ① Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is recommended for protection of product surface. Adhesive type protection sheet may change color or characteristics of the polarizer.
- Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.



7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
- ③ Do not operate in high magnetic field. If not, circuit boards may be broken.
- 4) This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- ③ Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- ④ The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

7.3.4 Others

- ① All GND and VCC terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ See "REPLACEMENT MANUAL FOR LAMP HOLDER SET", when replacing lamp holder set.
- 4 Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to TMJ for repairing and so on.
- (5) The information of China RoHS (II) six hazardous substances or elements in this product is as follows.

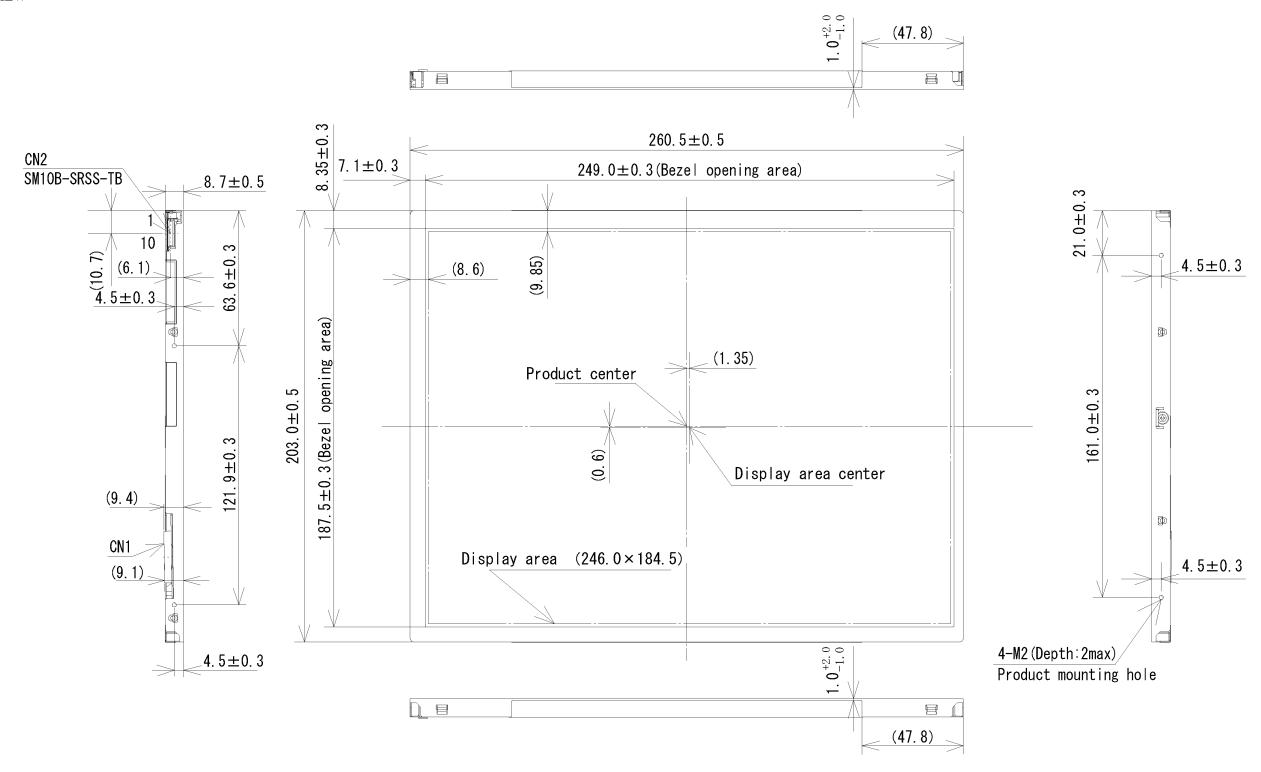
China RoHS (II) six hazardous substances or elements							
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)			
×	0	0	0	0	0		

- Note1: O: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of GB/T26572-2011 standard regulation.
 - X: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of GB/T26572-2011 standard regulation.



8. OUTLINE DRAWINGS

8.1 FRONT VIEW

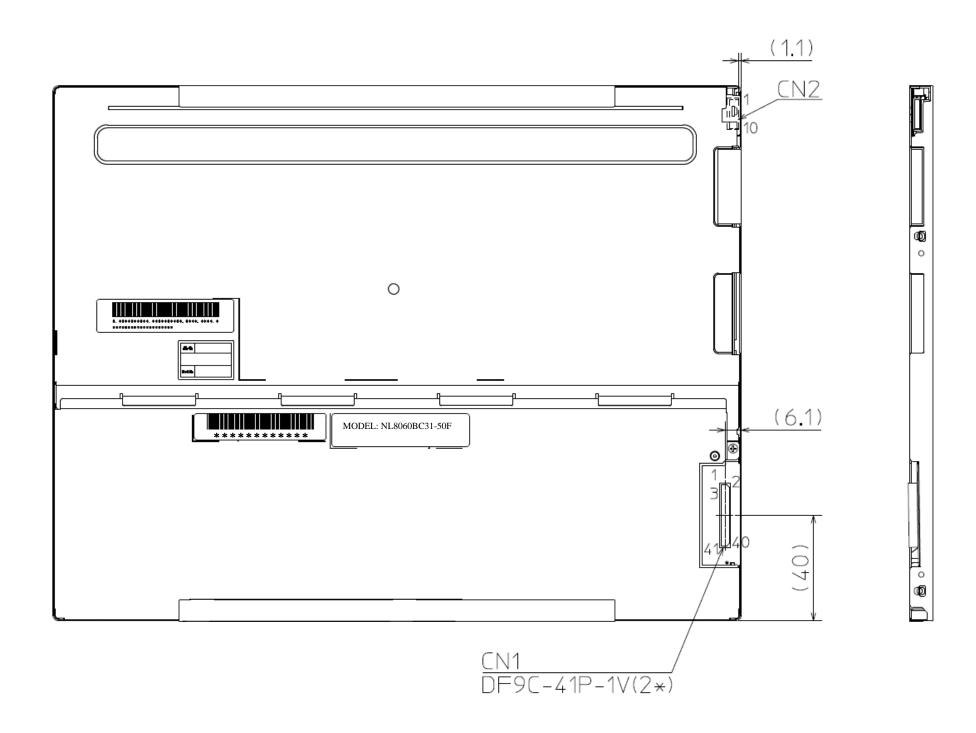


Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed 0.23 N·m. And the length of product mounting screws must be \leq 2.0mm.

Unit: mm

8.2 REAR VIEW



Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed 0.23 N·m. And the length of product mounting screws must be \leq 2.0mm.

Unit: mm