

<IGBT Modules>

CM450DX-34T/CM450DXP-34T

HIGH POWER SWITCHING USE

INSULATED TYPE



- Flat base type
- •Copper base plate (Nickel-plating)
- •RoHS Directive compliant
- •Tin-plating pin terminals



Collector current Ic	4	5	0	Α
Collector-emitter voltage V_{CES}	7	0	0	٧
Maximum junction temperature T _{vjmax}	1	7	5	°C

- Flat base type
- Copper base plate (Nickel-plating)
- RoHS Directive compliant
- Tin-plating pressfit terminals
- •UL Recognized under UL1557, File No. E323585

APPLICATION

AC Motor Control, Motion/Servo Control, Power supply, etc.

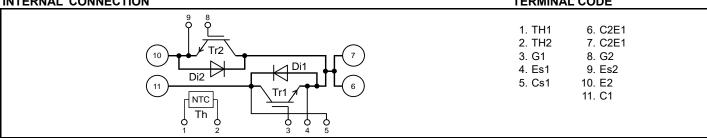
dual switch (half-bridge)

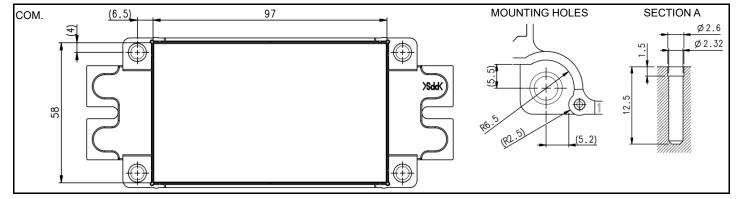
OPTION (Below options are available.)

- ●PC-TIM (Phase Change Thermal Interface Material) pre-apply(Note10)
- V_{CEsat} selection for parallel connection

INTERNAL CONNECTION

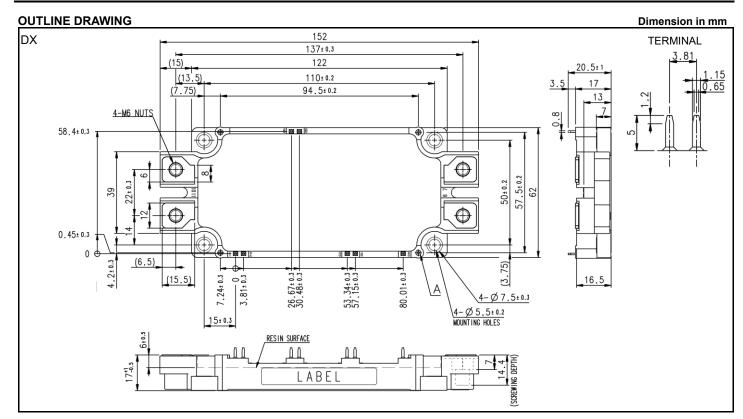
TERMINAL CODE





HIGH POWER SWITCHING USE

INSULATED TYPE

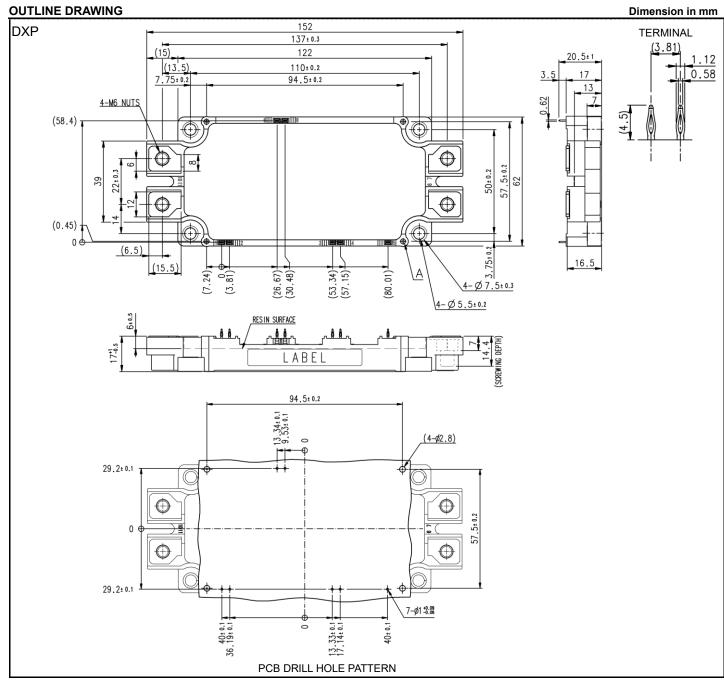


Tolerance otherwise specified

Divisio	Tolerance			
0.5		to	3	±0.2
over	3	to	6	±0.3
over	6	to	30	±0.5
over	30	to	120	±0.8
over 120		to 400		±1.2

HIGH POWER SWITCHING USE

INSULATED TYPE



Tolerance otherwise specified

Division of Dimension				Tolerance				
0.5		to 3		±0.2				
over	3	to	6	±0.3				
over	6	to	30	±0.5				
over	30	to	120	±0.8				
over	120	to 4	400	±1.2				

HIGH POWER SWITCHING USE

INSULATED TYPE

MAXIMUM RATINGS (Tvj=25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Symbol	Item	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E short-circuited	1700	V
V_{GES}	Gate-emitter voltage	C-E short-circuited	± 20	V
Ic	Collector current	DC, T _C =84 °C (Note2, 4)	450	^
I _{CRM}	Collector current	Pulse, Repetitive (Note3)	900	Α
P _{tot}	Total power dissipation	T _C =25 °C (Note2, 4)	2235	W
l _E (Note1)	Cmitter current	DC (Note2)	450	^
I _{ERM} (Note1)	Emitter current	Pulse, Repetitive (Note3)	900	Α

MODULE

Symbol	Item	Conditions	Rating	Unit
V _{isol}	Isolation voltage	Terminals to base plate, RMS, f=60 Hz, AC 1 min	4000	V
T _{vjmax}	Maximum junction temperature	Instantaneous event (overload) (Note10)	175	°C
T _{Cmax}	Maximum case temperature	(Note4, 10)	125	
T _{vjop}	Operating junction temperature	Continuous operation (under switching) (Note10)	-40 ~ +150	°C
T _{stq}	Storage temperature	-	-40 ~ +125	C

ELECTRICAL CHARACTERISTICS (T $_{vj}$ =25 °C, unless otherwise specified)

INVERTER PART IGBT/FWD

Cumahad	14	Item Conditions M			Limits		1.1:4
Symbol	item	Conditions			Тур.	Max.	Unit
I _{CES}	Collector-emitter cut-off current	V _{CE} =V _{CES} , G-E short-circuited		-	-	1.0	mA
I _{GES}	Gate-emitter leakage current	V _{GE} =V _{GES} , C-E short-circuited		-	-	0.5	μΑ
$V_{GE(th)}$	Gate-emitter threshold voltage	I _C =45 mA, V _{CE} =10 V		5.4	6.0	6.6	V
.,		I _C =450 A, V _{GE} =15 V,	T _{vj} =25 °C	-	2.05	2.45	
V _{CEsat} (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.45	-	V
(Terrillial)		(Note5)	T _{vj} =150 °C	-	2.55	-	
	Collector-emitter saturation voltage	I _C =450 A,	T _{vj} =25 °C	-	1.95	2.35	
V _{CEsat}		V _{GE} =15 V,	T _{vj} =125 °C	-	2.35	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.45	-	
Cies	Input capacitance			-	-	120	
Coes	Output capacitance	V _{CE} =10 V, G-E short-circuited	V _{CE} =10 V, G-E short-circuited		-	3.3	nF
Cres	Reverse transfer capacitance	· · · ·		-	-	1.1	
Q _G	Gate charge	V _{CC} =1000 V, I _C =450 A, V _{GE} =15 V		-	3.53	-	μC
t _{d(on)}	Turn-on delay time	V_{CC} =1000 V, I_{C} =450 A, V_{GE} =±15 V, R_{G} =0 Ω, Inductive load		-	-	800	ns
tr	Rise time			-	-	200	
t _{d(off)}	Turn-off delay time			-	-	800	
t _f	Fall time			-	-	600	
Note1)		I _E =450 A, G-E short-circuited,	T _{vj} =25 °C	-	2.75	3.35	
V _{EC} (Note1) (Terminal)		Refer to the figure of test circuit	T _{vj} =125 °C	-	2.95	-	V
(Terrillial)	Emitter cellector veltage	(Note5)	T _{vj} =150 °C	-	2.95	-	
Note1)	Emitter-collector voltage	I _E =450 A,	T _{vj} =25 °C	-	2.65	3.25	
V _{EC} (Note1) (Chip)		G-E short-circuited,	T _{vj} =125 °C	-	2.75	-	V
(Chip)		(Note5)	T _{vj} =150 °C	-	2.75	-	
t _{rr} (Note1)	Reverse recovery time	V_{CC} =1000 V, I_E =450 A, V_{GE} =±15 V,		-	-	300	ns
Q _{rr} (Note1)	Reverse recovery charge	R_G =0 Ω , Inductive load			21.5	-	μC
Eon	Turn-on switching energy per pulse	V _{CC} =1000 V, I _C =I _E =450 A,			113.4	-	m
E _{off}	Turn-off switching energy per pulse	V _{GE} =±15 V, R _G =0 Ω, T _{vj} =150 °C,		-	116.4	-	mJ
E _{rr} (Note1)	Reverse recovery energy per pulse	Inductive load	Inductive load		56.3	-	mJ
R _{CC'+EE'}	Internal lead resistance	Main terminals-chip, per switch, T _C =25 °C	(Note4)		0.75	-	mΩ
r _g	Internal gate resistance	Per switch		-	1.7	-	Ω

HIGH POWER SWITCHING USE

INSULATED TYPE

ELECTRICAL CHARACTERISTICS (cont.; Tvj=25 °C, unless otherwise specified)

NTC THERMISTOR PART

Symbol	Item	Conditions		Unit		
		Conditions	Min.	Тур.	Max.	Offile
R ₂₅	Zero-power resistance	T _C =25 °C (Note4)		5.00	5.15	kΩ
ΔR/R	Deviation of resistance	R ₁₀₀ =493 Ω, T _C =100 °C (Note4)		-	+7.8	%
B _(25/50)	B-constant	Approximate by equation (Note6)	-	3375	-	K
P ₂₅	Power dissipation	T _C =25 °C (Note4)	-	-	10	mW

THERMAL RESISTANCE CHARACTERISTICS

Symbol	Item	Conditions		Limits			Unit
Symbol		Conditions		Min.	Тур.	Max.	Offic
$R_{th(j-c)Q}$	Thermal resistance Junction to case, per Inverter IGBT (Note4)				-	67	K/kW
$R_{th(j-c)D}$	Thermai resistance	Junction to case, per Inverter FWD (Note4)		-	-	101	r/KVV
В	Contact thermal resistance	Case to heat sink,	Thermal grease applied (Note4, 7,10)	-	11.5	-	K/kW
$R_{th(c-s)}$		per 1 module,		PC-TIM applied (Note4, 8,10)	-	3.1	-

MECHANICAL CHARACTERISTICS

Cumbal	Itam	Conditions		Limits			Unit
Symbol	Item	Conc	Conditions			Max.	Unit
M _t	Mounting torque	Main terminals	M 6 screw	3.5	4.0	4.5	N·m
Ms	Mounting torque	Mounting to heat sink	M 5 screw	2.5	3.0	3.5	N·m
		Colder nin tune (DV)	Terminal to terminal	17	-	-	ma ma
	Creepage distance	Solder pin type (DX)	Terminal to base plate	18.1	-	-	mm
d _s		Pressfit pin type (DXP)	Terminal to terminal	17	-	-	
			Terminal to base plate	18.6	-	-	mm
		0.11	Terminal to terminal	10	-	-	ma ma
d		Solder pin type (DX)	Terminal to base plate	16.2	-	-	mm
d _a	Clearance	Descrit win to the (DVD)	Terminal to terminal	10	-	-	
		Pressfit pin type (DXP)	Terminal to base plate	16.2	-	-	mm
ec	Flatness of base plate	On the centerline X, Y (Notes))	±0	-	+200	μm
m	mass	-	-		300	-	g

^{*.} This product is compliant with the Restriction of the Use of Certain Hazardous Substances in Electrical and Electronic Equipment (RoHS) directive 2011/65/EU and (EU) 2015/863.

Note1. Represent ratings and characteristics of the anti-parallel, emitter-collector free-wheeling diode (FWD).

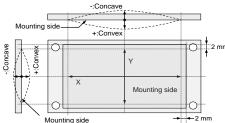
- 2. Junction temperature (T_{vj}) should not increase beyond T_{vjmax} rating.
- 3. Pulse width and repetition rate should be such that the device junction temperature (T_{vj}) dose not exceed $T_{vj\,m\,a\,x}$ rating.
- 4. Case temperature (T_C) and heat sink temperature (T_S) are defined on the each surface (mounting side) of base plate and heat sink just under the chips. Refer to the figure of chip location.
- 5. Pulse width and repetition rate should be such as to cause negligible temperature rise. Refer to the figure of test circuit.

6.
$$B_{(25/50)} = ln(\frac{R_{25}}{R_{50}})/(\frac{1}{T_{25}} - \frac{1}{T_{50}})$$

 R_{25} : resistance at absolute temperature T_{25} [K]; $T_{25}\text{=}25$ [°C]+273.15=298.15 [K]

 R_{50} : resistance at absolute temperature T_{50} [K]; T_{50} =50 [°C]+273.15=323.15 [K]

- 7. Typical value is measured by using thermally conductive grease of λ =0.9 W/(m·K)/D_(C-S)=50 μ m.
- 8. Typical value is measured by using PC-TIM of λ =3.4 W/(m·K)/D_(C-S)=50 μ m.
- 9. The base plate (mounting side) flatness measurement points (X,Y) are shown in the following figure.



10. Long term performance related to thermal conductive grease and PC-TIM (including but not limited to aspects such as the increase of thermal resistance due to pumping out, etc.) should be verified under your specific application conditions. Each temperature condition (T_{vj max}, T_{vj op}, T_{C max}) must be maintained below the maximum rated temperature throughout consideration of the temperature rise even for long term usage.

HIGH POWER SWITCHING USE

INSULATED TYPE

Note11. Use the following screws when mounting the printed circuit board (PCB) on the standoffs.

PCB thickness: t1.6

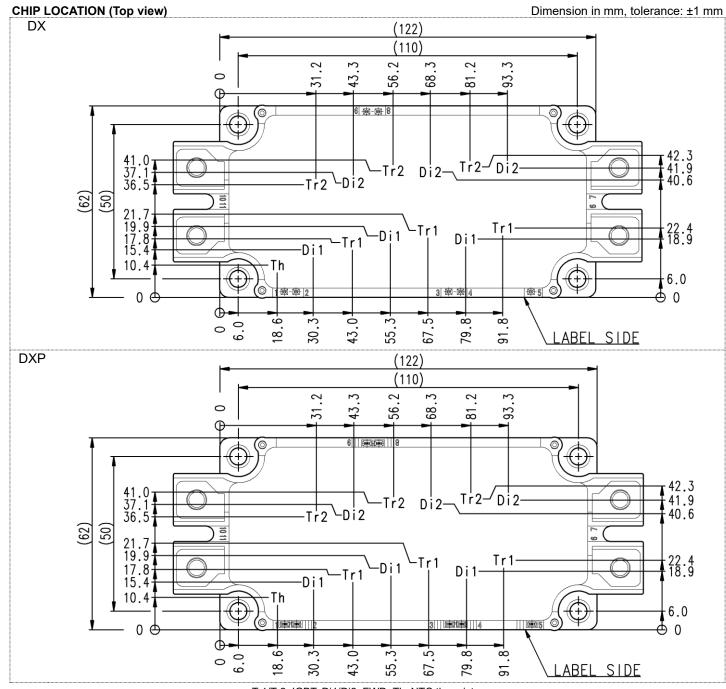
	Туре	Manufacturer	Size	Tightening torque (N·m)	Recommended tightening method
(1)	PT®	EJOT	K25×8	0.55 ± 0.055	
(2)	PT®		K25×10	0.75 ± 0.075 N·m	by handwork (equivalent to 30 rpm
(3)	DELTA PT®		25×8	0.55 ± 0.055 N·m	by mechanical screw driver)
(4)	DELTA PT®		25×10	0.75 ± 0.075 N·m	~ 600 rpm (by mechanical screw driver)
(5)	B1	-	φ2.6×10	0.75 ± 0.075 N·m	
	tapping screw		φ2.6×12	0.75 ± 0.075 N•III	

RECOMMENDED OPERATING CONDITIONS

Symbol	Item	Conditions		Unit		
	item	Conditions	Min.	Тур.	Max.	Offic
V _{cc}	(DC) Supply voltage	Applied across C1-E2 terminals		1000	1200	V
V_{GEon}	Gate (-emitter drive) voltage	Applied across G1-Es1/G2-Es2 terminals	13.5	15.0	16.5	V
R_G	External gate resistance	Per switch	0	-	16	Ω

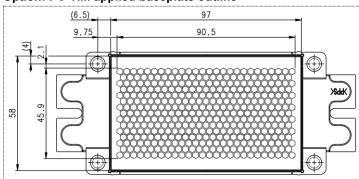
HIGH POWER SWITCHING USE

INSULATED TYPE



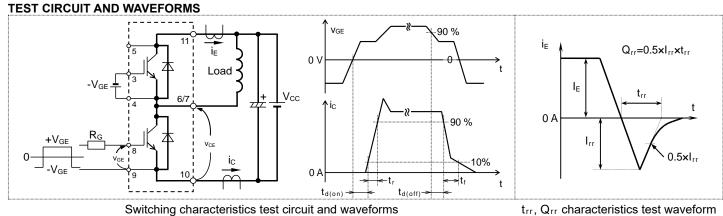
Tr1/Tr2: IGBT, Di1/Di2: FWD, Th: NTC thermistor

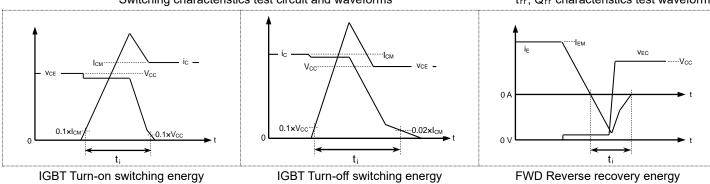
Option: PC-TIM applied baseplate outline



HIGH POWER SWITCHING USE

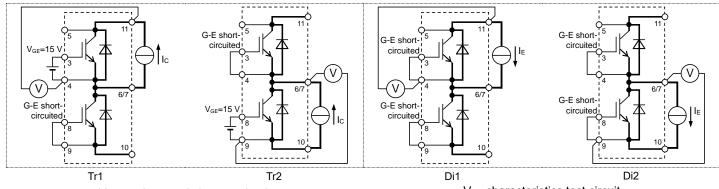
INSULATED TYPE





Switching energy and Reverse recovery energy test waveforms (Integral time instruction drawing)

TEST CIRCUIT



V_{CEsat} characteristics test circuit

V_{EC} characteristics test circuit

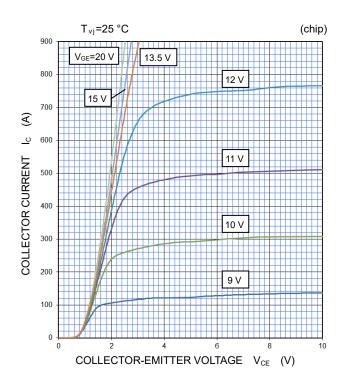
HIGH POWER SWITCHING USE

INSULATED TYPE

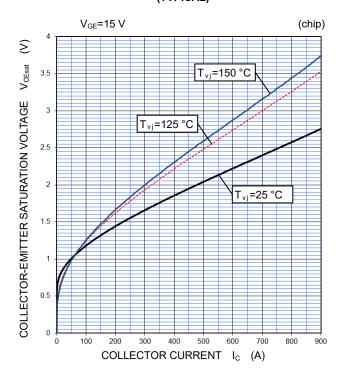
PERFORMANCE CURVES

INVERTER PART

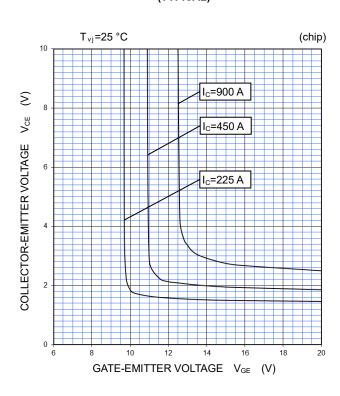
OUTPUT CHARACTERISTICS (TYPICAL)



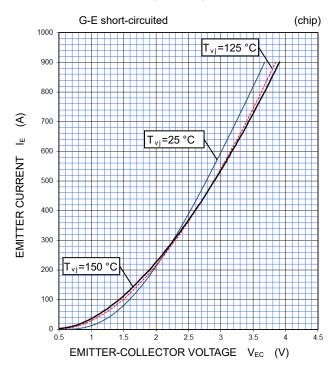
COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)



COLLECTOR-EMITTER VOLTAGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE FORWARD CHARACTERISTICS (TYPICAL)



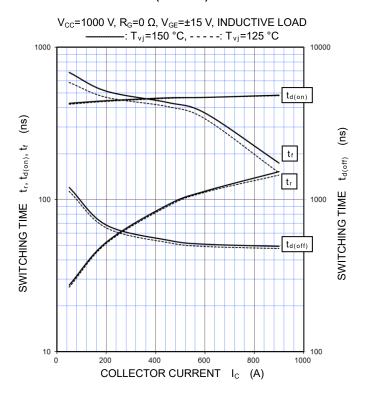
HIGH POWER SWITCHING USE

INSULATED TYPE

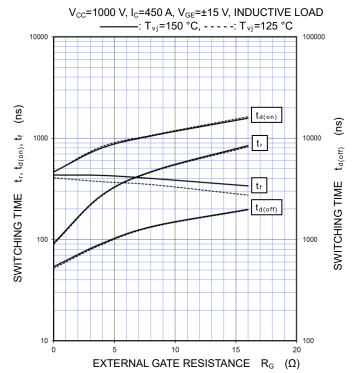
PERFORMANCE CURVES

INVERTER PART

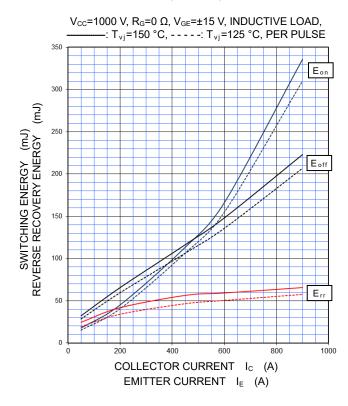
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



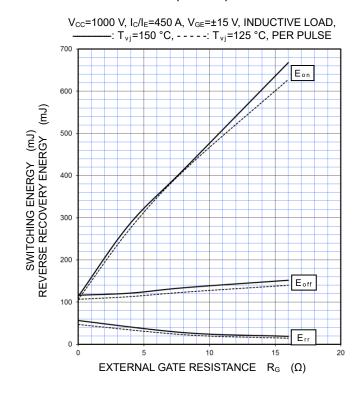
HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



HALF-BRIDGE SWITCHING CHARACTERISTICS (TYPICAL)



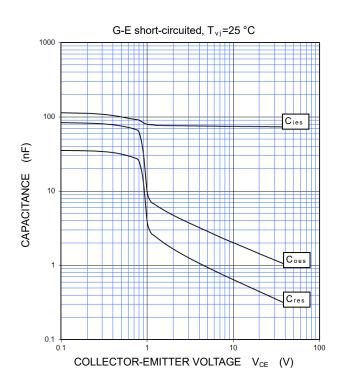
HIGH POWER SWITCHING USE

INSULATED TYPE

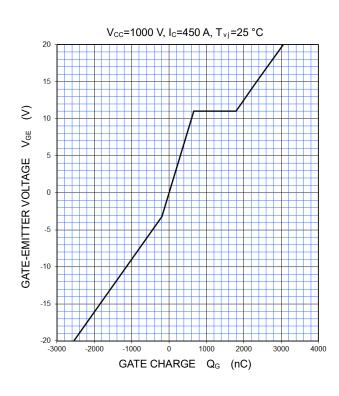
PERFORMANCE CURVES

INVERTER PART

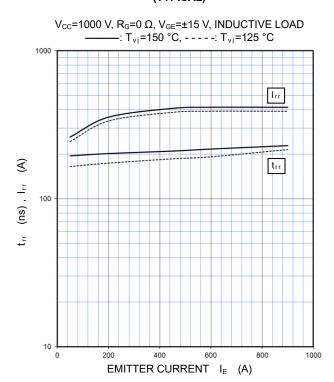
CAPACITANCE CHARACTERISTICS (TYPICAL)



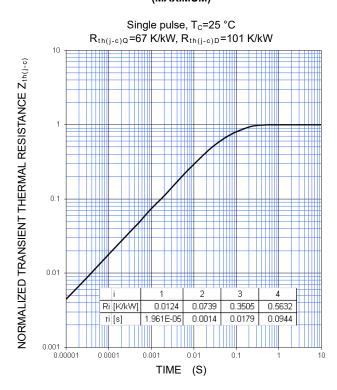
GATE CHARGE CHARACTERISTICS (TYPICAL)



FREE WHEELING DIODE REVERSE RECOVERY CHARACTERISTICS (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (MAXIMUM)



HIGH POWER SWITCHING USE

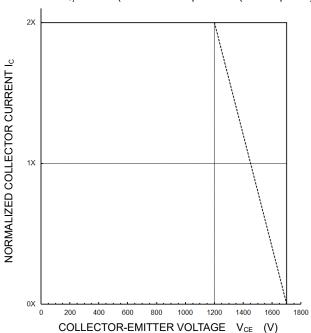
INSULATED TYPE

PERFORMANCE CURVES

INVERTER PART

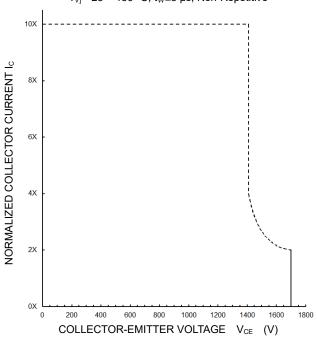
TURN-OFF SWITCHING SAFE OPERATIONG AREA (REVERSE BIAS SAFE OPERATING AREA) (MAXIMUM)

 V_{CC} ≤1200 V, R_G =0~16 Ω, V_{GE} =±15 V, ------: T_{v_j} =25~150 °C (Normal load operations (Continuous) -----: T_{v_j} =175 °C (Unusual load operations (Limited period)



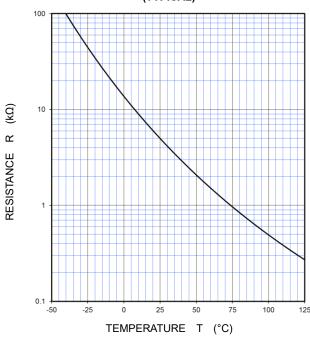
SHORT-CIRCUIT SAFE OPERATING AREA (MAXIMUM)

 $V_{CC} \le 1200 \text{ V}, R_G = 0 \sim 16 \Omega, V_{GE} = \pm 15 \text{ V},$ $T_{vi} = 25 \sim 150 \text{ °C}, t_W \le 8 \mu\text{s}, Non-Repetitive}$



NTC thermistor part

TEMPERATURE CHARACTERISTICS (TYPICAL)



Note: The characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

HIGH POWER SWITCHING USE INSULATED TYPE

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HIGH POWER SWITCHING USE INSULATED TYPE

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