

	SPECIFICATIO	NS
CUSTOMER	: PTC	
SAMPLE CODE	NSC10	602LRU-JWA-H
MASS PRODUCTION CODE	NPC10	602LRU-JWA-H
SAMPLE VERSION	. 01	
SPECIFICATIONS EDITION	. 002	
DRAWING NO. (Ver.)	: JLMD-	NPC1602LRU-JWA-H_001
PACKAGING NO. (Ver.)	: JPKG	NPC1602LRU-JWA-H_001
		POWERTIP
		Date: 2015.05.26 JS RD APPROVED JS RD APPROVED
Approved	Checked	
Approved	Checked 劉進	JS RD APPROVED
閏偉 □ Preliminary specificati ■ Specification for samp	劉進 on for design input	Designer 周志仙



History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
06/13/2014	01	001	New Sample		周志仙
05/23/2015	01	002	Modify IDD Condition	-	周志仙
				То	tal: 30 Pages



Contents

- **1. SPECIFICATIONS**
 - 1.1 Features
 - **1.2 Mechanical Specifications**
 - 1.3 Absolute Maximum Ratings
 - **1.4 DC Electrical Characteristics**
 - **1.5 Optical Characteristics**
 - **1.6 Backlight Characteristics**
- 2. MODULE STRUCTURE
 - 2.1 Counter Drawing
 - 2.2 Interface Pin Description
 - 2.3 Timing Characteristics
 - 2.4 Display Command
 - 2.5 Character Pattern
 - 2.6 Jumper (Setting different use)

3. QUALITY ASSURANCE SYSTEM

- 3.1 Quality Assurance Flow Chart
- 3.2 Inspection Specification

4. RELIABILITY TEST

4.1 Reliability Test Condition

5. PRECAUTION RELATING PRODUCT HANDLING

- 5.1 Safety
- 5.2 Handling
- 5.3 Storage
- 5.4 Terms of Warranty

Appendix :

- 1. LCM drawing
- 2. PKG drawing

Note : For detailed information please refer to IC data sheet : SITRONIX---ST7066U-0A



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Y/G , Positive , Transflective
Driver Condition	LCD Module : 1/16 Duty , 1/5 Bias
Viewing Direction	6 O'clock
Weight	32.2g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website :
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	85.0 (L) * 30.0 (W) *12.7(H)	mm
Viewing Area	66.0 (L) * 16.0 (W)	mm
Active Area	56.2 (L) * 11.5 (W)	mm
Character Size	2.95mm * 5.55mm	mm
Character Pitch	3.55mm * 5.95mm	mm

Note : For detailed information please refer to LCM drawing



1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	Vdd	-	-0.3	7.0	V
LCD Driver Supply Voltage	VLCD	-	Vdd -10.0	Vdd +0.3	V
Input Voltage	Vin	-	-0.3	V _{DD} +0.3	V
Operating Temperature	Тор	-	-20	70	°C
Storage Temperature	Tst	-	-30	80	°C
Storage Humidity	HD	Ta<60 ℃	-	90	%RH

1.4 DC Electrical Characteristics

					Ta = 1	25° ∁
Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	Vdd	-	4.5	5.0	5.5	V
"H" Input Voltage	Vін		0.7 Vdd	-	Vdd	V
"L" Input Voltage	Vil	-	-0.3	-	0.6	V
"H" Output Voltage	Vон	IOH=-0.1mA	3.9	-	Vdd	V
"L" Output Voltage	Vol	IOL=0.1mA	-	-	0.4	V
Supply Current	Idd	Vdd= 5.0 V ,Vop= 4.5V *1	-	2.0	3.0	mA
	Vop	-20 °C	4.4	4.6	4.8	
LCM Driver Voltage		25 ℃	4.3	4.5	4.7	V
	*2	70 ℃	4.1	4.3	4.5	

NOTE: *1 The Maximum current display

*2 The VOP test point is (VDD –V0)



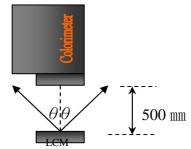
1.5 Optical Characteristics

			LCD Panel	: 1/16 Du	ty,1/5 Bia	as , V _{LCD} :	= 4.5V,	Ta =25℃
Item	Item		Conditions	Min.	Тур.	Max.	Unit	Reference
Posponoo Timo	Rise	tr		-	80	125	m 0	Note 2
Response Time	Fall	tf	-	-	220	330	ms	Note 2
	Τορ θ+		-	40	-			
Viewing angle	Bottom	θ-	C≥2.0	-	40	-	Deg	Note 1
range	Left	θL		-	45	-		
	Right	θR		-	45	-		
Contrast Ra	tio	С	-	-	10	-	-	Note 3
Average Brightness (with LCD) *2		IV	IE 100 m 1	30	35	-	cd/m ²	
Wavelength (with LCD)		λр	IF=100 mA	568	571	574	nm	Note 4
Uniformity '	`1	∆B	IF=100 mA	70	-	-	%	

Note 4 :

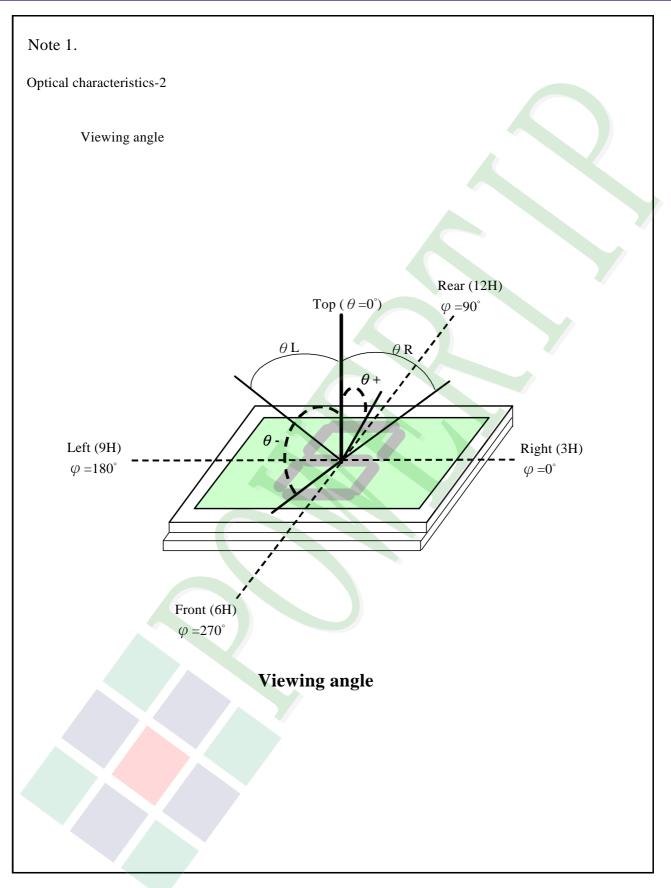
- 1 : △B=B(min) / B(max) * 100%
- 2 : Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C ±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 ± 50 mm \rightarrow (θ = 0°)
 - c: Equipment: TOPCON BM-7 fast , (field 1°) , after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01 , Average Brightness ± 4%



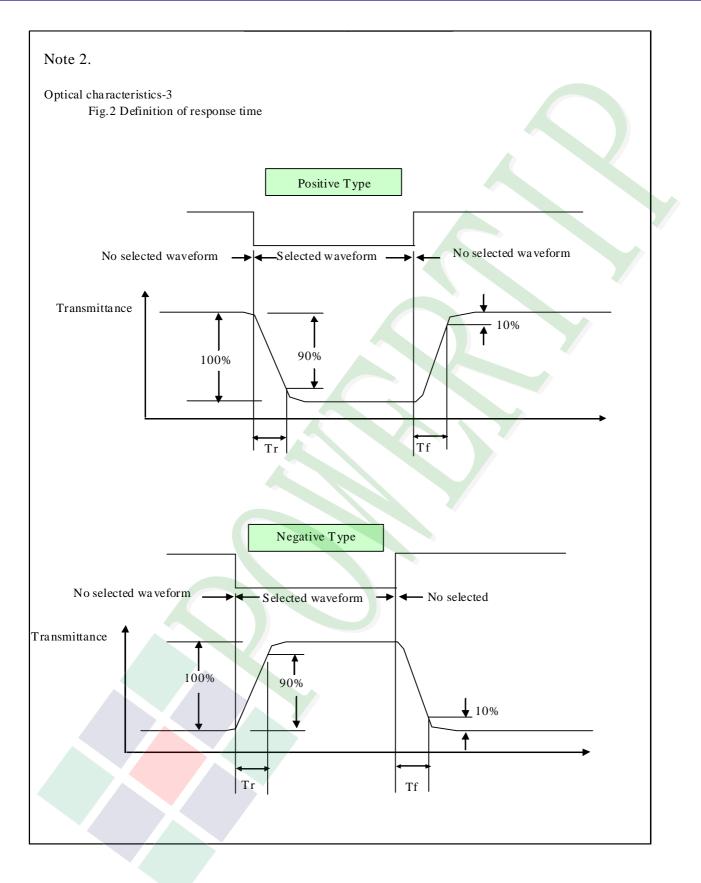


Colorimeter=BM-7 fast

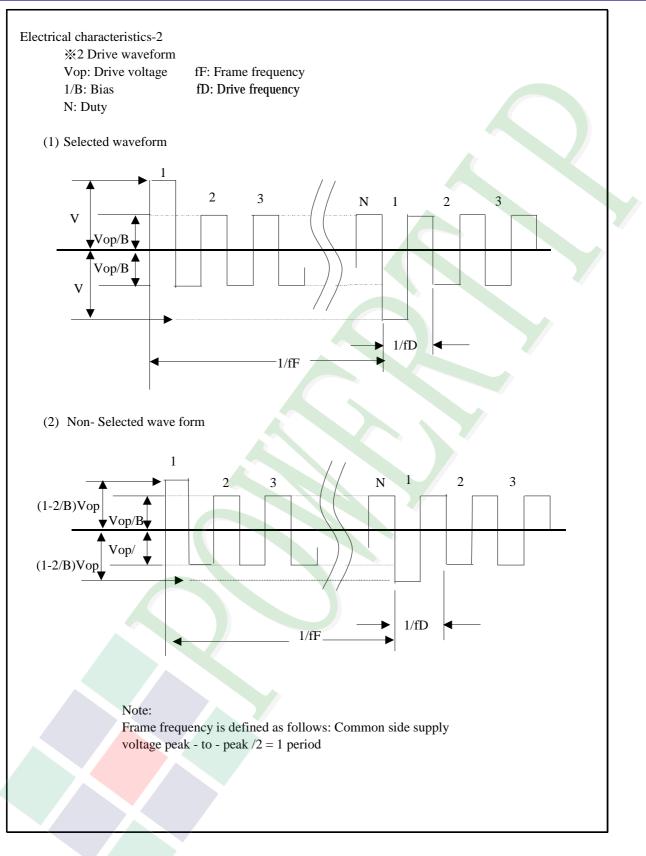




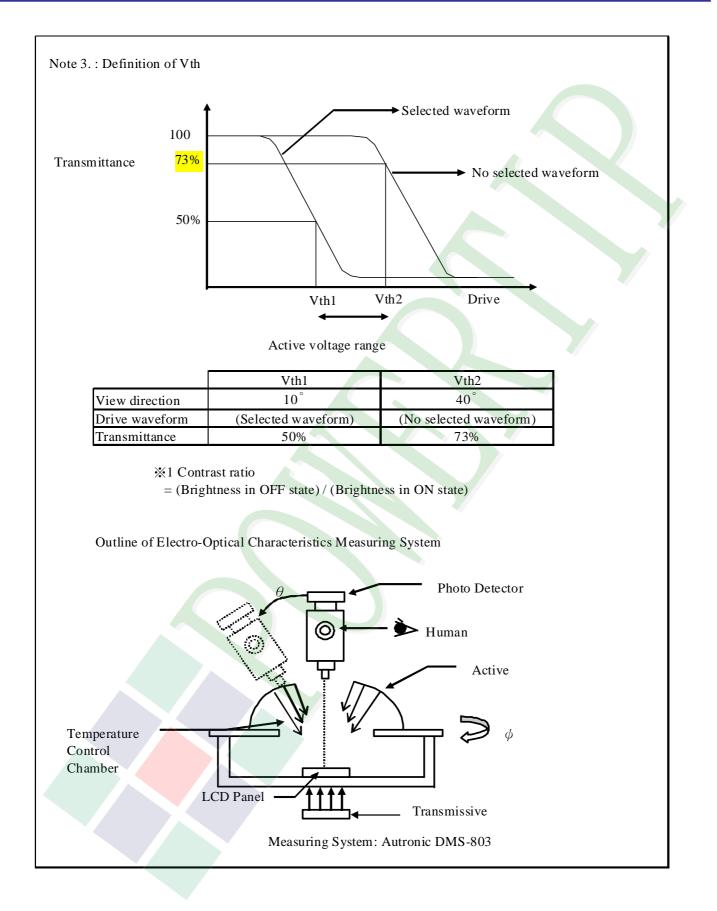














1.6 Backlight Characteristics

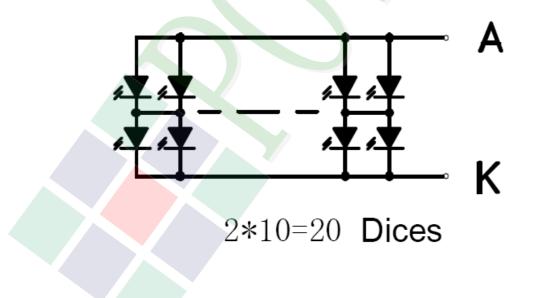
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25 ℃	-	150	mA
Reverse Voltage	VR	Ta =25 ℃	-	8	V
Power Dissipation	PD	Ta =25 ℃	-	660	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage	VF		4.0	4.2	4.4	V	
Reverse Current	IR		-	-	100	uA	
Average Brightness (without LCD)	IV	IF= 100 mA	165	190	-	cd/m ²	
Wavelength (Without LCD)	λρ	$ \land $	569	572	575	nm	
Color	Yellow/Green						

Internal Circuit Diagram:





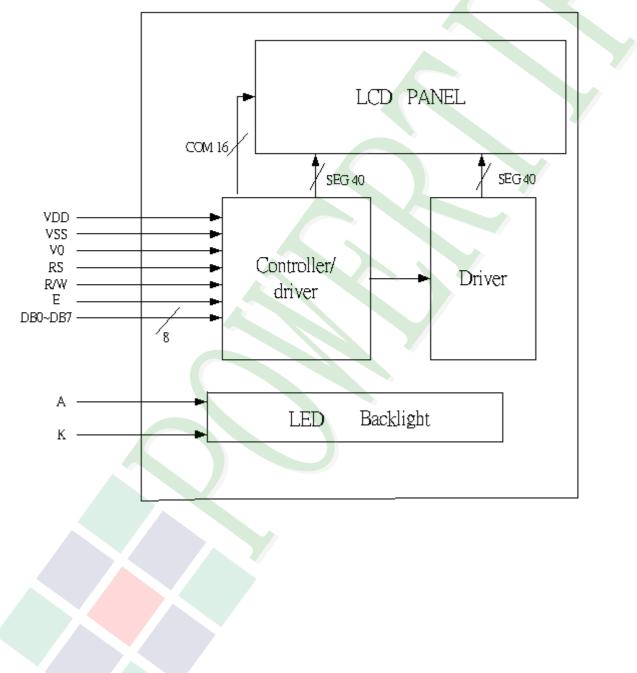
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram

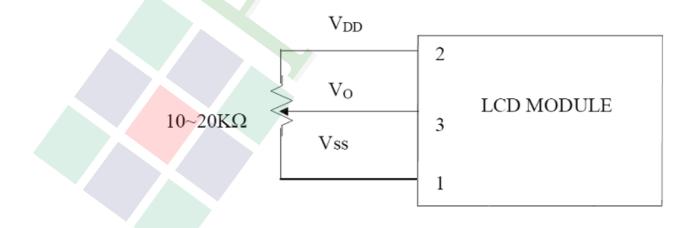


2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	Vss	Power Supply (Vss=0)
2	Vdd	Power Supply (5V)
3	Vo	Operating voltage for LCD
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	R/W	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7	DB0	
8	DB1	Four low order bi-directional three-state data bus lines. Use for data transfer between the MPU and the LCD module.
9	DB2	These four are not used during 4-bit operation.
10	DB3	These four are not used during 4-bit operation.
11	DB4	
12	DB5	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module.
13	DB6	DB7 can be used as a busy flag.
14	DB7	Der oan be used as a busy hag.

2.2.1 Application Notes

Contrast Adjust





2.2.2 Refer Initial code

void initial()

{

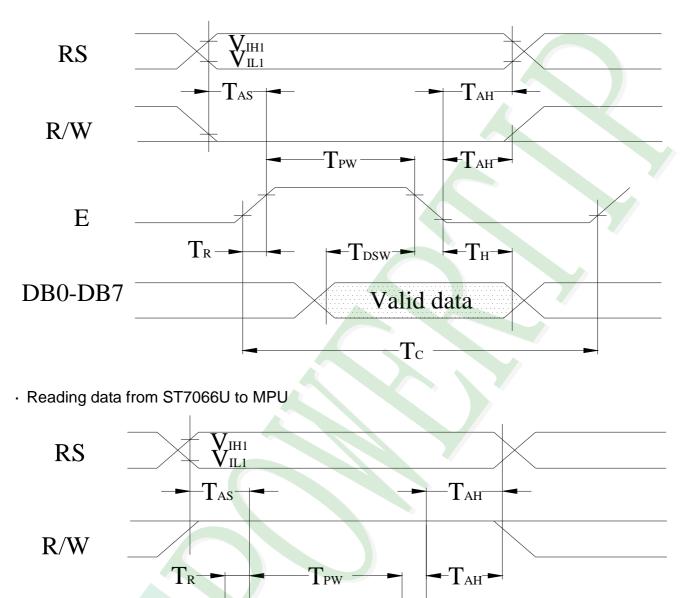
}

delay(40); write_com(0x01); delay(5); write_com(0x38); delay(5); write_com(0x0c); delay(5); write_com(0x06); delay(5);



2.3 Timing Characteristics

· Writing data from MPU to ST7066U



E

DB0-DB7

-−T_H-•

Valid data

-T_C-

-Tddr



• Write Mode (Writing data from MPU to ST7066U)

				()	/DD = 5V,	la=25°C)
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	-	-	ns
TPW	Enable Pulse Width	Pin E	140	-	-	ns
T_R, T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
Tas	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
Тан	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
Tosw	Data Setup Time	Pins:DB0~DB7	40	-	-	ns
Тн	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

• Read Mode (Reading data from ST7066U to MPU)

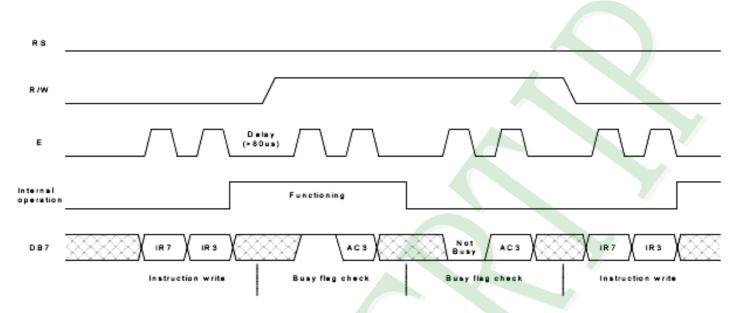
(VDD = 5V,Ta=25°C)

						,
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	-	-	ns
TPW	Enable Pulse Width	Pin E	140	-	-	ns
T_R , T_F	Enable Rise / Fall Time	Pin E	-	-	25	ns
Tas	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
T _{AH}	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T _{DDR}	Data Setup Time	Pins:DB0~DB7	-	-	100	ns
Тн	Data Hold Time	Pins:DB0~DB7	10	-	-	ns

NPC1602LRU-JWA-H

For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used

Exa	mple of busy flag check timing sequence
RS	
R /W	
E	Delay (>80us)
internal operation	Functioning
DB7	Date Busy Busy Nat Busy Date Instruction write Busy flag check Busy flag check Busy flag check Instruction write

NPC1602LRU-JWA-H



2.4 Display Command

				I	nstru	ction	Code	e				Description
Instructions		R/	DB	DB	DB	DB	DB	DB	DB	DB	Description	Time
	RS	W	7	6	5	4	3	2	1	0		(270KHz)
											Write "20H" to DDRAM. and set	
Clear	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms
Display											AC.	
											Set DDRAM address to "00H"	
											from AC and return cursor to it's	
Return	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms
Home											The contents of DDRAM	
											are not changed.	
											Sets cursor move direction and	
Entry Mode	0	0	~	0	0	0	0	4		S	specifies display shift. These	07 0
Set	0	0	0	0	0	0	0	1	I/D	3	operations are performed	37 µs
											during data write and read .	
Display											D=1 : entire display on	
ON/OFF	0	0	0	0	0	0	1	D	С	В	C=1 : cursor on	37 μ s
											B=1 : cursor position on	
Cursor or											Set cursor moving and display	
Display	0	0	0	0	0	1	S/C	R/L	×	×	shift control bit, and the	37 µs
Shift	U	Ŭ	Ŭ	Ũ	Ŭ		0,0	102			the direction, without changing	01 40
		-									of DDRAM data.	
Function											DL: interface data is 8/4 bits	
Set	0	0	0	0	1	DL	Ν	F	×	×	NL: number of line is 2/1	37 µs
											F: font size is 5×11/5×8	
Set					AC	AC	AC	AC	AC	AC	Set CGRAM address	
CGRAM	0	0	0	1	5	4	3	2	1	0	in address counter.	37 μ s
Address										-		
Set				AC	AC	AC	AC	AC	AC	AC	Set DDRAM address	07
DDRAM	0	0	1	6	5	4	3	2	1	0	in address counter.	37 µs
Address												
DUID											Whether during internal	
Read Busy			в	AC	AC	AC	AC	AC	AC	AC	operation or not can be	0
Flag and	0	1	F	6	5	4	3	2	1	0	known by reading BF.	0 μ s
Address											The contents of address	
											counter can also be read.	



Write Data	1	0	D	D6	D5	D4	2	20	D1	D0	Write data into internal RAM	37 µs
to RAM	1	0	7	00	05	04	03	DZ		00	(DDRAM/CGRAM).	57 µ 5
Read Data	1	1	D	De	DE		50	2			Read data from internal RAM	270
from RAM	I	I	7	D6	D5	D4	03	DZ	וט	D0	(DDRAM/CGRAM).	37 μ s

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF Refer to Instruction Table for the list of each instruction execution time .

2.5 Character Pattern

<u>NO.7066-0A</u>

402.1 67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	11 11
	CG RAM (1)															
0001	(2)															
0010	3															
0011	(4)															
0 100	(5)															
0101	(6)															
0 110	(7)															
0111	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	0)															
1111	(8)															

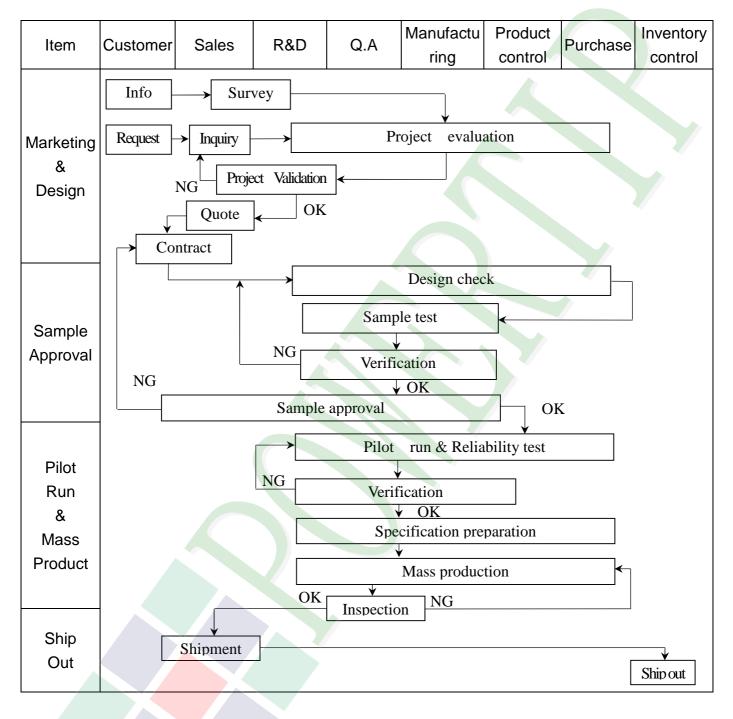
2.6 JUMPER (Setting different use)

J4



3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart





ltem	Customer	Sales	R&D	Q.A	Manufact uring	Product control	Purchase	Inventory control
Sales Service	Info	➤ Claim sis report	[Trackin	Failure an Corrective			
Q.A Activity	1. ISO 900 3. Equipme 5. Standare		ion	4	Process in . Education	·		es

3.2 Inspection Specification

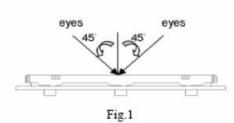
Scope : The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).

igoplusInspection Standard : MIL-STD-105E Table Normal Inspection Single Sampling Level $I\!I$.

- ◆Equipment : Gauge、MIL-STD、Powertip Tester、Sample
- ◆Defect Level : Major Defect AQL : 0.4 ; Minor Defect : AQL : 1.5.
- ♦OUT Going Defect Level : Sampling .

◆Manner of appearance test :

- (1). The test be under 20W×2 fluorescent light ' and distance of view must be at 30 cm.
- (2). Standard of inspection : (Unit : mm)
- (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
- (4). Definition of area . (Fig. 2)



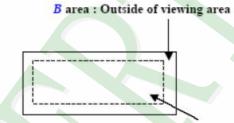


Fig. 2

A area : viewing area

Specification:

NO	Item	Criterion	Level
		1. 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1.3 Assembled in inverse direction.	Major
02	Quantity	2, 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3.1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
		4. 2 No function or no display.	Major
04	Electrical Testing	4. 3 Output data is error.	Major
		4. 4 LCD viewing angle defect.	Major
		4.5 Current consumption exceeds product specifications.	Major



♦Spe	cification For Mono	otype and Color STN :					(Ver.B01)
NO	Item	(Criteri	on			Level
	Black or white dot 、scratch 、 contamination	 5. 1 Round type: 5. 1. 1 display only : • White and black spots on 4 white or black spots pr • Densely spaced : NO mor 	resent.				n.
		5. 1. 2 Non-display : Dimension		Accontonco	(O ?tri)		
	Round type	(diameter : Φ)		Acceptance A area		area	
		Φ≤ 0.10		ept no dense	Ь		
		$0.10 < \Phi \leq 0.20$		3			
05	₩	$0.20 < \Phi \leq 0.30$		2	Igr	lore	Minor
	Φ=(x+y)/2	Total quantity		4			
	$\Psi^{-}(\mathbf{x}+\mathbf{y})/2$			4			
		5. 1. 3 Line type:					_
	Line type	Dimension		Accep	tance ((Q'ty)	
	Line type	Length (L) Width (W		A area		B area	_
	∽∕ [‡] w	W≦		Accept no de	nse		
		$L \le 3.0$ 0.03 < W \le	0.05	4		Ignore	
	L	$L \le 2.5$ 0.05 < W ≤ 0	. 075	-			
		W >0	. 075	As	round	type	
		Dimension		Acceptan	ce (Q't		
		(diameter : Φ)		A area		B area	
		$\Phi \leq 0.20$	A	ccept no dense			
06	Polarizer	$0.20 < \Phi \leq 0.50$		3			Minor
	Bubble	$0.50 < \Phi \leq 1.00$		2		Ignore	
		$\Phi > 1.00$		0			
		Total quantity		4			
			·				_

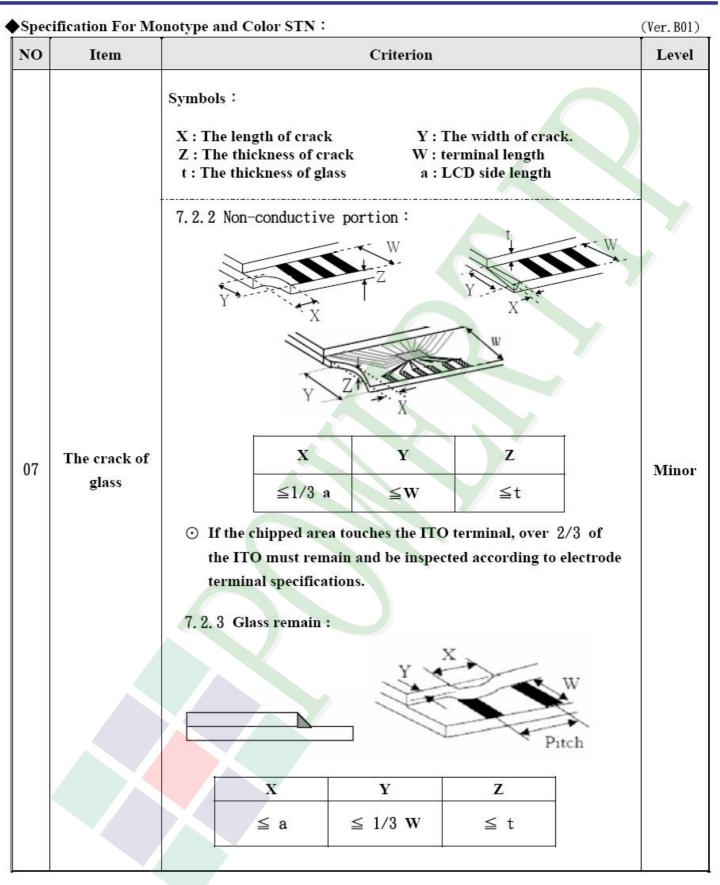


♦Speci	ification For Mono	otype and Color STN :	(Ver.B01)
NO	Item	Criterion	L	Level
		Z : The thickness of crack W	: The width of crack. : terminal length : LCD side length	
		7.1 General glass chip: 7.1.1 Chip on panel surface and crac	k between panels:	
		y z z	Z Y Y X	
07	The crack of glass		SP [NG]	Minor
		Seal width	Y	
		XY	Z	
		≤ a Crack can't enter viewing area	≦1/2 t	
		≤ a Crack can't exceed th half of SP width.	$\begin{array}{c c} \mathbf{e} \\ 1/2 \mathbf{t} < \mathbf{Z} \\ \leq 2 \mathbf{t} \end{array}$	



NO	Item			Criterion		Leve
		t : The thick	eness of crack eness of glass	W : term	width of crack. inal length side length	
		7.1.2 Corne	r crack :	Y A TOTAL		
		X		Y	z	
		≤1/5	9	an't enter ng area	$Z \leq 1/2 t$	
	The crack of	≤1/5	9	't exceed the 1 SP width.	$/2 t < Z \leq 2 t$	
07	glass					Mino
		100 0 23 300	on over termi on electrode			
		WY	X	z X	Y Z	
				X		
			X	Y	Z	
		Front	≦ a	\leq 1/2 W	≦ t	
		Back		Neglect		







♦Speci	ification For Mo	notype and Color STN :	(Ver.B01)
NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
		9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9.5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≦1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

4.1	Reliability Test Collu		(vei.bui)						
NO.	TEST ITEM	TEST CO	ONDITION						
1	High Temperature	Keep in +80 ±2° C 96 hrs							
	Storage Test	Surrounding temperature, then sto	orage at normal condition 4hrs.						
2	Low Temperature	Keep in -30 ±2° C 96 hrs							
	Storage Test	Surrounding temperature, then sto	orage at normal condition 4hrs.						
	High Temperature /	Keep in +60 °C / 90% R.H duration							
3	High Humidity	Surrounding temperature, then storage at normal condition 4hrs.							
	Storage Test	(Excluding the polarizer)							
		-30°C → +25°C -	\rightarrow +80°C \rightarrow +25°C						
4	Temperature Cycling	(30mins) (5mins)	(30 mins) (5 mins)						
	Storage Test	10 (Cycle						
		Surrounding temperature, then sto	orage at normal condition 4hrs.						
		Air Discharge:	Contact Discharge:						
	ESD Test	Apply 2 KV with 5 times	Apply 250 V with 5 times						
		Discharge for each polarity +/-	discharge for each polarity +/-						
		1. Temperature ambiance : 15° C ~ 35° C							
5		2. Humidity relative : $30\% \sim 60\%$							
		3. Energy Storage Capacitance(Cs+Cd) : 150pF±10%							
		4. Discharge Resistance(Rd) : $330 \Omega \pm 10\%$							
		5. Discharge, mode of operation :							
		Single Discharge (time between successive discharges at least 1 sec)							
		(Tolerance if the output voltage ind	,						
_	Vibration Test	1. Sine wave 10~55 Hz frequence							
6	(Packaged)	2. The amplitude of vibration :1.							
		3. Each direction $(X \cdot Y \cdot Z)$ dur	ration for 2 Hrs						
		Packing Weight (Kg)	Drop Height (cm)						
		0 ~ 45.4	122						
	Drop Test	45.4 ~ 90.8	76						
7	(Packaged)	90.8 ~ 454	61						
	(Fackaged)	0ver 454	46						
		Drop Direction : ※1 corner / 3 edg	es / 6 sides each 1time						

(Ver.B01)

5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks , be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth , as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands , this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is $320\pm10^{\circ}$ C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM .

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is $25^{\circ}C \pm 5^{\circ}C$ and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush , shake , or jolt the module.

5.4 TERMS OF WARRANTY

- 5.4.1 Applicable warrant period The period is within thirteen months since the date of shipping out under normal using and storage conditions.
- 5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.

_	003 002	004	007		
NEW DRAWING				NOTES: N	
REV RY				PADI-R2.5 (15.4) (15.24) (15.4) (15.4) (15.4) (15.4) (15.4) (15.4) (15.4) (15.24) (15.4) (15.24) (15.24) (15.2)	t
Terry				-14-ø1.0 PAD14-ø1.8 C C TRANSFLECTIVE	(
2014/04/08					
LCD Module Drawing		DRAWING NAME :	NPC1602LRU-JWA-H		
Approve	Check	Design			
Ryan	Eddy		□ 久正光 POWERTIP 1	(9.25) + 16.0 + 16.0 + 11.5(A.A)	
Page	Coole				-
1/1	, M		5		
Quantity	Material	Surface	子有限公 CORPORATION		
16 ~ 63 63 ~ 250	4 16	公 港 精 度 観	ON 回		
• • •	1	精殺		<u></u> Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ Δ	_

