APEX

APEX SCIENCE & ENGINEERING CORP

（OPTOELECTRONIC DIV.）

新北市中和區新民街 112 號 4 樓　　Http：www.apexgroup.com.tw
4F,No.112,Shin－Min St., Chungho Dist , New Taipei City 235, Taiwan, R.O.C.
Tel：886－2－2228－7331　Fax：886－2－2221－9105

TADD430RHFR40N

ROHS

DATA SHEET

Acceptance	

ISSUE	VERSION	APPROVER	CHECKER	ENGINEER
奉典 05/07 Edward	A	奉典 05/07 Donlin		奉典 05/07 Alan

Records of Revision

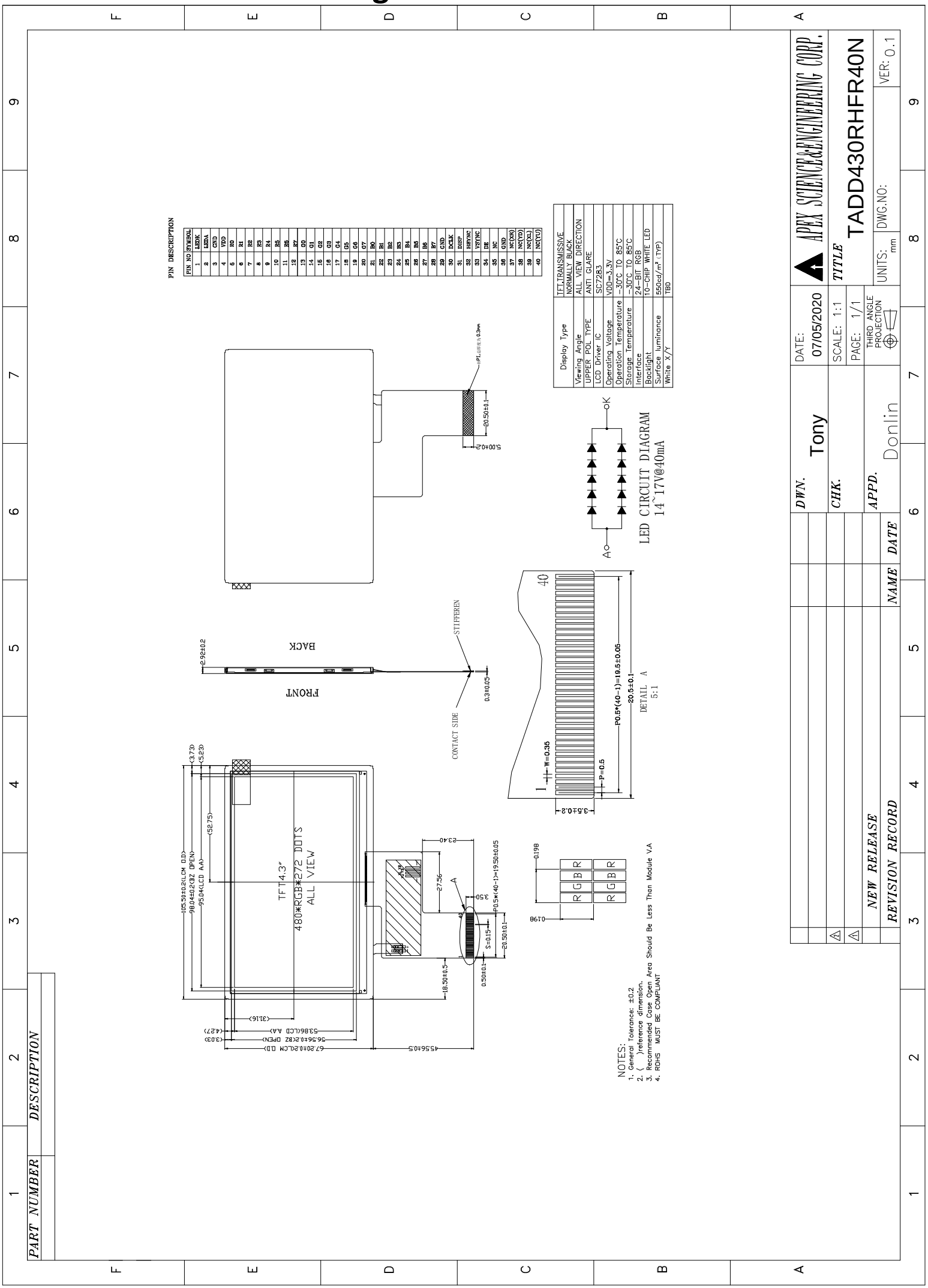
DATE	REF.PAGE PARAGRAPH DRAWING No.	REVISED No.	SUMMARY	REMARK
2020-05-07		A	First Issue	

Contents

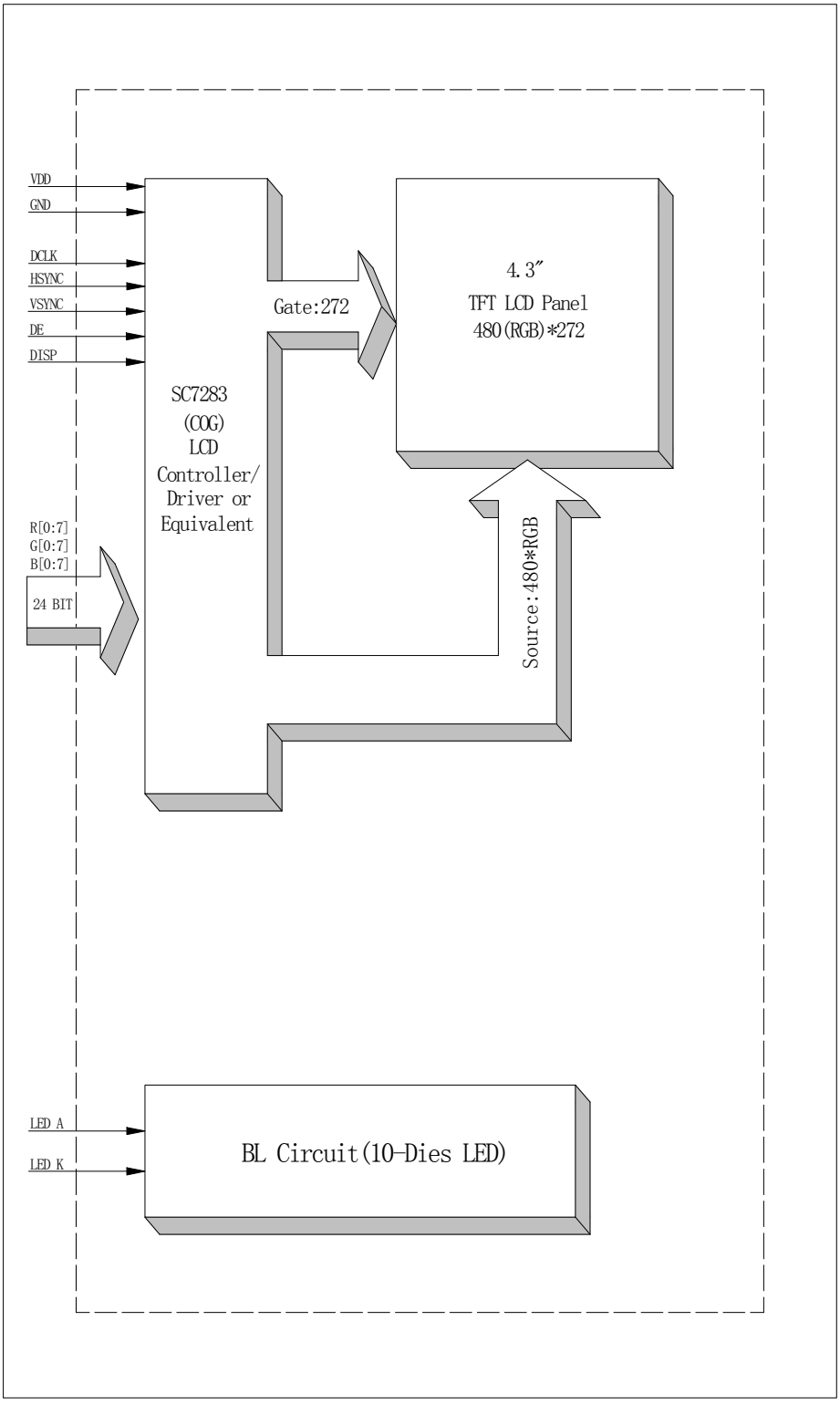
1. General Specification

Item	Contents	Unit
LCD TYPE	TFT/TRANSMISSIVE	
MODULE SIZE (W*H*T)	105.5*67.2*2.92	MM
ACTIVE SIZE (W*H)	95.04*53.86	MM
PIXEL PITCH (W*H)	0.198*0.198	MM
NUMBER OF DOTS	480*272	
DIVER IC	SC7283	
INTERFACE TYPE	24 BIT RGB	
TOP POLARIZER TYPE	ANTI-GLARE	
RECOMMEND VIEWING DIRECTION	ALL	O'CLOCK
GRAY SCALE INVERSION DIRECTION	-	O'CLOCK
BACKLIGHT TYPE	10-DIES WHITE LED	
TOUCH PANEL TYPE	WITHOUT	

2. Mechanical Drawing



3. Block Diagram



4. Interface Pin Function

Pin No.	Symbol	Description
1	VLED-	Cathode of LED backlight
2	VLED+	Anode of LED backlight
3	GND	Power ground
4	VDD	Power voltage
5	R0	Red data (LSB)
6	R1	Red data
7	R2	Red data
8	R3	Red data
9	R4	Red data
10	R5	Red data
11	R6	Red data
12	R7	Red data (MSB)
13	G0	Green data (LSB)
14	G1	Green data
15	G2	Green data
16	G3	Green data
17	G4	Green data
18	G5	Green data
19	G6	Green data
20	G7	Green data(MSB)
21	B0	Blue data(LSB)
22	B1	Blue data
23	B2	Blue data
24	B3	Blue data
25	B4	Blue data
26	B5	Blue data
27	B6	Blue data
28	B7	Blue data(MSB)
29	GND	Power ground
30	DCLK	Pixel clock
31	DISP	Display on/off
32	HSYNC	Horizontal sync signal
33	VSYNC	Vertical sync signal
34	DE	Data enable
35	NC	NO connect
36	GND	Power ground
37	NC	NO connect
38	NC	NO connect
39	NC	NO connect
40	NC	NO connect

5. Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply voltage for analog	VDD	-0.3	4.5	V
Supply voltage for logic	VDD	-0.3	4.5	V
Supply current (One LED)	I_{LED}		30	mA
Operating temperature	T_{OP}	-30	+85	°C
Storage temperature	T_{ST}	-30	+85	°C

Note : The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

6. Electrical Characteristics

6.1 Input Power

Item	Symbol	Min	Typ.	Max	Unit	Applicable terminal
Supply Voltage for Analog	VDD	3.0	3.3	3.6	V	
Supply Voltage for Logic	VDD	3.0	3.3	3.6	V	
Input Voltage	V_{IL}	GND	-	0.3VDD	V	
	V_{IH}	0.7 VDD	-	VDD		
Input leakage Current	I_{LKG}	-1		1	μA	

6.2 Backlight Driving Conditions

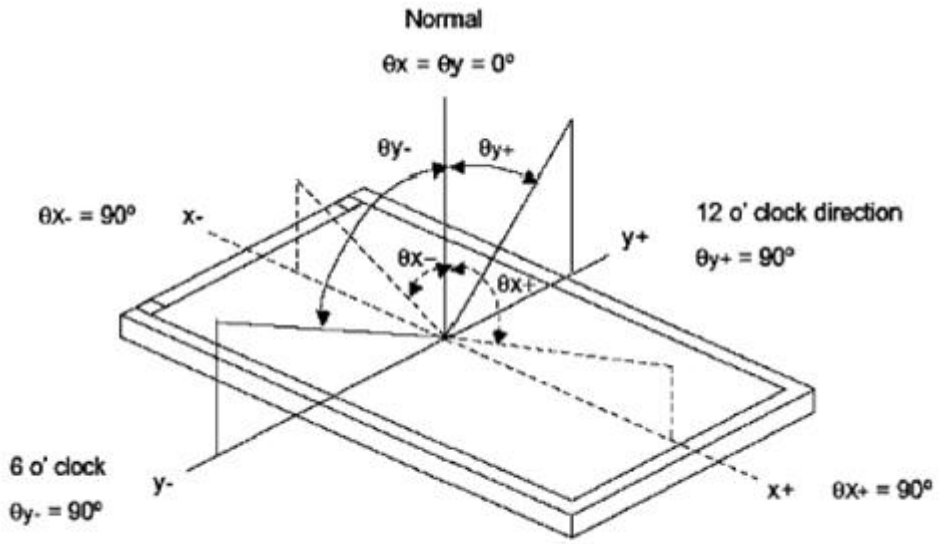
Item	Symbol	Value			Unit	Remark
		Min.	Typ.	Max.		
Voltage for LED Backlight	V_F	14	16	17	V	I_L =40mA
Current for LED Backlight	I_L		40		mA	
Power Consumption	P		0.64		W	
LED Life Time		30,000	50,000		Hr	Note

Note: Brightness to be decreased to 50% of the initial value at ambient temperature TA=25℃

7. Optical Characteristics

ITEM		SYMBOL	CONDITIONS	SPECIFICATIONS			UNIT	NOTE
				MIN	TYP.	MAX		
Luminance		L	I_L =40mA	440	550	770	Cd/m^2	
Contrast Ratio		CR	θ=0°	640	800			
Response Time		T_{ON}	25℃		30	40	ms	
		T_{OFF}						
CIE Color Coordinate	Red	X_R	Viewing normal angle	0.5628	0.6028	0.6428		
		Y_R		0.3149	0.3549	0.3949		
	Green	X_G		0.3381	0.3781	0.4181		
		Y_G		0.5323	0.5723	0.6123		
	Blue	X_B		0.1056	0.1456	0.1856		
		Y_B		0.0823	0.1223	0.1623		
	White	X_W		0.2883	0.3283	0.3683		
		Y_W		0.3196	0.3596	0.3996		
Viewing Angle	Hor.	θ_{X+}	CR≥10	70	80		Degree	
		θ_{X-}		70	80			
	Ver.	θ_{Y+}		70	80			
		θ_{Y-}		70	80			
Uniformity	Un			80	85		%	

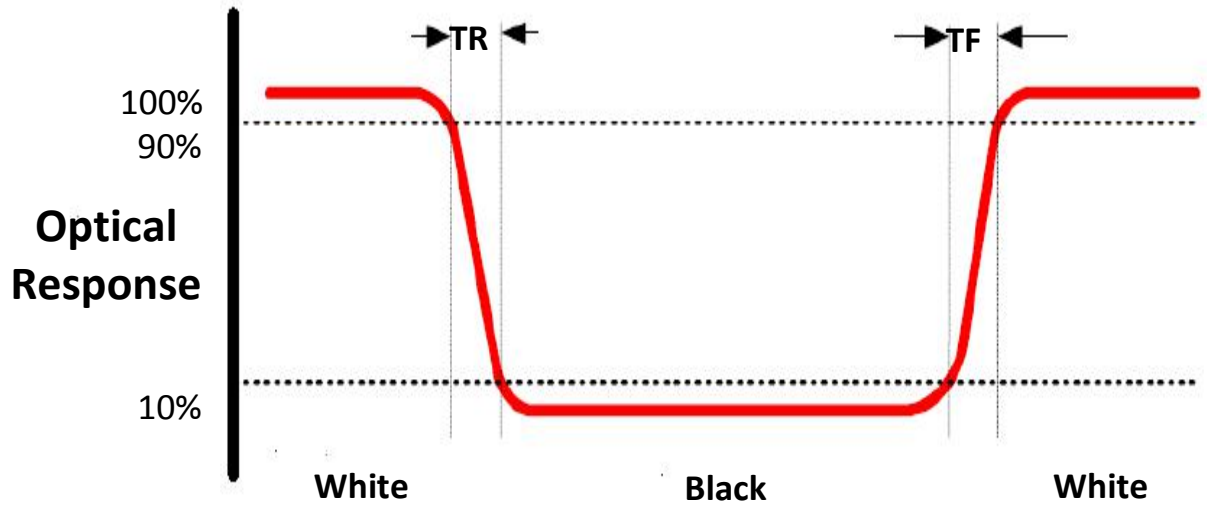
Note 1: Definition of Viewing Angle θx and θy:



Note 2: Definition of contrast ratio CR:

$$CR = \frac{\text{Luminance of white state}}{\text{Luminance of black state}}$$

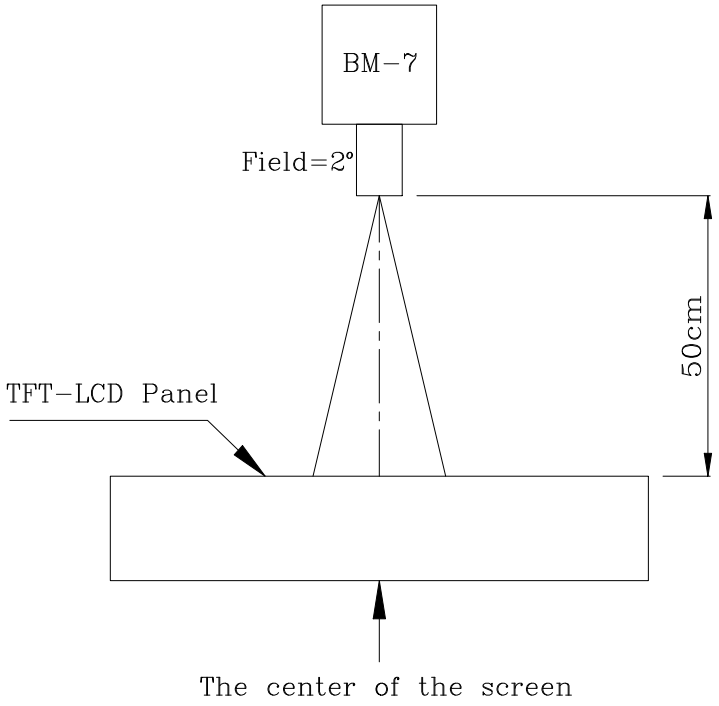
Note 3: Definition of Response Time(Tr,Tf)



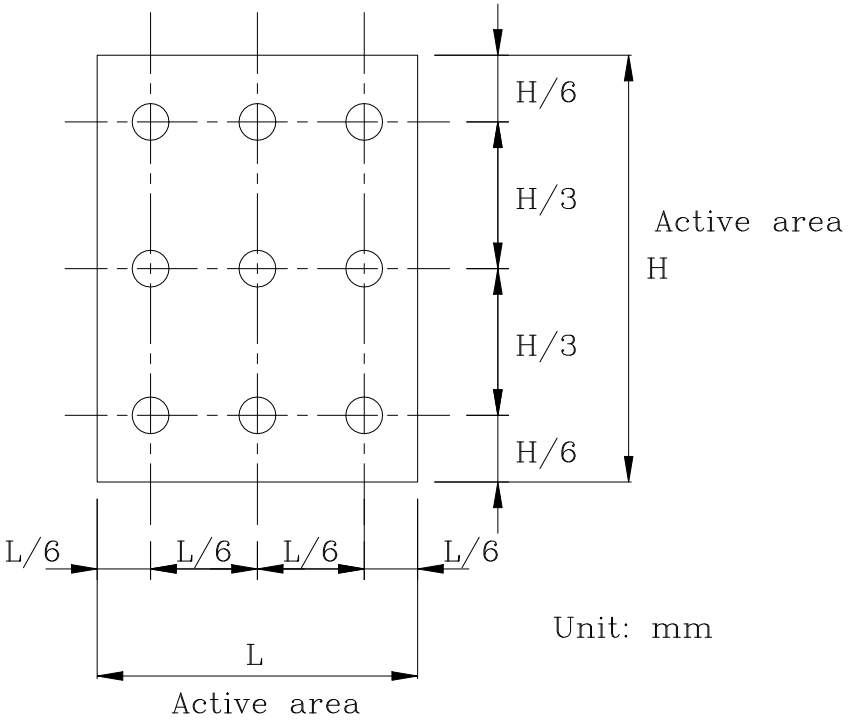
Note 4: Definition of Luminance

①The Brightness Test Equipment Setup

Field=2°(As measuring “black” image, field=2°is the best testing condition)



②The Brightness Test Point Setup



APEX

8. Timing Characteristics

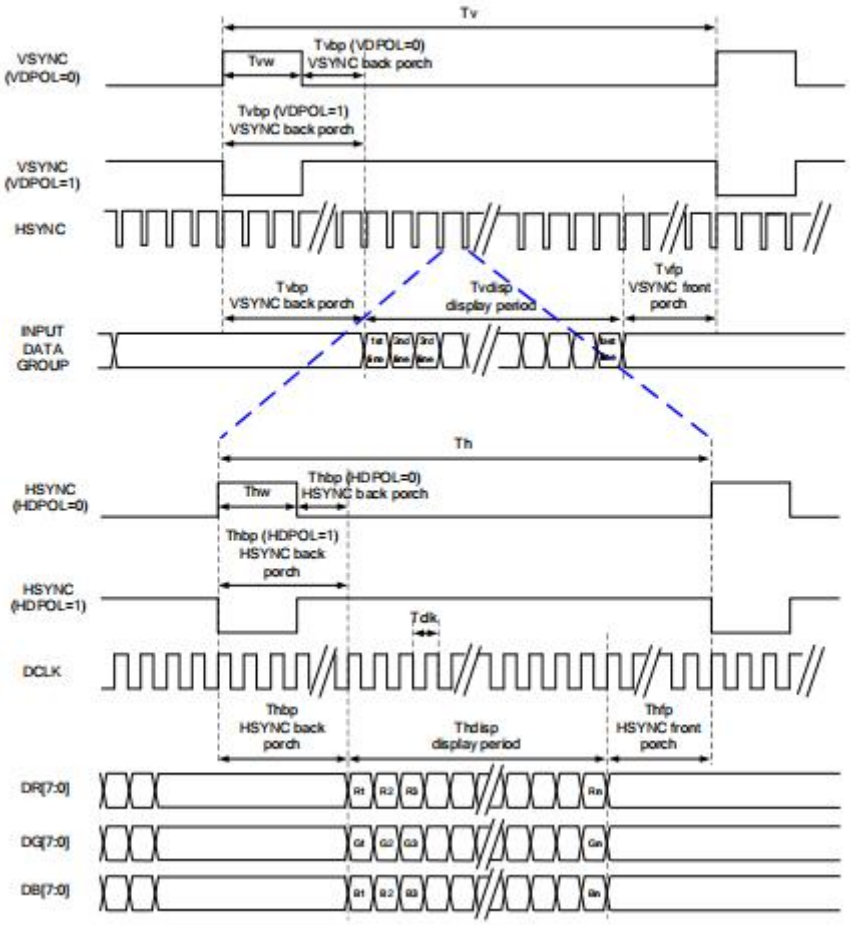
8.1 parallel 24 bit RGB input timing table

480RGB X 272 Resolution Timing Table							
Item		Symbol	Min.	Typ.	Max.	Unit	Remark
DCLK Frequency		Fclk	8	9	12	MHz	
DCLK Period		Tclk	83	111	125	ns	
HSYNC	Period Time	Th	485	531	598	DCLK	
	Display Period	Thdisp		480		DCLK	
	Back Porch	Thbp	3	43	43	DCLK	By H_BLANKING setting
	Front Porch	Thfp	2	8	75	DCLK	
	Pulse Width	Thw	2	4	43	DCLK	
VSYNC	Period Time	Tv	276	292	321	HSYNC	
	Display Period	Tvdisp		272		HSYNC	
	Back Porch	Tvbp	2	12	12	HSYNC	By V_BLANKING setting
	Front Porch	Tvfp	2	8	37	HSYNC	
	Pulse Width	Tvw	2	4	12	HSYNC	

Note: It is necessary to keep Tvbp =12 and Thbp =43 in sync mode. DE mode is unnecessary to keep it.

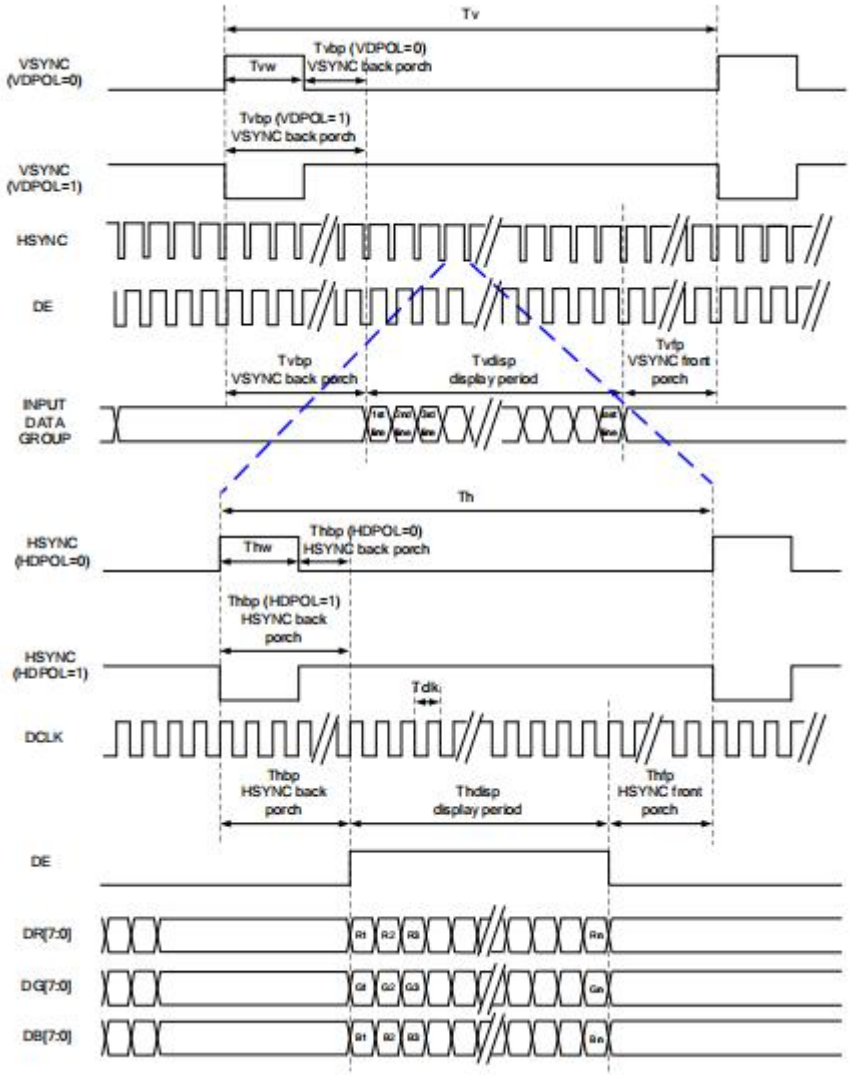
8.2 Parallel RGB Mode Timing Diagram

8.2.1 sync mode

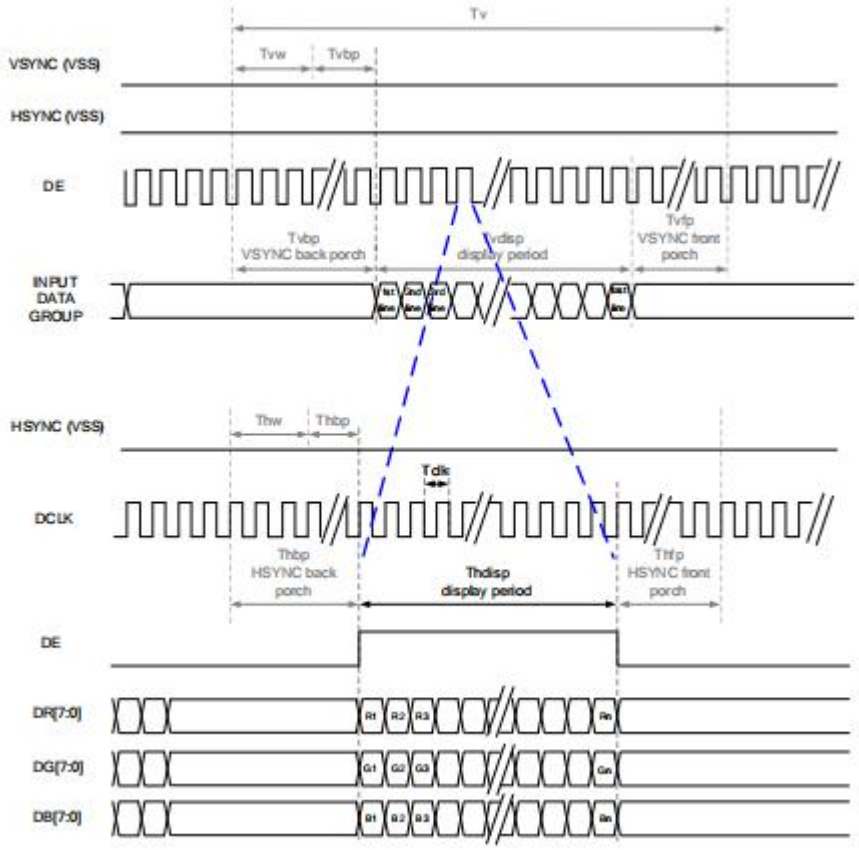


APEX

8.2.2 sync-de mode



8.2.3 de mode



RGB Mode Selection Table	DCLK	HSYNC	VSYNC	DE
SYNC - DE Mode	Input	Input	Input	Input
SYNC Mode	Input	Input	Input	GND
DE Mode	Input	GND	GND	Input

Note: "Input" means these signals are driven by host side.

9. Standard Specification for Reliability

9.1 Standard Specification for Reliability of LCD Module

No.	Item	Description	Remarks
01	High temperature operation	Ts=+85℃, 240hrs.	Note1, IEC60068-2-2,GB2423.2—89
02	Low temperature operation	Ta=-30℃, 240hrs	Note2, IEC60068-2-1,GB2423.1—89
03	High temperature storage	Ta=+85℃, 240hrs	IEC 60068-2-2, GB2423.2-89
04	Low temperature storage	Ta=-30℃, 240hrs	IEC 60068-2-1, GB2423.1-89
05	High Temperature & High Humidity （NonOperation）	+60℃, 90% RH max,240 hours	IEC60068-2-3, GB/T2423.3—2006
06	Thermal Shock (Nonoperation)	-30℃ 30 min~+80℃ 30 min, Change time:5min, 30 Cycle	Start with cold temperature, end with high temperature IEC60068214,GB2423.22—87
07	Electro Static Discharge (Operation)	C=150pF, R=330Ω， 5points/panel Air:±8KV， 5times;Contact:±4KV， 5 times; （Environment: 15℃～35℃， 30%～60%， 86Kpa～106Kpa）	IEC 61000-4-2 GB/T17626.2-1998
08	Vibration (Nonoperation)	Frequency range:10~55Hz, Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction of X.Y.Z.(packagecondition)	IEC60068-2-6 GB/T2423.10—1995
09	Shock (Nonoperation)	60G 6ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27 GB/T2423.5—1995
10	Package Drop Test	Height:80 cm， 1 corner, 3 edges, 6 surfaces	IEC60068-2-32 GB/T2423.8—1995

Note1: Ts is the temperature of panel's surface.
Note2: Ta is the ambient temperature of sample.

9.2 Testing Conditions and Inspection Criteria

For the final test, the testing sample must be stored at room temperature for 24 hours. After the tests listed in Table 9.2, standard specifications for reliability will be executed in order to ensure stability.

No.	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

9.3 MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature (25±5℃), normal humidity (50±10% RH), and in area not exposed to direct sun light.

10. Specification of Quality Assurance

This standard of Quality Assurance confirms to the quality of LCD module products supplied by APEX.

10.1 Quality Test

Before delivering, the supplier should conduct the following tests to confirm the quality of products.

- Electrical-Optical Characteristics: According to the individual specification to test the product.
- Appearance Characteristics: According to the individual specification to test the product.
- Reliability Characteristics: According to the definition of reliability on the specification for testing products.

10.2 Delivery Test

Before delivering, the supplier should conduct the delivery test.

- Test method: According to MIL-STD105E.General Inspection Level Ⅱ take a single Time.
- The defects classify of AQL as following:
  Major defect: AQL = 0.65
  Minor defect: AQL = 1.5
  Total defects: AQL = 1.5

10.3 Non-conforming Analysis & Deal With Manners

10.3.1 Non-conforming Analysis

- Purchaser should provide the data detail of non-conforming sample and the non-conforming.
- After receiving the data detail from purchaser, the analysis of non-conforming should be finished within two weeks.
- If the analysis can't be finished on time, supplier must notice purchaser 3 days in advance.

10.3.2 Disposition of non-conforming

- If any product defect be found during assembling, supplier must change the good for every defect after confirmation.
- Both supplier and customer should analyze the reason and discuss the disposition of non-conforming when the reason of nonconforming is not sure.

10.4 Agreement items

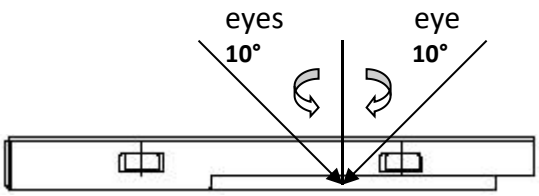
Both parties should negotiate together when the following problems happen.

- There is any problem of standard of quality assurance, and both sides should agree that it must be modified.
- There is any argument item which does not record in the standard of quality assurance.
- Any other special problem.

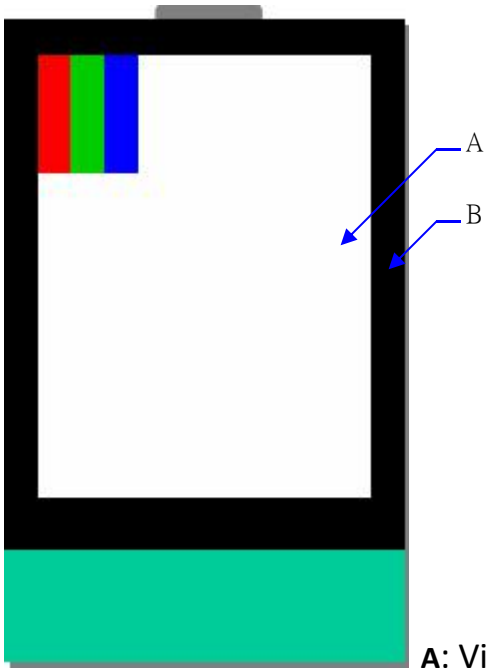
10.5 Standard of The Product Appearance Test

10.5.1 Manner of appearance test

- The test must be under 20W × 2 or 40W fluorescent light, and the distance of view must be at 30±5cm.
- When test the model of transmissive product must add the reflective plate.
- The test direction is base on around 10° of vertical line.
- Temperature: 25±5℃ Humidity: 60±10%RH



- Definition of area:

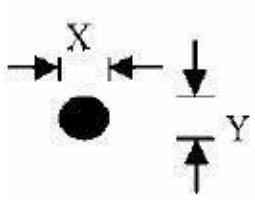
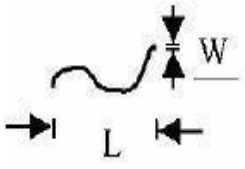


A: Viewing area B: Outside viewing area

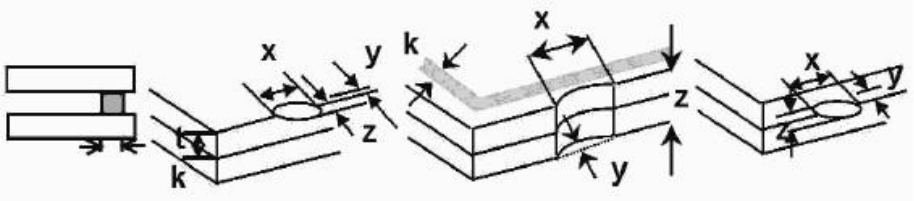
10.5.2 Basic principle

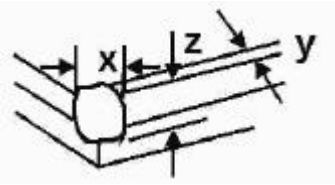
- When the standard can not be described, AQL will be applied.
- The sample of the lowest acceptable quality level must be negotiated by both supplier and customer when any dispute happened.
- New item must be added on time when it is necessary.

10.6 Inspection Specification

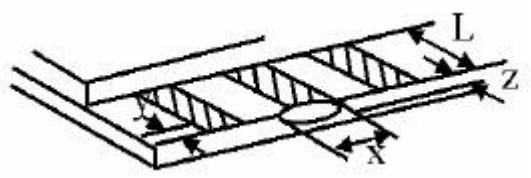
NO.	Item	Criterion	AQL
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character, dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Flicker	**0.65**
02	Black or White spots or Bright spots or Color spots on LCD (Display only)	2.1 White and black or color spots on display ≦ 0.25mm, no more than Five spots. 2.2 Densely spaced: No more than three spots within 3mm.	**1.5**
03	LCD and Touch Panel black spots, white spots, contaminat i on (non – display)	3.1 Round type: As following drawing Φ = (X+Y) / 2  * Densely spaced: No more than two spots within 3mm.	**1.5**
		3.2 Line type: (As following drawing)  * Densely spaced: No more than two lines within 3mm.	**1.5**

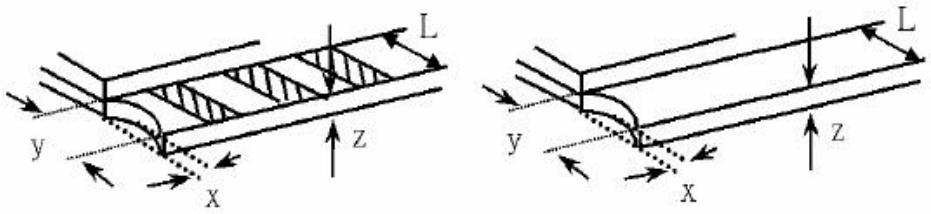
APEX

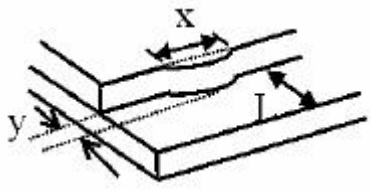
<table>
<tr><th>NO.</th><th>Item</th><th colspan="3">Criterion</th><th>AQL</th></tr>
<tr><td>04</td><td>Polarizer bubbles</td><td>If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction</td><td>Size Φ(mm)
Φ≦0.30
0.30< Φ≦0.50
0.50< Φ≦1.00
1.00< Φ
Total Q’ty</td><td>Acceptable Q'ty
Accept no dense
0
0
0
0</td><td>1.5</td></tr>
<tr><td>05</td><td>Scratches</td><td colspan="3">Follow NO.3 -2 Line Type.</td><td></td></tr>
<tr><td>06</td><td>Chipped glass</td><td colspan="3">Symbols:
x: Chip length y: Chip width z: Chip thickness
k: Seal width t: Glass thickness a: LCD side length
L: Electrode pad length
6.1 General glass chip:
6.1.1 Chip on panel surface and crack between panels:
 

<table><tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr><tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr><tr><td>1/2t< z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr></table>⊙ Unit: mm
⊙ If there are 2 or more chips, x is the total length of each chip
6.1.2 Corner crack:
 

<table><tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr><tr><td>Z≦1/2t</td><td>Not over viewing area</td><td>x≦1/8a</td></tr><tr><td>1/2t< z≦2t</td><td>Not exceed 1/3k</td><td>x≦1/8a</td></tr></table>⊙ Unit: mm
⊙ If there are 2 or more chips, x is the total length of each chip</td><td>1.5</td></tr>
</table>

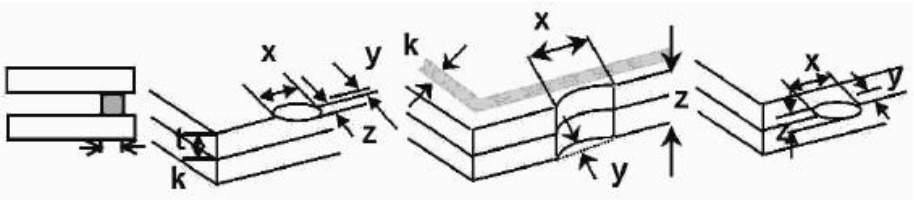
<table>
<tr><th>NO.</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>07</td><td>Glass crack</td><td>
Symbols:
x: Chip length y: Chip width z: Chip thickness
k: Seal width t: Glass thickness a: LCD side length
L: Electrode pad length
7.2 Protrusion over terminal:
7.2.1 Chip on electrode pad:


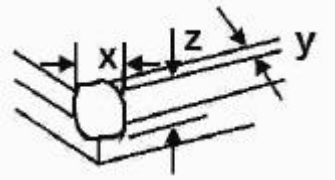
<table>
<tr><td>y: Chip width</td><td>x: Chip length</td><td>z: Chip thickness</td></tr>
<tr><td>y≦0.5mm</td><td>x≦1/8a</td><td>0< z≦t</td></tr>
</table>
7.2.2
Non-conductive portion:


<table>
<tr><td>y: Chip width</td><td>x: Chip length</td><td>z: Chip thickness</td></tr>
<tr><td>y≦L</td><td>x≦1/8a</td><td>0< z≦t</td></tr>
</table>
⊙ If there chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.
⊙ If the product will be heat sealed by the customer, the alignment mark must mot be damaged.
7.2.3 Substrate protuberance and internal crack


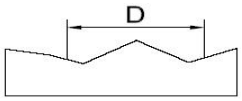
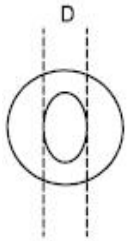
<table>
<tr><td>y: width</td><td>x: length</td></tr>
<tr><td>y≦1/3L</td><td>X≦a</td></tr>
</table>
</td><td>1.5</td></tr>
</table>

NO.	Item	Criterion	AQL
08	Cracked glass	No crack is allowed.	1.5
09	Backlight elements	9.1 Illumination source flickers when lit. 9.2 Spots or scratches that appear when lit must be judged. Using LCD spot, lines and contamination standards. 9.3 Backlight doesn't light or color is wrong.	1.5 1.5 0.65
10	Bezel	No scratches with W>0.1 and Length>2.5mm.	1.5
11	PCB、COB	11.1 COB seal may not have pinholes larger than 0.2mm or contamination. 11.2 COB seal surface may not have pinholes through to the IC. 11.3 The height of the COB should not exceed the height indicated in the assembly diagram. 11.4 There may not be more than 2mm of sealant outside the seal area on PCB. And there should be no more than three places. 11.5 Parts on PCB must be the same as on the production characteristic chart, There should be no wrong parts, missing parts or excess parts. 11.6 The jumper on the PCB should conform to the product characteristic chart.	1.5 1.5 1.5 1.5 0.65 0.65
12	FPC	12.1 FPC terminal damage ≦ 1/2 FPC terminal width and can not affect the function , we judge accept. 12.2 FPC alignment hole damage ≦ 1/2 alignment area and can not affect the function , we judge accept.	1.5 1.5
13	Soldering	13.1 No cold solder joints, missing solder connections, oxidation or icicle. 13.2 No short circuits in components on PCB or FPC.	1.5 0.65

<table>
<tr><th>NO.</th><th>Item</th><th>Criterion</th><th>AQL</th></tr>
<tr><td>14</td><td>Touch Panel
Chipped glass</td><td>
Symbols:
x: Chip length y: Chip width z: Chip thickness
k: Seal width t: Touch Panel Total thickness a: LCD side length
L: Electrode pad length
14.1 General glass chip:
14.1.1 Chip on panel surface and crack between panels:


<table>
<tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr>
<tr><td>Z≦t</td><td>≦1/2 k and not over viewing area</td><td>x≦1/8a</td></tr>
</table>
⊙ Unit: mm
⊙ If there are 2 or more chips, x is the total length of each chip
14.1.2 Corner crack:


<table>
<tr><td>z: Chip thickness</td><td>y: Chip width</td><td>x: Chip length</td></tr>
<tr><td>z≦t</td><td>≦1/2 k and not over viewing area</td><td>x≦1/8a</td></tr>
</table>
⊙ Unit: mm
⊙ If there are 2 or more chips, x is the total length of each chip
</td><td>1.5</td></tr>
</table>

NO.	Item	Criterion	AQL
15	Touch Panel(Fish eye、dent and bubble on film)	<table><tr><td>SIZE(mm)</td><td>Acceptable Q'ty</td></tr><tr><td>Φ≦0.2</td><td>Accept no dense</td></tr><tr><td>0.2< D ≦0.4</td><td>5</td></tr><tr><td>0.4< D ≦0.5</td><td>2</td></tr><tr><td>0.5< D</td><td>0</td></tr></table>  	1.5
16	Touch Panel Newton ring	Newton ring dimension ≦ 1/2 touch panel area and not affect font and line distortion(≦2.5%) , it is acceptable.	1.5
17	Touch Panel Linearity	Less than 1.5% is acceptable.	1.5
18	LCD Ripple	Touch the touch panel , can not see the LCD ripple. Pen: R 1.0mm silicon rubber. Operation Force: 80g	1.5
19	General appearance	19.1 Pin type must match type in specification sheet. 19.2 LCD pin loose or missing pins. 19.3 Product packaging must the same as specified on packaging specification sheet. 19.4 Product dimension and structure must conform to product specification sheet.	0.65 0.65 0.65 0.65

11. Handling Precaution

11.1 Handling of LCM

- Avoid external shock.
- Don't apply excessive force on the surface.
- Liquid in LCD is hazardous substance, do not lick or swallow. When the liquid is attaching to your hand, skin, cloth, etc., wash it thoroughly and immediately.
- Don't operate it above the absolute maximum rating.
- Don't disassemble the LCM.
- The operators should wear protections whenever he/she comes into contact with the module. Never touch any of the conductive parts such as the LSI pads, the copper leads on the PCB and the interface terminals with any parts of the human body.
- The modules should be kept in antistatic bags or other containers resistant to static for storage.
- The module is coated with a film to protect the display surface, be careful when peeling off this protective film since static electricity may be generated.

11.2 Storage

- Store it in an ambient temperature of 25±10℃, and in a relative humidity of 50±10%RH. Don't expose to sunlight or fluorescent light.
- Store it in a clean environment, free from dust, active gas, and solvent.
- Store it in anti-static electricity container.
- Store it without any physical load.

11.3 Soldering

- Use only soldering irons with proper grounding and no leakage.
- Iron: no higher than 280±10℃ and less than 3 sec during hand soldering.
- Rewiring: no more than 2 times.

12. Packing Method

No.	Item	Dimensions(mm)	Quantity	Remark
1	LCM Module	105.50*67.20*2.92	160PCS	
2	PALLET	344*285*85 (include 80pcs products/one pallet)	2PCS	
3	LARGE CARTON	385*315*227 (include 160pcs products/one carton)	1PCS	