MODEL NO : __TM057KBHG01__

MODEL VERSION: __40__

SPEC VERSION : __Ver 1.0__

ISSUED DATE: __2016-08-05__

■**Preliminary Specification**
□**Final Product Specification**

Customer :_____

Approved by	Notes

TIANMA Confirmed :

Prepared by	Checked by	Approved by

This technical specification is subjected to change without notice

Table of Contents

Record of Revision

Rev	Issued Date	Description	Editor
1.0	2016-08-05	Preliminary Specification Release	Xunqiang Ji

1 General Specifications

Feature		Spec
Display Spec.	Size	5.7 inch
	Resolution	320 (RGB)×240
	Technology Type	a-Si
	Pixel Configuration	R.G.B. Vertical Stripe
	Pixel pitch(mm)	0.360 x 0.360
	Display Mode	TM with Normally White
	Surface Treatment	AG
	Viewing Direction	12 o'clock(source IC 6 o'clock)
	Gray Scale Inversion Direction	6 o'clock
Mechanical Characteristics	LCM (W x H x D) (mm)	144 x 104.6 x 13.5
	Active Area(mm)	115.20 x 86.40
	With /Without TSP	With TSP
	LED Numbers	15LEDs
	Weight (g)	TBD
Electrical Characteristics	Interface	RGB 18-bit
	Color Depth	262K
	Driver IC	Source IC: NT39413TH-D/4EC Gate IC: NT39208H-D/3IB

Note 1: Viewing direction for best image quality is different from TFT definition. There is a 180 degree shift.

Note 2: Requirements on Environmental Protection: Q/S0002

Note 3: LCM weight tolerance: ± 5%

2 Input/Output Terminals

2.1 CN1 pin assignment (Signal interface)

Connector type: 089H33-000100-G2-R (STARCONN)

Pin No.	Symbol	I/O	Function	Remark
1	GND	P	Ground	
2	DOTCLK	I	Dot clock. Latch data at falling edge of DOTCLK.	
3	Hsync	I	Horizontal sync signal in SYNC mode. Pull low or floating in DE mode.	
4	Vsync	I	Vertical sync signal in SYNC mode. Pull low or floating in DE mode.	
5	GND	P	Ground	
6	R0	I	Red data (LSB)	
7	R1	I	Red data	
8	R2	I	Red data	
9	R3	I	Red data	
10	R4	I	Red data	
11	R5	I	Red data (MSB)	
12	GND	P	Ground	
13	G0	I	Green data(LSB)	
14	G1	I	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data(MSB)	
19	GND	P	Ground	
20	B0	I	Blue data(LSB)	
21	B1	I	Blue data	
22	B2	I	Blue data	
23	B3	I	Blue data	
24	B4	I	Blue data	
25	B5	I	Blue data(MSB)	
26	GND	P	Ground	
27	ENABLE	I	Data enable signal in DE mode. This pin must pull high in SYNC mode.	

28	VCC	P	Power supply	
29	VCC	P	Power supply	
30	R/L	I	Set horizontal scan direction: Low/NC: left to right;　High: right to left	
31	U/D	I	Set vertical scan direction: High/NC: up to down;　Low: down to up	
32	NC	-	No connection	
33	GND	P	Ground	

2.2 CN2 pin assignment (Backlight interface)

Connector type: SHLP-06V-S-B(JST)

Pin No.	Symbol	I/O	Function	Remark
1	AN1	P	LED driving anode 1 (high voltage)	
2	AN2	P	LED driving anode 2 (high voltage)	
3	AN3	P	LED driving anode 3 (high voltage)	
4	CA1	P	LED driving cathode 1 (low voltage)	
5	CA2	P	LED driving cathode 2 (low voltage)	
6	CA3	P	LED driving cathode 3 (low voltage)	

Note1: CN2 Matching Connector type: SM06B-SHLS-TF (JST)

2.3 CN3 pin assignment (Touchpanel interface)

Used FPC: pitch 1.0 mm，T=0.3mm，4 pin

Pin No.	Symbol	I/O	Function	Remark
1	XR	I	X right side	
2	YU	I	Y 12 O'clock side	
3	XL	I	X left side	
4	YD	I	Y 6 O'clock side	

Note1: CN3 Matching Connector type: SFW4R-1STAE1-LF (FCI)

3 Absolute Maximum Ratings

GND=0V

Item	Symbol	MIN	MAX	Unit	Remark
Power Supply Voltage	VDD	-0.5	5.0	V	Note1
Operating Temperature	Top	-20	70	℃	
Storage Temperature	Tst	-30	80	℃	
Relative Humidity Note2	RH	--	≤95	%	Ta≤40℃
		--	≤85	%	40℃＜Ta≤50℃
		--	≤55	%	50℃＜Ta≤60℃
		--	≤36	%	60℃＜Ta≤70℃
		--	≤24	%	70℃＜Ta≤80℃
Absolute Humidity	AH	--	≤70	g/m³	Ta＞70℃

Table 3   Absolute Maximum Ratings

Note1: Input voltage include R0~R5, G0~G5, B0~B5, Dotclk, Hsync, Vsync, Enable, R/L, U/D.

4 Electrical Characteristics

4.1 LCD module

GND=0V,Ta=25℃

Item		Symbol	MIN	TYP	MAX	Unit	Remark
Supply voltage		VCC	3.0	3.3	3.6	V	
Permissive input ripple voltage		V_{RF}	-	-	100	mVp-p	VCC=3.3V
Input Signal Voltage	High Level	V_{IL}	0	-	0.3xVCC	V	R0~R5;G0~G5;B0~B5 DOTCLK; Hsync; Vsync ENABLE;R/L;U/D
	Low Level	V_{IH}	0.7xVCC	-	VCC	V	
Common Electrode Driving Signal		VCOM	-	4.87	-	V	Note1
Current of VCC Power supply		I_{VCC}	-	TBD	TBD	mA	Note2

Note1: For different LCM, the value may have a bit of difference.
Note2: To test the current dissipation, use "all Black Pattern".

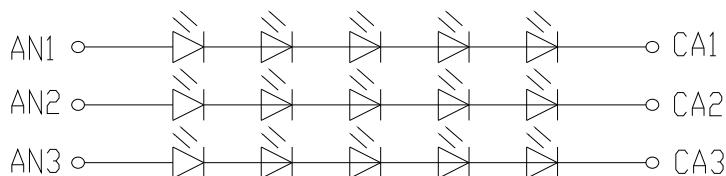**Table 4.1 LCD module electrical characteristics**

4.2 Backlight Unit

Ta=25℃

Item	Symbol	Min	Typ	Max	Unit	Remark
Channel1	$I_{channel\ 1}$	-	25.0	-	mA	Note 1
Channel2	$I_{channel\ 2}$	-	25.0	-	mA	
Channel3	$I_{channel\ 3}$	-	25.0	-	mA	
Forward Voltage	V_{BL}	14.85	16	18.15	V	
Life Time	-	25,000	（50,000）		Hrs	Note 3

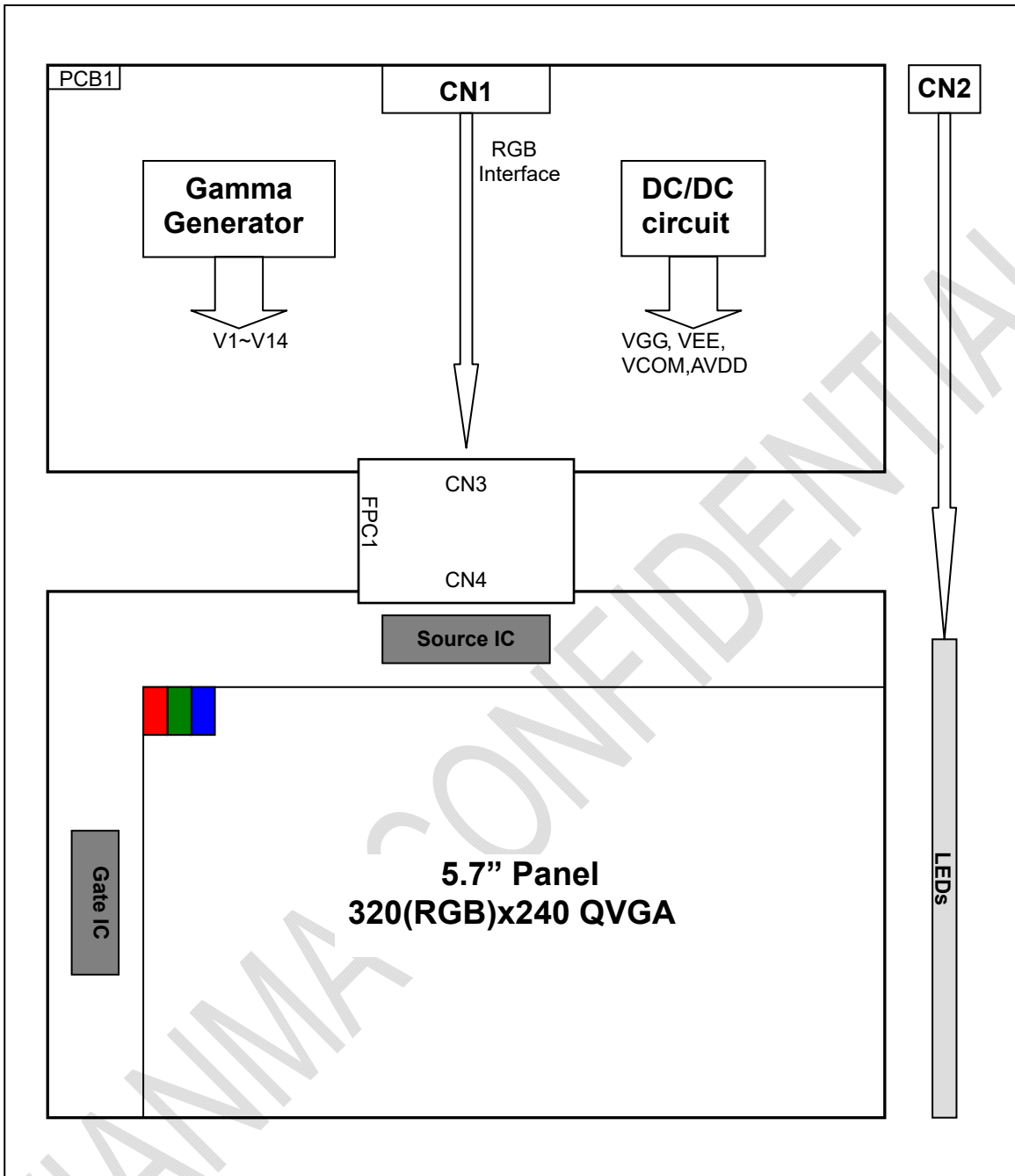Note 1: I_F is defined for one channel LED. There are total three LED channels in back light unit
Note 2: Optical performance should be evaluated at Ta=25℃ only.
Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced.Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

LED Curcuit

4.3   Block Diagram


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┌─────────────────────────────────────────────────────────────────┐
│  ┌──────────────────────────────────────────────┐    ┌─────┐     │
│  │ PCB1                    ┌───────────┐          │    │ CN2 │     │
│  │                         │    CN1    │          │    └─────┘     │
│  │   ┌──────────────┐      └───────────┘  ┌─────────────┐         │
│  │   │    Gamma     │        RGB          │   DC/DC     │         │
│  │   │  Generator   │      Interface      │   circuit   │         │
│  │   └──────────────┘                     └─────────────┘         │
│  │         ↓                  ↓                 ↓                  │
│  │       V1~V14                              VGG, VEE,            │
│  │                                           VCOM,AVDD            │
│  │                        ┌───────────┐                          │
│  │                   FPC1 │   CN3     │                          │
│  │                        │           │                          │
│  │                        │   CN4     │                          │
│  │                        └───────────┘                          │
│  │                     ┌──────────────┐                          │
│  │                     │  Source IC   │                          │
│  │   ┌──┬──┬──┐        └──────────────┘              ┌──┐        │
│  │   │R │G │B │                                       │LEDs│       │
│  │   └──┴──┴──┘                                       │   │        │
│  │   ┌───┐              5.7" Panel                    │   │        │
│  │   │Gate│          320(RGB)x240 QVGA                │   │        │
│  │   │ IC │                                           │   │        │
│  │   └───┘                                            └──┘        │
│  └──────────────────────────────────────────────┘                │
└─────────────────────────────────────────────────────────────────┘
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5. Data input timing
5.1 SYNC mode

Parameter	Symbol	Symbol	Min	Typ	Max	Unit
DOTCLK	DOTCLK frequency	Fclk	6.2	6.4	12.1	MHz
	DOTCLK cycle	Tclk	82.64	156.25	161.29	ns
Hsync	Horizontal display area	Thd	320	320	320	Tclk
	1 horizontal line	Th	406	408	560	Tclk
	Hsync pulse width	Thpw	1	-	-	Tclk
	Horizontal blanking	Thb	70	70	70	Tclk
	Horizontal front porch	Thfp	16	18	170	Tclk
Vsync	Frame rate	-	-	60	65	Hz
	Vertical display area	Tvd	240	240	240	Th
	Vsync period time	Tv	254	263	360	Th
	Vsync pulse width	Tvpw	1	-	-	Th
	Vsync blanking	Tvb	13	13	13	Th
	Vsync front porch	Tvfp	1	10	107	Th

5.2 DE mode

Description		Symbol	Min.	Typ.	Max.	Unit
DOTCLK frequency		Fclk	6.2	6.4	12.1	MHz
Horizontal section	Horizontal total	Th	406	408	560	Tclk
	H Total blank	Thb+Thfp	86	88	240	Tclk
	Valid Data Width	Thd	320	320	320	Tclk
Vertical section	Frame rate	-	-	60	65	Hz
	Vertical total	Tv	254	263	360	Th
	V total blank	Tvb+Tvfp	14	23	120	Th
	Valid Data Width	Tvd	240	240	240	Th

Note: The LCM could auto-detect which mode is working.

5.3 Timing Diagram

5.3.1 Vertical Input Timing
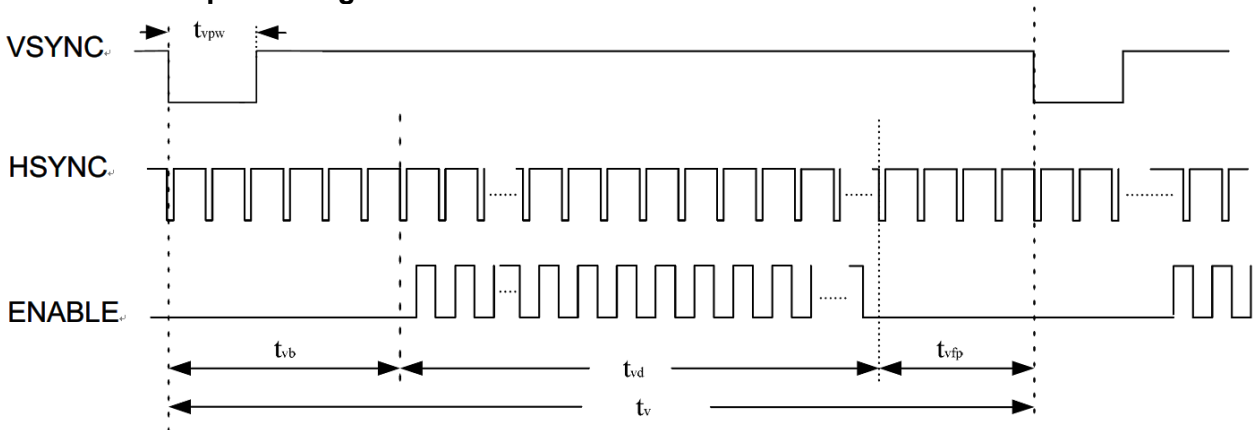
5.3.2 Horizontal Input Timing
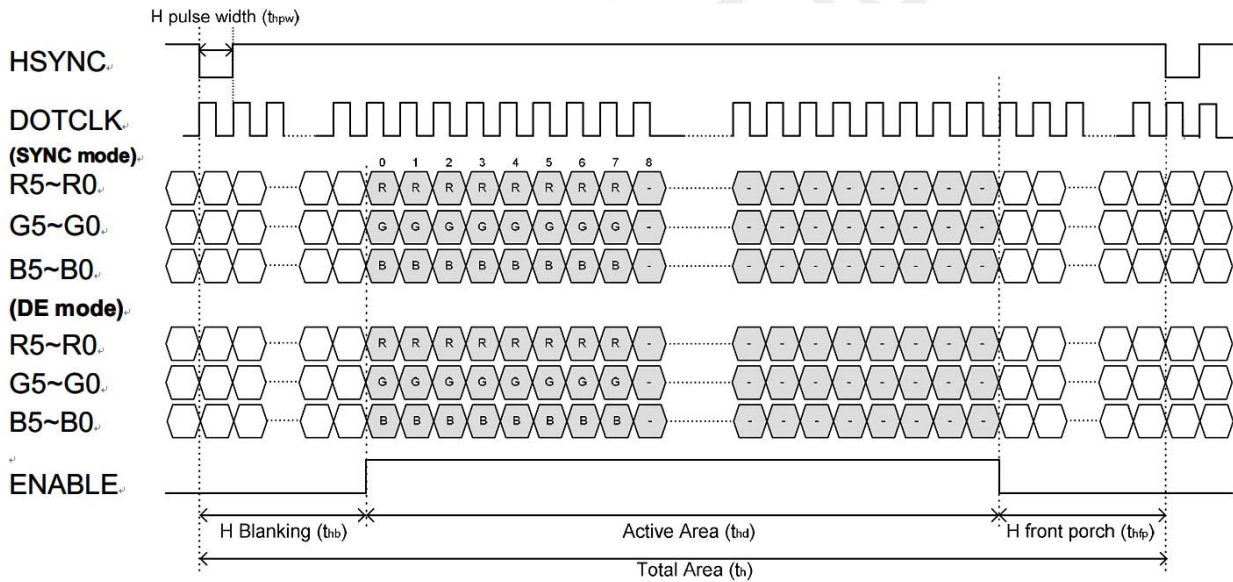
5.4 AC input characteristics

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
DOTCLK pulse duty	Tcwh	40%	50%	60%	Tclk	Tcph is DCLK cycle
VSYNC setup time	Tvst	10	-	-	ns	
VSYNC hold time	Tvhd	10	-	-	ns	
HSYNC setup time	Thst	10	-	-	ns	
HSYNC hold time	Thhd	10	-	-	ns	
Data setup time	Tdsu	10	-	-	ns	Rn, Gn, Bn to DCLK
Data hold time	Tdhd	10	-	-	ns	Rn, Gn, Bn to DCLK
Enable setup time	Tesu	10			ns	
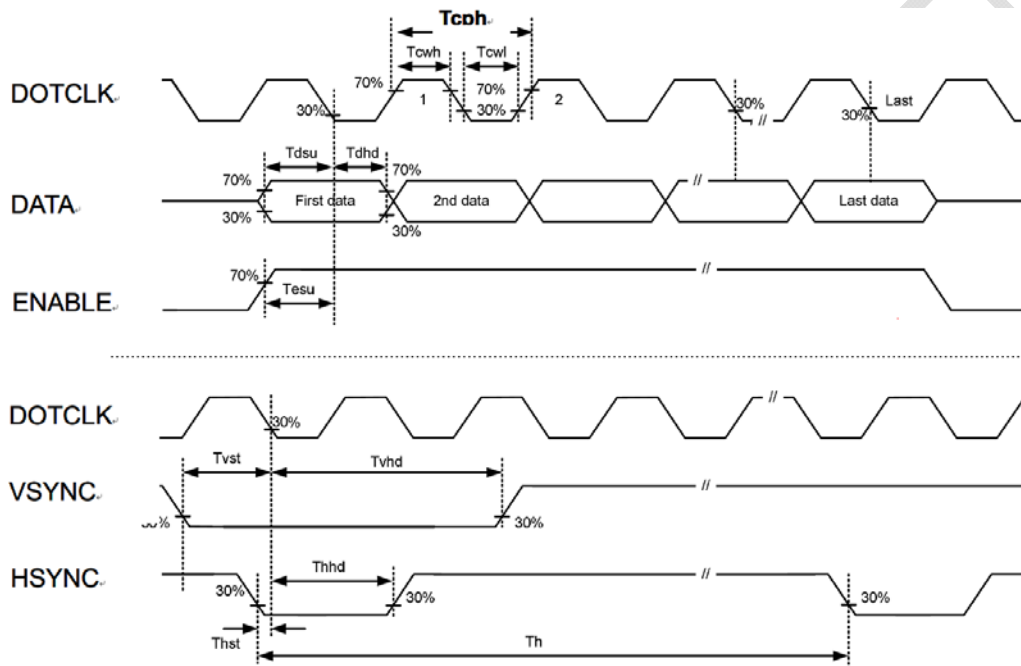
5.5 Power ON/OFF Sequence

Item	Symbol	Min	Typ	Max	Unit	Remark
VCC 3.0V to signal starting	Tp1	5	-	50	ms	
Signal starting to backlight on	Tp2	50	-	-	ms	
Signal off to VCC 3.0V	Tp3	5	-	50	ms	
Backlight off to signal off	Tp4	50	-	-	ms	
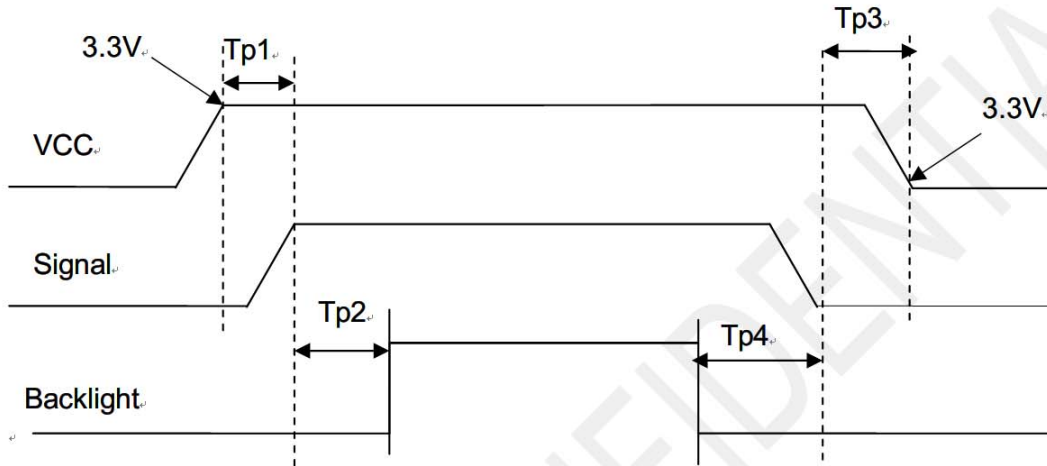
Figure 5.5 Power on/off sequence

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6 Optical Characteristics

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θT	CR≧10	60	70	--	Degree	Note2
		θB		50	60	--		
		θL		60	70	--		
		θR		60	70	--		
Contrast Ratio		CR	θ=0°	400	500	-		Note 3
Response Time		T_{ON}	25℃	-	20	30	ms	Note 4
		T_{OFF}						
Chromaticity	**White**	x	Backlight is on	0.264	0.314	0.364		Note 1,5
		y		0.297	0.347	0.397		
	Red	x		0.549	0.599	0.649		Note 1,5
		y		0.297	0.347	0.397		
	Green	x		0.284	0.334	0.384		Note 1,5
		y		0.519	0.569	0.619		
	Blue	x		0.086	0.136	0.186		Note 1,5
		y		0.075	0.125	0.175		
Uniformity		U		75	80	-	%	Note 6
NTSC				-	50	-	%	Note 5
Luminance		L		250	320	-	cd/m^2	Note 7

Test Conditions:

1. I_F= 25 mA(one LED), and the ambient temperature is 25℃.

2. The test systems refer to Note 1 and Note 2.

Note 1:   Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 Minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

Photo detector

Field

500mm

TFT-LCD Module

LCD Panel

The center of the screen
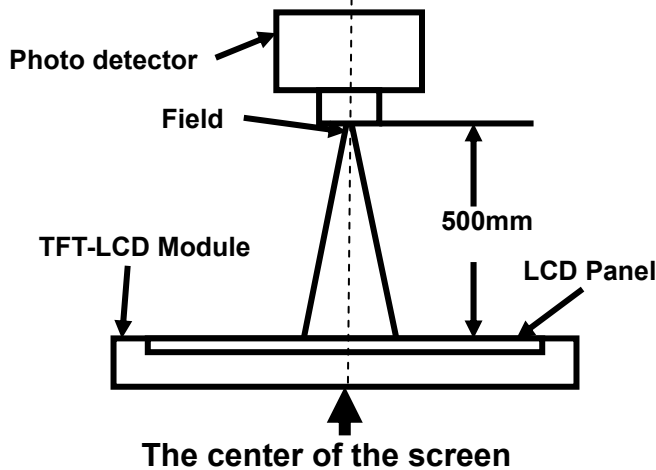
Note 2:   Definition of viewing angle range and measurement system.

viewing angle is measured at the center point of the LCD
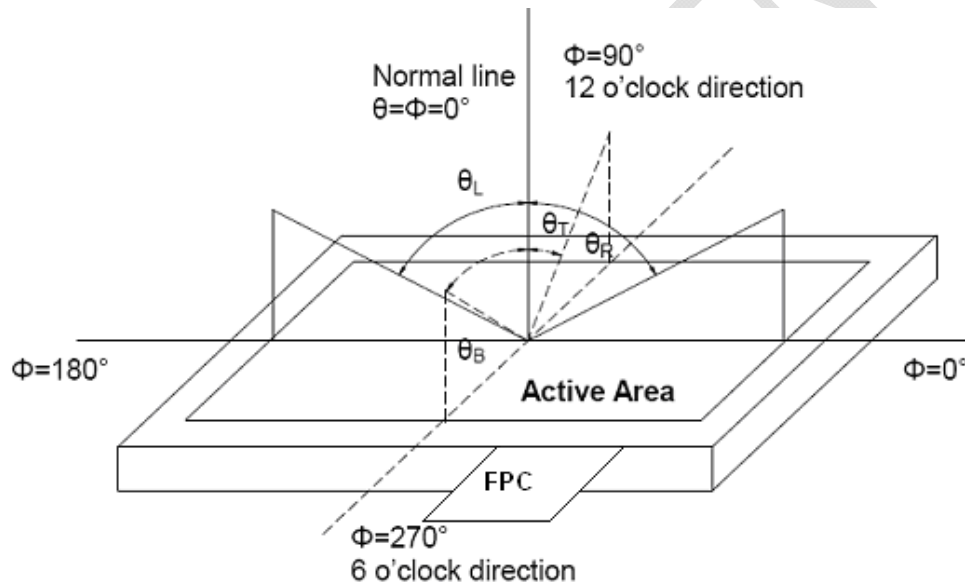
Note 3:   Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

"White state ": The state is that the LCD should drive by Vwhite.

"Black state": The state is that the LCD should drive by Vblack.

Vwhite: To be determined   Vblack: To be determined.

Note 4:   Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.
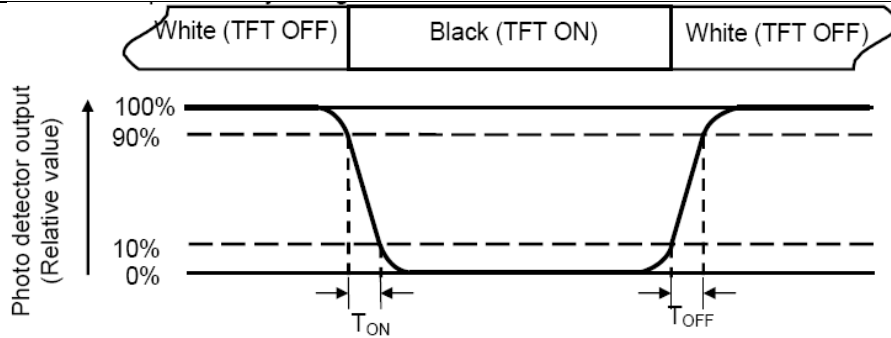
Note 5:   Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6:   Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer Fig. 2). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U) = Lmin/ Lmax

L-------Active area length W----- Active area width
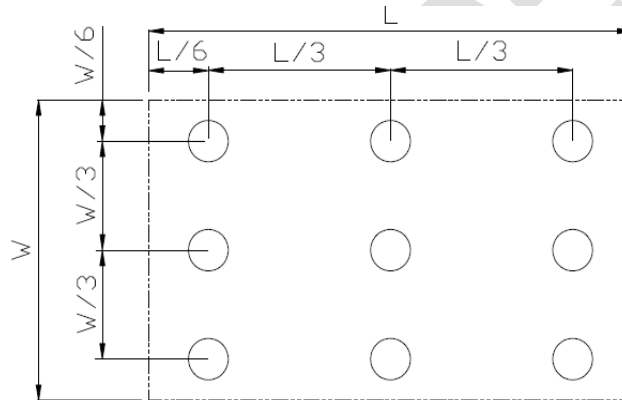
Lmax: The measured Maximum luminance of all measurement position.

Lmin: The measured Minimum luminance of all measurement position.

Note 7:   Definition of Luminance: Measure the luminance of white state at center point.

7 Environmental / Reliability Test

No	Test Item	Condition	Remarks
1	High Temperature Operation	Ts=+70℃±2℃, 96hrs	IEC60068-2-1:2007 GB2423.2-2008
2	Low Temperature Operation	Ta=-20℃±2℃, 96hrs	IEC60068-2-1:2007 GB2423.1-2008
3	High Temperature Storage	Ta=+80℃±2℃, 96hrs	IEC60068-2-1:2007 GB2423.2-2008
4	Low Temperature Storage	Ta=-30℃±2℃, 96hrs	IEC60068-2-1:2007 GB2423.1-2008
5	Storage at High Temperature and Humidity	Ta=+60℃, 90% RH, 96hrs	IEC60068-2-78 :2001 GB/T2423.3—2006
6	Thermal Shock (non-operation)	-20℃ 30 min~+60℃ 30 min, Change time:5min, 100 Cycle	Start with cold temperature, End with high temperature, IEC60068-2-14,GB2423.22
7	Electro Static Discharge (operation)	C=150pF,R=330Ω,5point/panel Air:±15Kv,5times; Contact:±8Kv,5times (Environment:15℃~35℃, 30%~60%.86Kpa~106Kpa)	IEC61000-4-2:2001 GB/T17626.2-2006
8	Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2h for each direction of X.Y.Z. (6 hours for total)	IEC60068-2-6:1982 GB/T2423.10—1995
9	Mechanical Shock (Non OP)	Half Sine Wave 60G 6ms, ±X,±Y,±Z 3times for each direction	IEC60068-2-27 GB/T2423.5
10	Package Drop Test	Height:80cm, 1corner,3edges,6surfaces	IEC60068-2-32:1990 GB/T2423.8—1995
11	Package Vibration Test	Frequency range: 10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2h for each direction of X.Y.Z. (6 hours for total)	IEC60068-2-34 GB/T2423.11

Note1: Ts is the temperature of panel's surface.
Note2: Ta is the ambient temperature of samples.

8 Mechanical Drawing
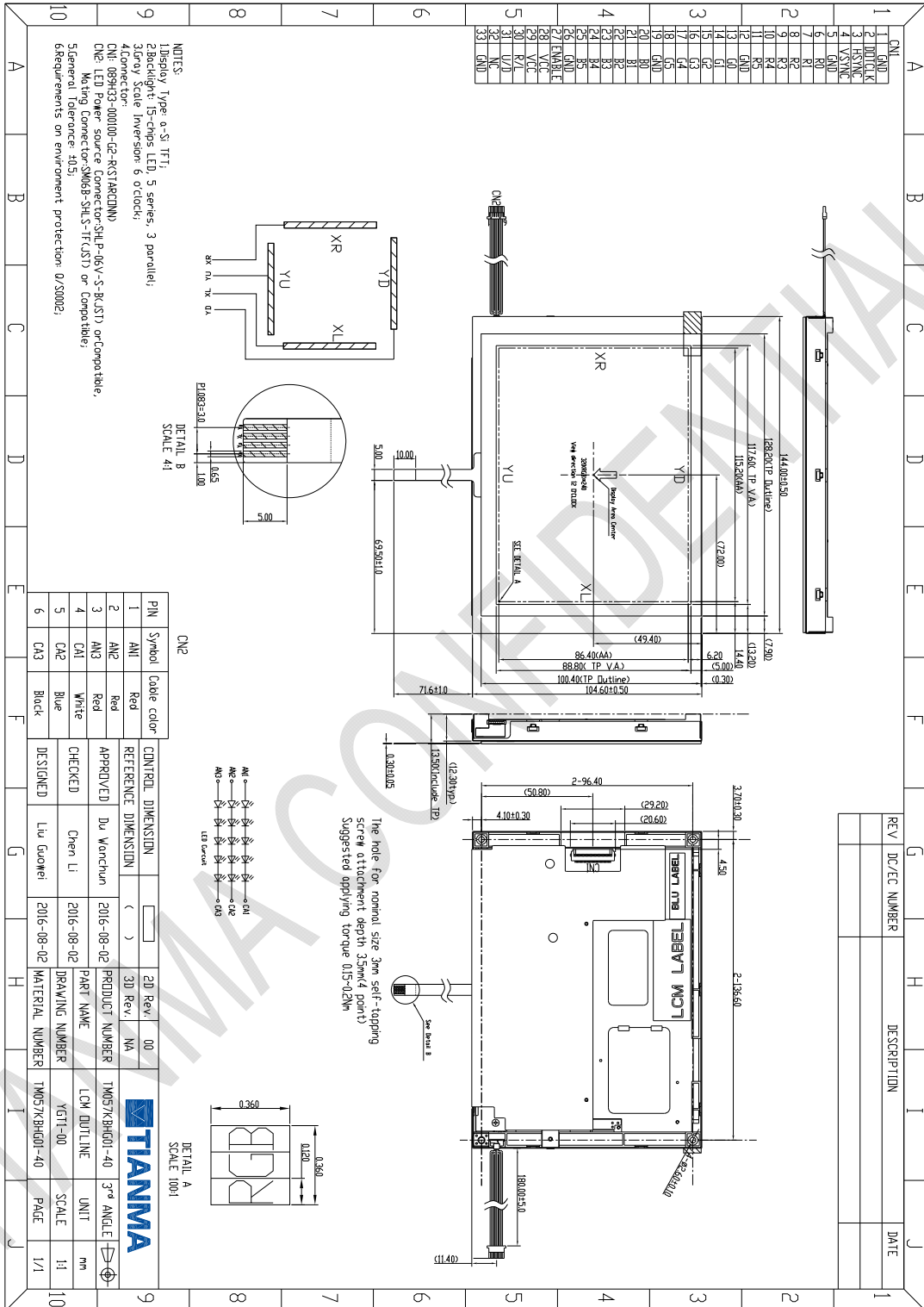
9 Packing Drawing

No	Item	Model(Material)	Dimensions (mm)	Unit Weigt (Kg)	Quantity	Remark
1	LCM module	TM057KBHG01-40	144 x 104.6 x 13.5	TBD	40	
2	Partition_1	Corrugated paper	513X333X215	1.388	1	
3	Anti-static Bag	PE	180X165X0.05	0.001	40	Anti-static
4	Dust-Proof Bag	PE	700X530	0.06	1	
5	Partition_2	Corrugated Paper	505X332X4.0	0.098	2	
6	Corrugated Bar	Corrugated paper	513X110×31	0.048	4	
7	Carton	Corrugated paper	530X350X250	1.12	1	
8	Total weight		TBD			
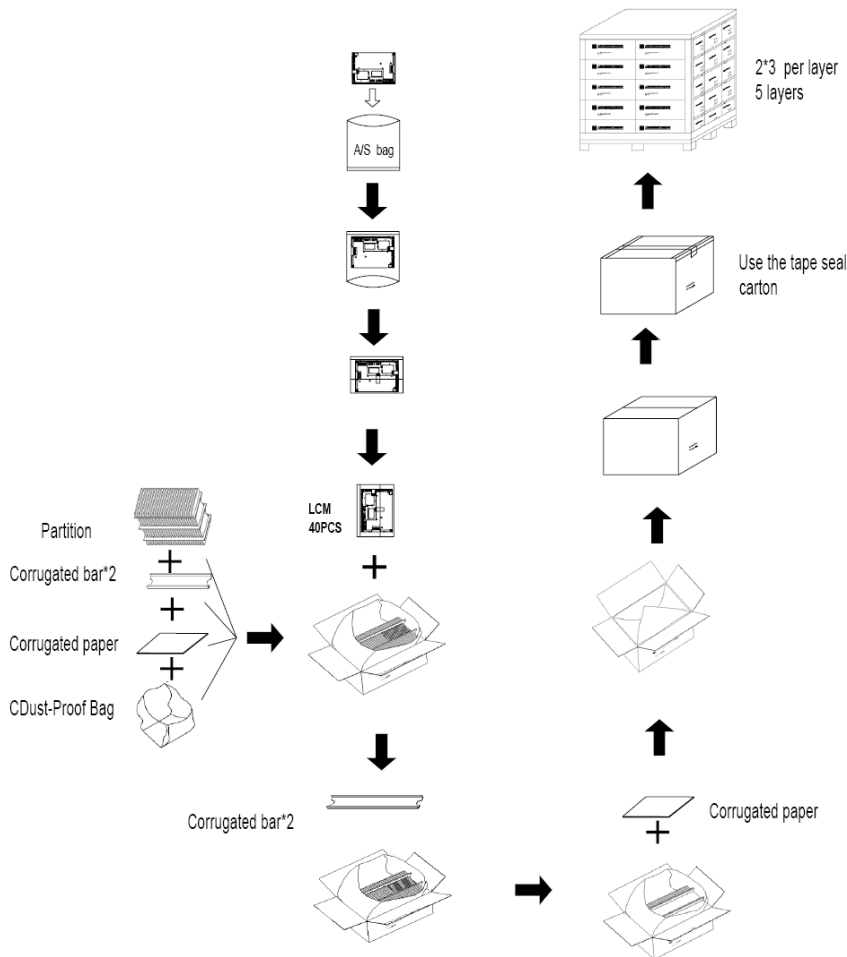
10 Precautions for Use of LCD Modules

a)   Handling Precautions

   i.   The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

   ii.   If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

   iii.   Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

   iv.   The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

   v.   If the display surface is contaMinated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

   一 Isopropyl alcohol

   一 Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer. Especially, do not use the following:

   一 Water

   一 Ketone

   一 Aromatic solvents

   vi.   Do not attempt to disassemble the LCD Module.

   vii.   If the logic circuit power is off, do not apply the input signals.

   viii.   To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

      10.1.8.1   Be sure to ground the body when handling the LCD Modules.

      10.1.8.2   Tools required for assembly, such as soldering irons, must be properly ground.

      10.1.8.3   To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

      10.1.8.4   The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

b)   Storage precautions

   i.   When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

   ii.   The LCD modules should be stored under the storage temperature range. If the LCD modules will be stored for a long time, the recommend condition is:

Temperature : 0℃ ～ 40℃ Relatively humidity: ≤80%

   iii.   The LCD modules should be stored in the room without acid, alkali and harmful gas.

c)   Transportation Precautions

   i.   The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.