

***NF695 Series***  
***User's Manual***

**NO. G03-NF695-F**

**Revision: 2.0**

**Release date: October 18, 2018**

**Trademark:**

\* Specifications and Information contained in this documentation are furnished for information use only, and are subject to change at any time without notice, and should not be construed as a commitment by manufacturer.

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## Environmental Protection Announcement

Do not dispose this electronic device into the trash while discarding. To minimize pollution and ensure environment protection of mother earth, please recycle.



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## Environmental Safety Instruction

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- I Avoid the dusty, humidity and temperature extremes. Do not place the product in any area where it may become wet.
- I 0 to 60 centigrade is the suitable temperature. (The figure comes from the request of the main chipset)
- I Generally speaking, dramatic changes in temperature may lead to contact malfunction and crackles due to constant thermal expansion and contraction from the welding spots' that connect components and PCB. Computer should go through an adaptive phase before it boots when it is moved from a cold environment to a warmer one to avoid condensation phenomenon. These water drops attached on PCB or the surface of the components can bring about phenomena as minor as computer instability resulted from corrosion and oxidation from components and PCB or as major as short circuit that can burn the components. Suggest starting the computer until the temperature goes up.
- I The increasing temperature of the capacitor may decrease the life of computer. Using the close case may decrease the life of other device because the higher temperature in the inner of the case.
- I Attention to the heat sink when you over-clocking. The higher temperature may decrease the life of the device and burned the capacitor.

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## USER'S NOTICE

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## Manual Revision Information

Reversion	Revision History	Date
2.0	Second Edition	October 18, 2018

## Item Checklist

- R** Motherboard
- R** User's Manual
- R** CD for motherboard utilities
- R** Cable(s)

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# Chapter 1

## Introduction of the Motherboard

### 1-1 Feature of Motherboard

- I Intel® Apollo Lake series SoC Processor, with low power consumption never denies high performance
- I 1 \* SO-DIMM slot supports 1\* 1867 MHz DDR3L SO-DIMM, up to 8GB
- I Realtek ALC662 Audio
- I 1/2 \*10/100/1000 based-TX ethernet ports (optional)
- I Onboard 1\* SATAIII port &1\* M.2 Socket 3 slot for M-Key type 2242/2260 SATA SSD
- I Support HDMI/VGA/LVDS or eDP multiple-display solution (optional)
- I Support 2/10 \* COM port expansion (optional)
- I 4 \* External USB 3.0 & 6/2 \* Internal USB 2.0(optional)
- I Onboard 1\* full-size Mini-PCIE slot & 1\* SIM card slot(optional)
- I 1 \* Internal PS2 Keyboard & Mouse pin header
- I Compliance with ErP standard
- I Support Watchdog function
- I Support TPM function (optional)
- I Solution for Digital Signage, POS & Industrial Automation

## 1-2 Specification

Spec	Description
<b>Design</b>	<ul style="list-style-type: none"> <li>! Mini-ITX form factor; PCB size: 17.0x17.0cm</li> </ul>
<b>CPU</b>	<ul style="list-style-type: none"> <li>! Intel® Apollo Lake <b>*SoC</b> CPU</li> <li><i>*CPU model varies from different IPC options. Please consult your dealer for more information of onboard CPU.</i></li> </ul>
<b>Memory</b>	<ul style="list-style-type: none"> <li>! 1*DDR3L SO-DIMM slot</li> <li>! Support DDR3L 1867 MHz SO-DIMM up to 8GB</li> </ul>
<b>Expansion Slot</b>	<ul style="list-style-type: none"> <li>! <b>MPE1:</b>1* full-size Mini-PCIE slot</li> <li>! <b>SIMCARD:</b> 1* SIM card slot (Optional for <b>NF695HV</b> series)</li> </ul>
<b>Storage</b>	<ul style="list-style-type: none"> <li>! 1* SATAIII 6Gb/s port</li> <li>! 1* M.2 Socket 3 slot (M2, M-key, support type-2242 /2260SATA SSD)</li> </ul>
<b>LAN Chip</b>	<ul style="list-style-type: none"> <li>! 2* RTL8111H GbE (Optional for <b>NF695HV</b> series)</li> <li>! 1* RTL8111H GbE (Optional for <b>NF695L</b> series)</li> <li>! Support Fast Ethernet LAN function of providing 10/100/1000Mbps Ethernet data transfer rate</li> </ul>
<b>Audio Chip</b>	<ul style="list-style-type: none"> <li>! Realtek ALC662 HD Audio Codec integrated</li> <li>! Audio driver and utility included</li> </ul>
<b>BIOS</b>	<ul style="list-style-type: none"> <li>! AMI Flash ROM</li> </ul>
<b>Rear I/O</b>	<ul style="list-style-type: none"> <li>! 1* DCIN (12V) Power Jack</li> <li>! 2 * RJ-45 LAN port (Optional for <b>NF695HV</b> series)</li> <li>! 1 * RJ-45 LAN port (Optional for <b>NF695L</b> series)</li> <li>! 1* HDMI port +1*VGA port (Optional for <b>NF695HV</b> series)</li> <li>! 2* HDMI port (Optional for <b>NF695L</b> series)</li> <li>! 4* USB3.0 port</li> <li>! 1* Line-out port</li> </ul>

<b>Internal I/O</b>	<ul style="list-style-type: none"> <li>  1* Front panel header</li> <li>  3* 9-pin USB 2.0 header (<i>Optional for <b>NF695HV</b> series</i>)</li> <li>  1* 9-pin USB 2.0 header(<i>Optional for <b>NF695L</b> series</i>)</li> <li>  <b>COM1/2</b>: 2* 9-pin Serial port header</li> <li>  <b>COM3-6/COM7-10</b> : 2 * <i>Serial port header expansion block</i> (<i>Optional for <b>NF695HV</b> series</i>)</li> <li>  2* <i>LAN activity LED header</i> (<i>Optional for <b>NF695HV</b> series</i>)</li> <li>  1* <i>LAN activity LED header</i>(<i>Optional for <b>NF695L</b> series</i>)</li> <li>  1* PS2 Keyboard /Mouse header</li> <li>  1* Front panel audio header</li> <li>  1* 10-pin GPIO header</li> <li>  1*SMBUS header</li> <li>  1* front panel buzzer header</li> <li>  1* LVDS header (*<i>Selectable with <b>EDP</b></i>)</li> <li>  1* EDP header (*<i>Selectable with <b>LVDS</b></i>)</li> <li>  1* LVDS/EDP inverter header</li> </ul>
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**Note:**

1. The module should be **DDR3L 1.35V SODIMM** and **not exceeding 8GB total capacity**.
2. The SODIMM installed should be of or above the memory clock the model supported, otherwise the board will not start.



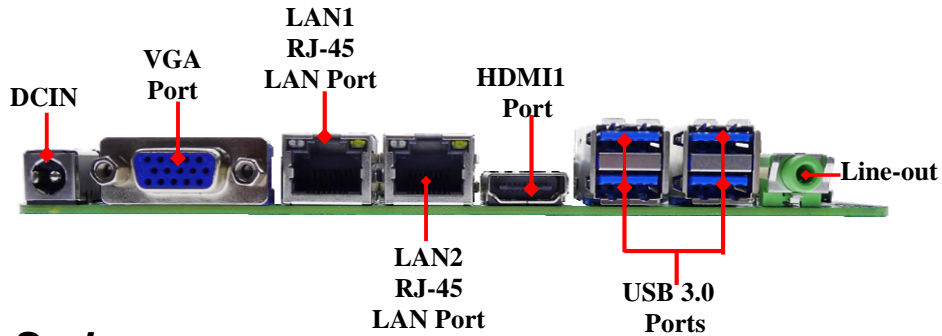
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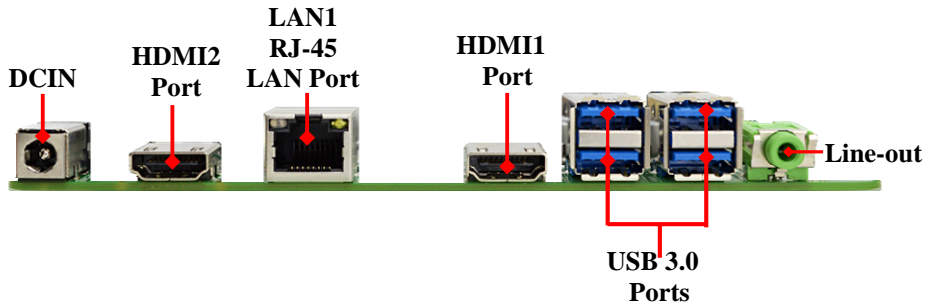
## 1-3 Layout Diagram

### *Rear IO Panel Diagram:*

#### *NF695HV Series:*



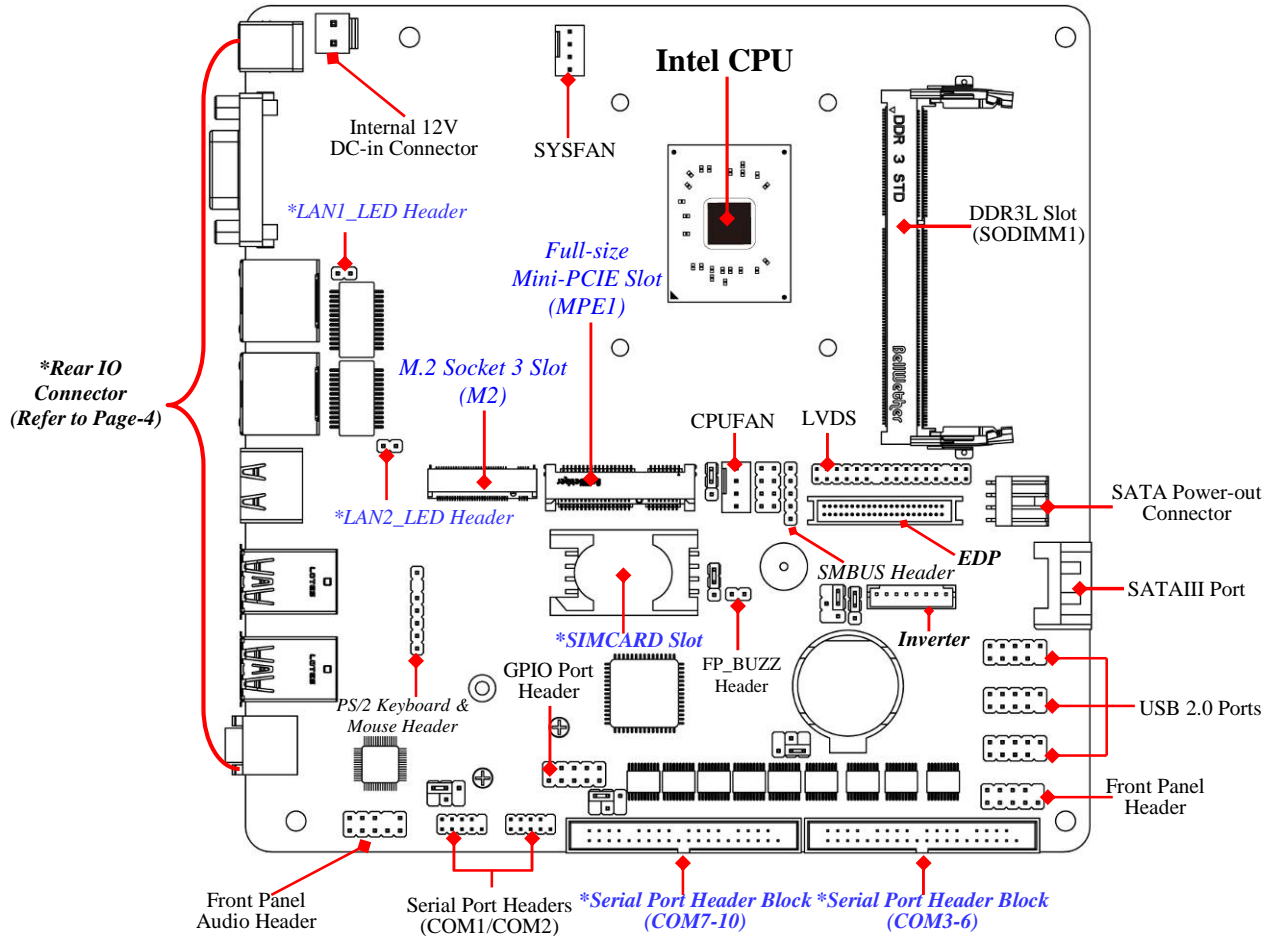
#### *NF695L Series:*



**Warning!!**The board has a 12V DC-in power connector (**DCIN3**) in I/O back panel and an internal 12V power connector (DCIN2). User can only connect one type of compatible power supply to one of them to power the system.

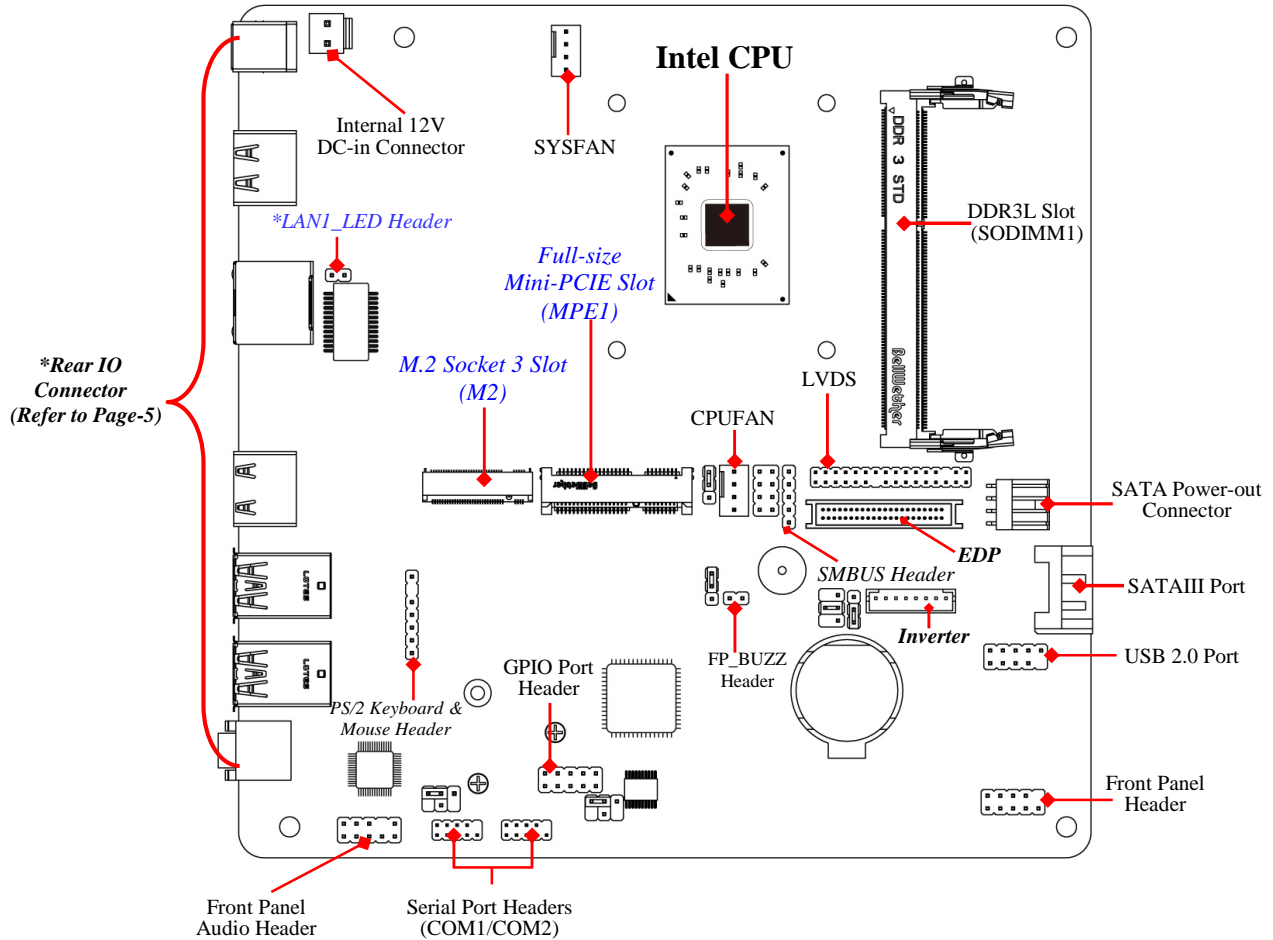
# Motherboard Internal Diagram

## NF695HV Series:



**Note:** SIMCARD slot only work when compatible SIM card installed & 3G LAN card installed in full-size Mini-PCIE (MPE1) slot.

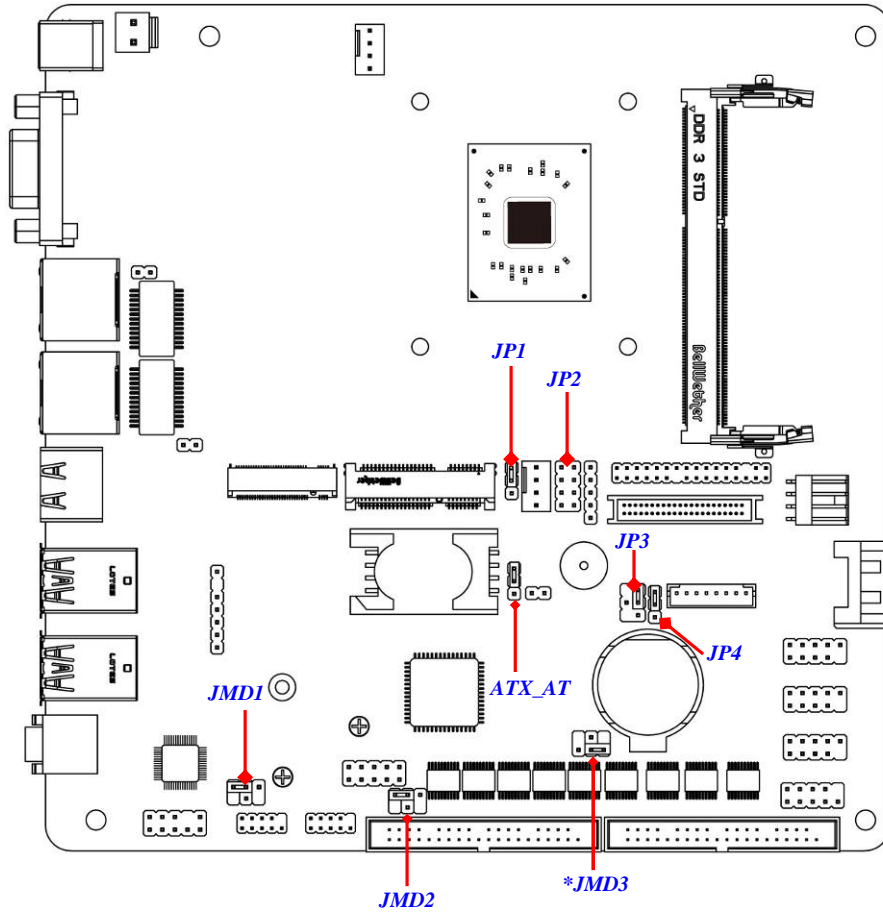
## NF695L Series:



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## Jumper Positions:



**\*Note:** the diagram above is from **NF695HV** series and serves as illustration purpose only. **JMD3** is optional to **NF695HV** series models, please refer to the model you purchased for actual specifications.

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## Jumper

Jumper	Name	Description
JMD1	COM1 Serial Port Power Select	4-pin Block
JMD2	COM2 Serial Port Power Select	4-pin Block
*JMD3 (Optional)	COM3 Serial Port Power Select	4-pin Block
JP1	MPE1 Mini-PCIE Power 3VSB / 3.3V Select	3-Pin Block
ATX_AT	ATX Mode / AT Mode Select	3-Pin Block
JP4	LVDS/EDP Inverter Power 3.3V/5V/12V Select	3-pin Block
JP3	LVDS/EDP Panel Power 3.3V/5V/12V Select	4-pin Block
JP2	Pin 1&2: CASE OPEN Pin 3&4: TXE Override Pin 5&6: Clear CMOS Pin 7&8: Clear ME_RTC	8-pin Block

**\*Note:** Jumper JMD3 is only optional for NF695HV series.

## Connectors

Connector	Name
DCIN3	12V DC input Power Jack
*HDMI2(Optional)	HDMI Port Connector
*VGA(Optional)	VGA Port Connector
LAN1	RJ-45 LAN Port Connector
*LAN2(Optional)	RJ-45 LAN Port Connector
HDMI1	HDMI Connector
USB31	USB3.0 Port Connector
USB32	USB3.0 Port Connector
FP_HP	<b>Top:</b> Audio Line-out Port Connector
DCIN2	Internal 12V DC-in Power Connector
SATA1	SATAIII Port Connector

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SATAPW	SATA Power Connector
CPUFAN	CPU Fan Connector
SYSFAN	System Fan Connector

**\*Note:** *VGA/LAN2* is only optional for **NF695HV** series; **HDMI2** is only optional for **NF695L** series.

## Headers

Header	Name	Description
FP	Front Panel Header (PWR LED/ HDD LED/ Power Button /Reset)	9-pin Block
F_USB1/ F_USB2/ F_USB3	USB 2.0 Header	9-pin Block
COM1	RS232 Serial Port Header	9-pin Block
COM2	RS232 Serial Port Header	9-pin Block
<b>*COM3-6</b> (Optional)	RS232 Serial Port Combo Block	36-pin Block
<b>*COM7-10</b> (Optional)	RS232 Serial Port Combo Block	36-pin Block
LAN1_LED	LAN Activity LED Header	2-pin Block
<b>*LAN2_LED</b> (Optional)	LAN Activity LED Header	2-pin Block
PS2KBMS	PS2 keyboard & Mouse	6-pin Block
FP_AUDIO	Front Panel Audio Header	9-pin Block
GPIO	GPIO Port Header	10-pin Block
SMBUS	SMBUS Header	5-pin Block
FP_BUZZ	Buzzer Header	2-pin Block
INVERTER	LVDS INVERTER Header	8-pin Block
EDP	EDP Wafer	40-pin Block
LVDS	LVDS Header	32-pin Block

**\*Note:** *COM3-6/COM7-10/LAN2\_LED* is only optional for **NF695HV** series.

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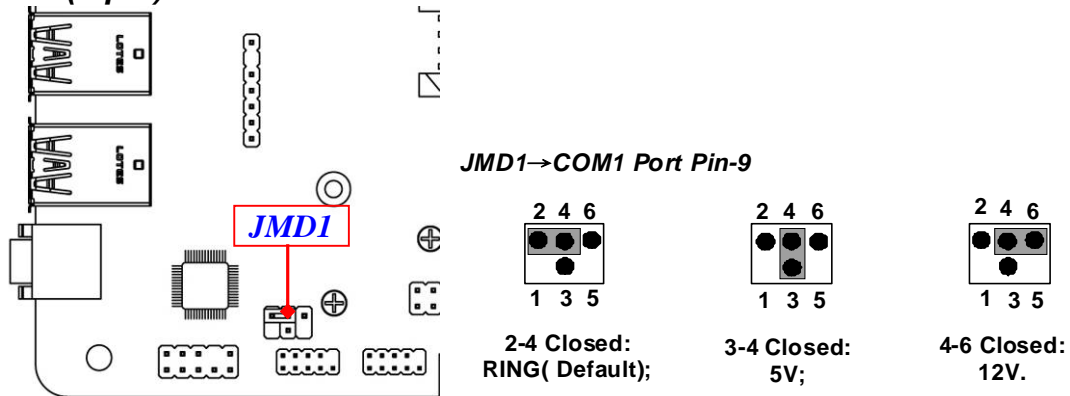
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# Chapter 2

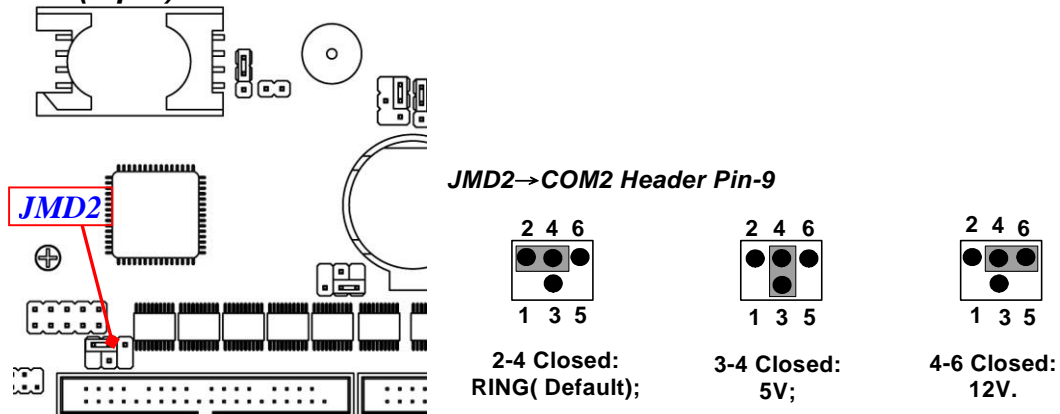
## Hardware Installation

### 2-1 Jumper Setting

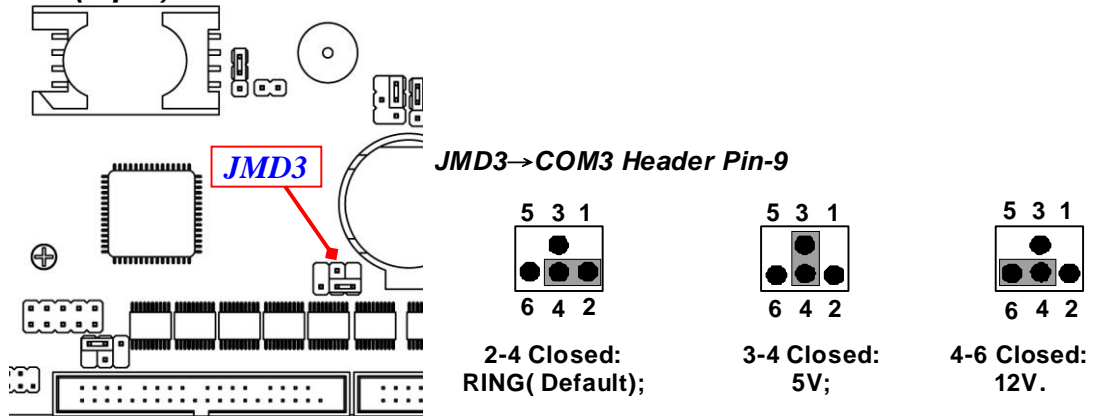
#### (1) JMD1 (4-pin): COM1 Port Pin9 Function Select



#### (2) JMD2 (4-pin): COM2 Header Pin9 Function Select

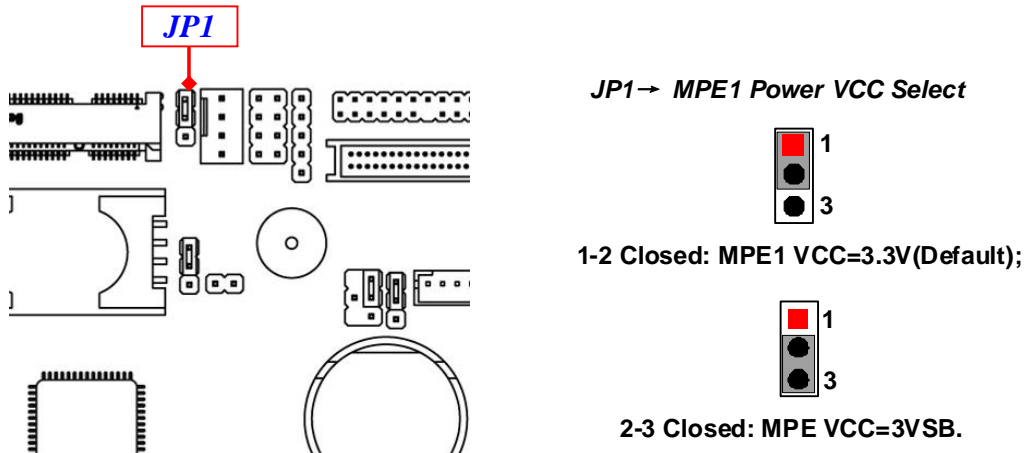


**(3) JMD3 (4-pin): COM3 Header Pin9 Function Select**



**\*Note:** JMD3 is only optional for NF695HV series models with COM3 serial port header(from COM3-6 combo block).

**(4) JP1 (3-pin): MPE1 Mini-PCIE Slot Power VCC3.3V / 3VSB Select**

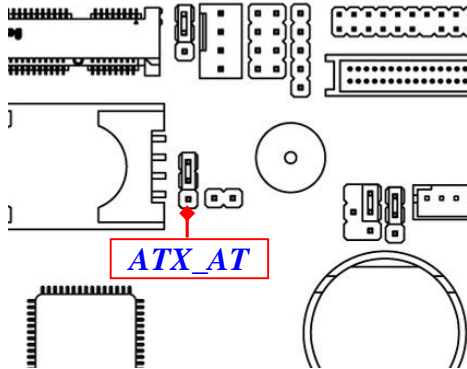




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**(5) ATX\_AT (3-pin): AT/ATX Mode Select**



ATX\_AT → ATX/AT Mode Select



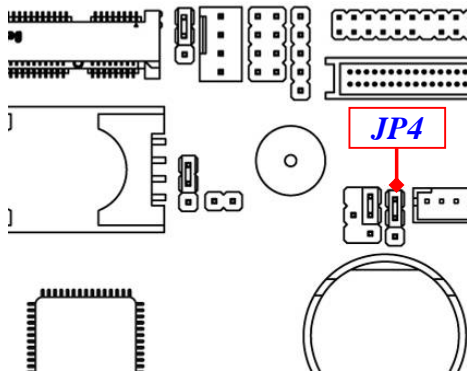
1-2 Closed: ATX Mode (Default);



2-3 Closed: AT Mode.

**\*ATX Mode Selected:** Press power button to power on after power input ready;  
**AT Mode Selected:** Directly power on as power input ready.

**(6) JP4 (3-pin): LVDS / EDP Inverter Backlight VCC 5V/12V Select**



JP4 → LVDS Panel VCC Select



1-2 Closed: VCC=5V (Default);

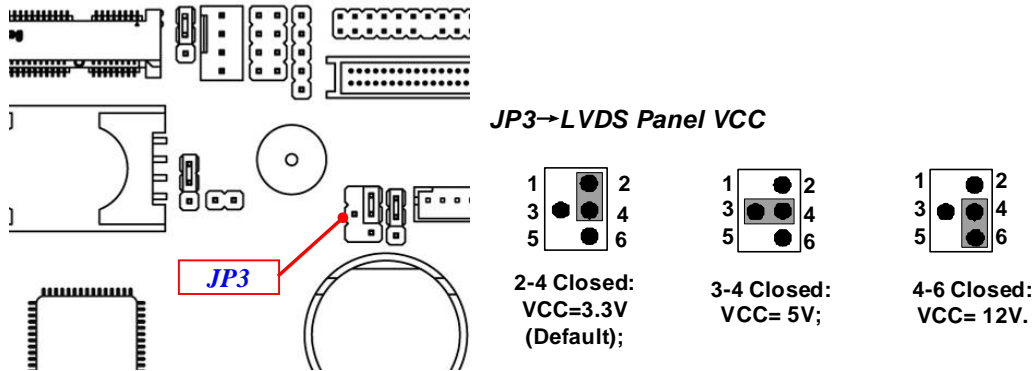


2-3 Closed: VCC=12V.

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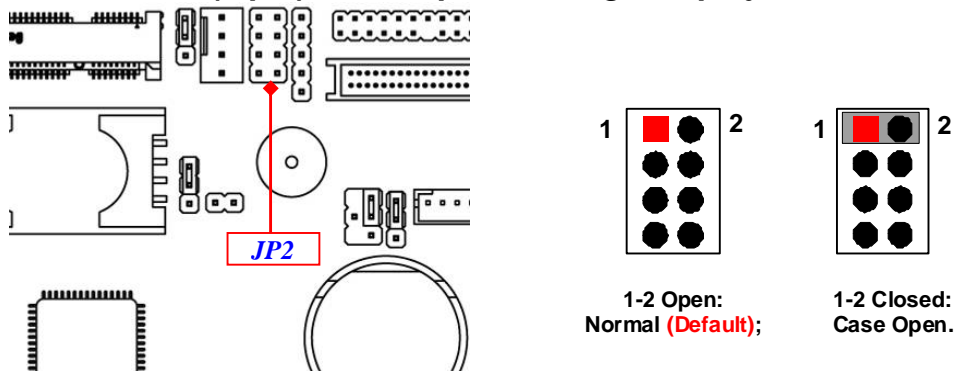
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**(7) JP3 (4-pin): LVDS / EDP Panel VCC 3.3V/5V/12V Select**



**(8) JP2 (8-pin): Jumper Combo Block**

**Pin 1&2 of JP2 (8-pin): Case Open Message Display Function Select**

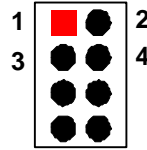
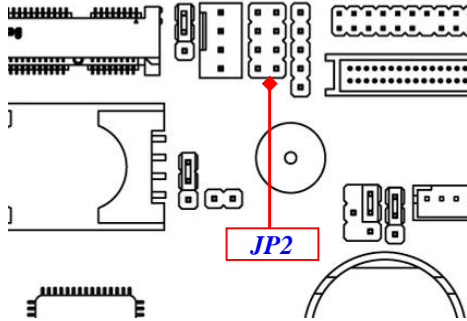


**Pin (1-2) Close:** When Case Open function pin short to GND, the Case Open function was detected. When Used, needs to enter BIOS and enable '**Case Open Detect**' function. In this case if your case is removed, next time when you restart your computer, a message will be displayed on screen to inform you of this.

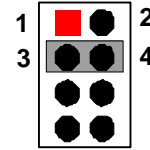
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**Pin 3&4 of JP2 (8-pin): TXE Override Setting**

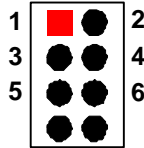
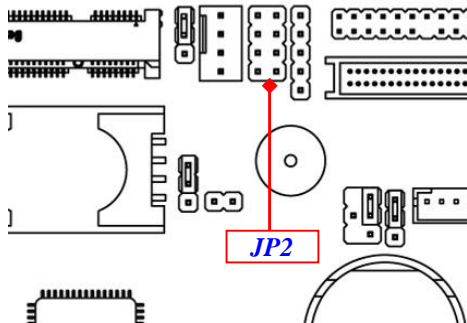


3-4 Open :  
Normal (Default);

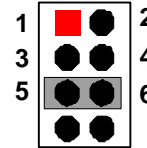


3-4 Closed :  
TXE Override.

**Pin 5&6 of JP2 (8-pin): Clear CMOS**

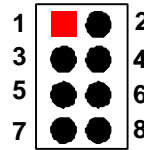
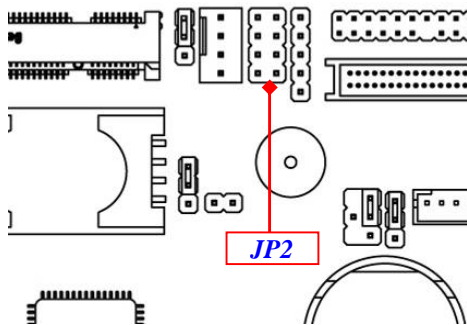


5-6 Open:  
Normal (Default);

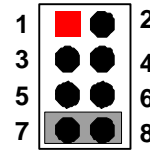


5-6 Closed:  
Clear CMOS.

**Pin 7&8 of JP2 (8-pin): Clear ME\_RTC**



7-8 Open :  
Normal (Default);



7-8 Closed :  
Clear ME\_RTC.

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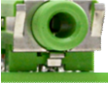
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## 2-2 Connectors and Headers

### 2-2-1 Connectors

#### (1) Rear I/O Connectors

***\*Refer to Page-4.***

Icon	Name	Function
	Power Jack	12V DC-in system power connector For user to connect compatible power adapter to provide power supply for the system.
	HDMI Port	To connect display device that support HDMI specification.
	*VGA Port (Optional)	To connect display device that support VGA specification.
	RJ-45 LAN Port	This connector is standard RJ-45 LAN jack for Network connection.
	USB 3.0 Port	To connect USB keyboard, mouse or other devices compatible with USB specification. USB 3.0 ports supports up to 5Gbps data transfer rate.
	Line-Out	This connector can functions as audio Line-Out jack and MIC jack with compatible cables & devices.

**\*Note:** The diagrams above are from **NF695HV** series and serves as illustration purpose only. In the case of any differences please refer to the model you purchased for actual specifications.

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## (2) DCIN2 (2-pin): Internal 12V DC-in power connector

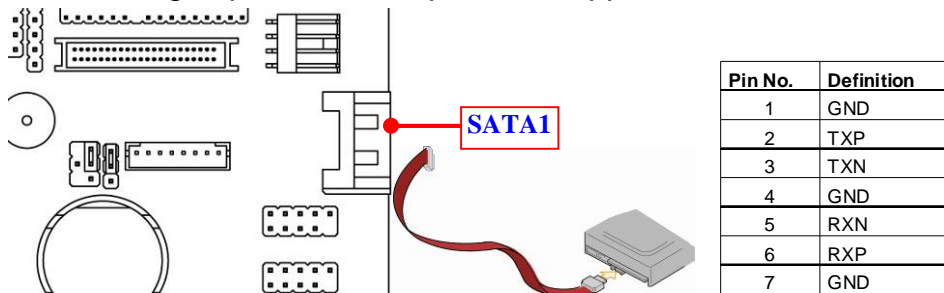


### Warning!!

The board has a 12V DC-in power jack (**DCIN3**) in I/O back panel and an internal 12V power connector (**DCIN2**). User can only connect one type of compatible power supply to one of them to power the system.

## (3) SATA1 (7-pin block): SATAIII Port connector

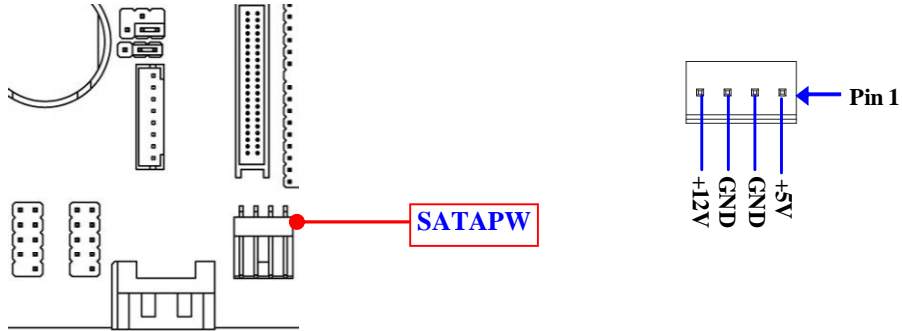
This connector is a high-speed SATAIII port that supports 6 GB/s transfer rate.



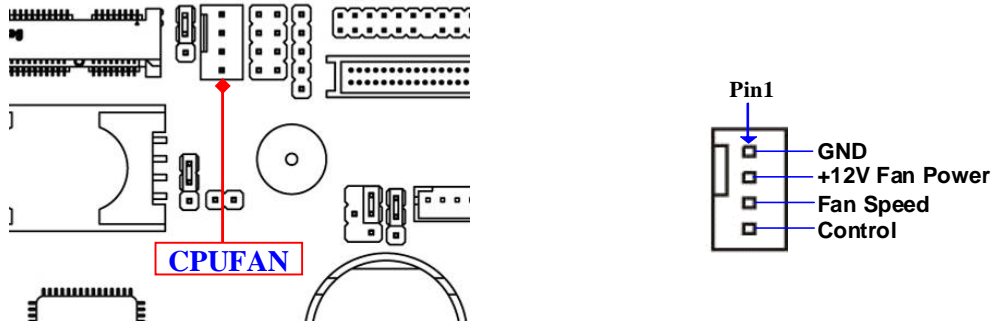
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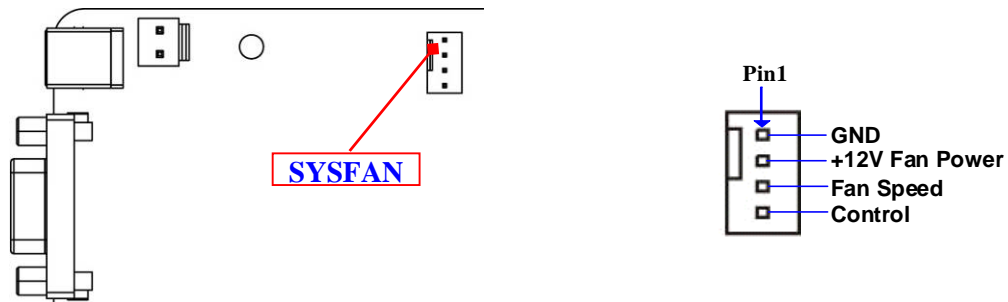
**(4) SATAPW (4-pin) : SATA Power-out Connector**



**(5) CPUFAN (4-pin): CPU FAN Connector**



**(6) SYSFAN (4-pin): FAN Connector**



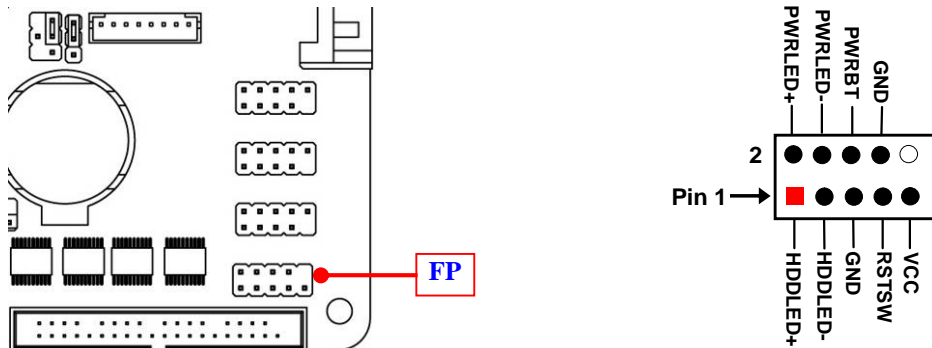
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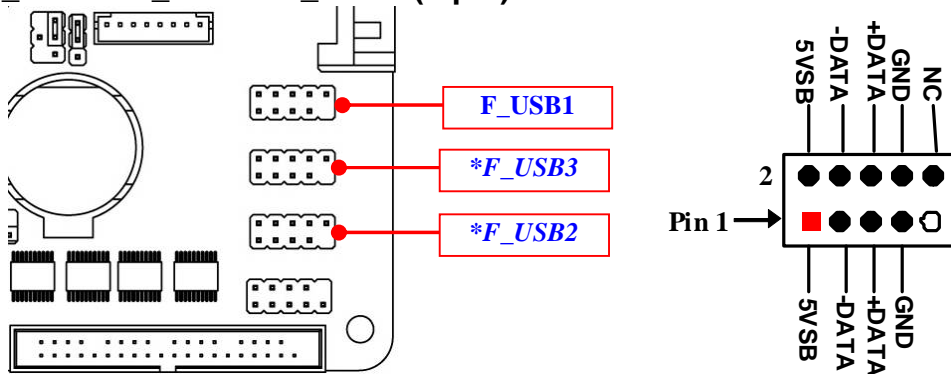
## 2-2-2 Headers

**\*Notice:** The following diagrams serves as illustration purpose only. Some of the headers are only optional. In the case of any differences please refer to the model you purchased for actual specifications.

### (1) FP (9-pin): Front Panel Header



### (2) F\_USB1 / F\_USB2 / F\_USB3 (9-pin): USB 2.0 Port Header



**\*Note:** The diagram serves as illustration purpose only. *F\_USB2* / *F\_USB3* is only optional to **NF695HV** series.

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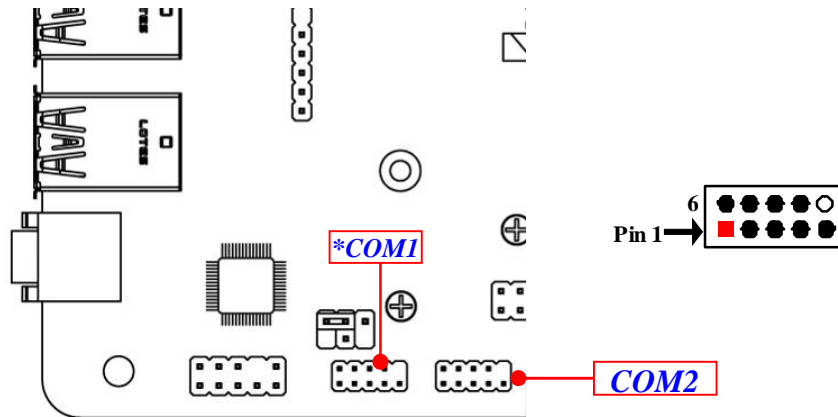
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### (3) COM1/COM2 (9-pin): Serial Port Header

**COM1:** RS232/422/485 Serial Port Header for NF695HV series;

RS232 Serial Port Header for NF695L series;

**COM2:** RS232 Serial Port Header.

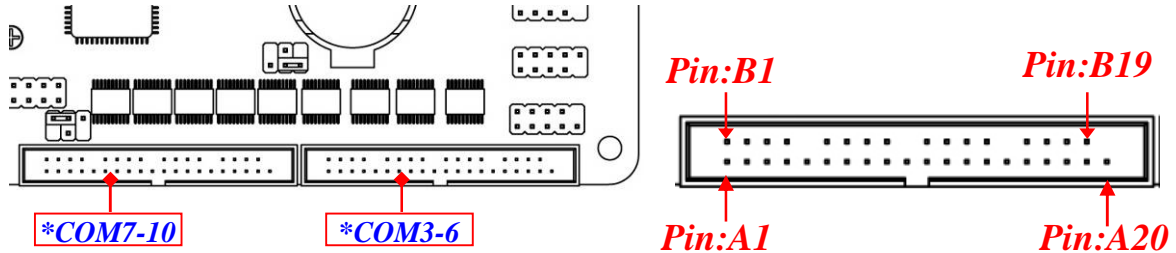


Pin NO.	RS232	*RS422	*RS485
Pin 1	DCD	TX-	DATA-
Pin 2	RXD	TX+	DATA+
Pin 3	TXD	RX+	NC
Pin 4	DTR	RX-	NC
Pin 5	GND	GND	GND
Pin 6	DSR	NC	NC
Pin 7	RTS	NC	NC
Pin 8	CTS	NC	NC
Pin 9	RI	NC	NC

**\*Note:** In the case that COM1 header supports RS422, RS485 function, besides connecting compatible COM cable for RS422 or RS 485 function, user also needs to go to BIOS to set corresponding '**Transmission Mode Select**' for COM1 (refer to Page-31).



#### (4) COM3-6/COM7-10 (36-Pin): RS232 Serial Port Header (\*Optional)



COM3-6	Pin NO.	RS232	Pin NO.	RS232
<b>COM3</b>	<b>Pin A1</b>	<b>DCD3</b>	<b>Pin B1</b>	<b>DSR3</b>
	Pin A2	SIN3	Pin B2	RTS3
	Pin A3	SOUT3	Pin B3	CTS3
	Pin A4	DTR3	Pin B4	RI3
	Pin A5	GND	Pin B5	N/A
<b>COM4</b>	<b>Pin A6</b>	<b>DCD4</b>	<b>Pin B6</b>	<b>DSR4</b>
	Pin A7	SIN4	Pin B7	RTS4
	Pin A8	SOUT4	Pin B8	CTS4
	Pin A9	DTR4	Pin B9	RI4
	Pin A10	GND	Pin B10	N/A
<b>COM5</b>	<b>Pin A11</b>	<b>DCD5</b>	<b>Pin B11</b>	<b>DSR5</b>
	Pin A12	SIN5	Pin B12	RTS5
	Pin A13	SOUT5	Pin B13	CTS5
	Pin A14	DTR5	Pin B14	RI5
	Pin A15	GND	Pin B15	N/A
<b>COM6</b>	<b>Pin A16</b>	<b>DCD6</b>	<b>Pin B16</b>	<b>DSR6</b>
	Pin A17	SIN6	Pin B17	RTS6
	Pin A18	SOUT6	Pin B18	CTS6
	Pin A19	DTR6	Pin B19	RI6
	Pin A20	GND	Pin B20	N/A

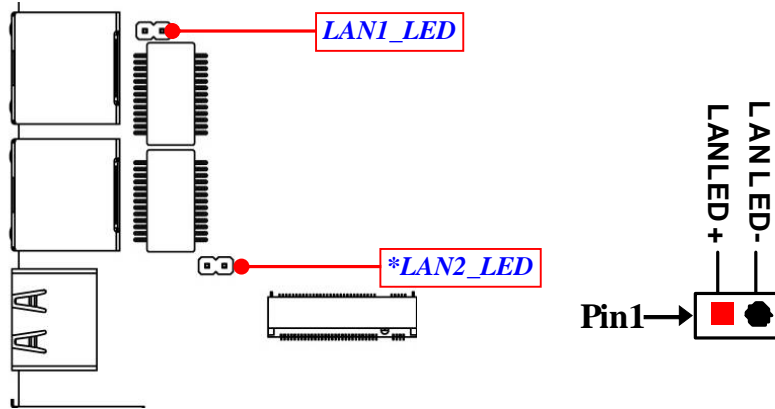
COM7-10	Pin NO.	RS232	Pin NO.	RS232
<b>COM7</b>	<b>Pin A1</b>	<b>DCD7</b>	<b>Pin B1</b>	<b>DSR7</b>
	Pin A2	SIN7	Pin B2	RTS7
	Pin A3	SOUT7	<b>Pin B3</b>	CTS7
	Pin A4	DTR7	Pin B4	RI7
	Pin A5	GND	Pin B5	N/A
<b>COM8</b>	<b>Pin A6</b>	<b>DCD8</b>	<b>Pin B6</b>	<b>DSR8</b>
	Pin A7	SIN8	Pin B7	RTS8
	Pin A8	SOUT8	Pin B8	CTS8
	Pin A9	DTR8	Pin B9	RI8
	Pin A10	GND	Pin B10	N/A
<b>COM9</b>	<b>Pin A11</b>	<b>DCD9</b>	<b>Pin B11</b>	<b>DSR9</b>
	Pin A12	SIN9	Pin B12	RTS9
	Pin A13	SOUT9	Pin B13	CTS9
	Pin A14	DTR9	Pin B14	RI9
	Pin A15	GND	Pin B15	N/A
<b>COM10</b>	<b>Pin A16</b>	<b>DCD10</b>	<b>Pin B16</b>	<b>DSR10</b>
	Pin A17	SIN10	Pin B17	RTS10
	Pin A18	SOUT10	Pin B18	CTS10
	Pin A19	DTR10	Pin B19	RI10
	Pin A20	GND	Pin B20	N/A

**\*Note:** The diagram serves as illustration purpose only. COM3-6 /COM7-10 is only optional to NF695HV series.

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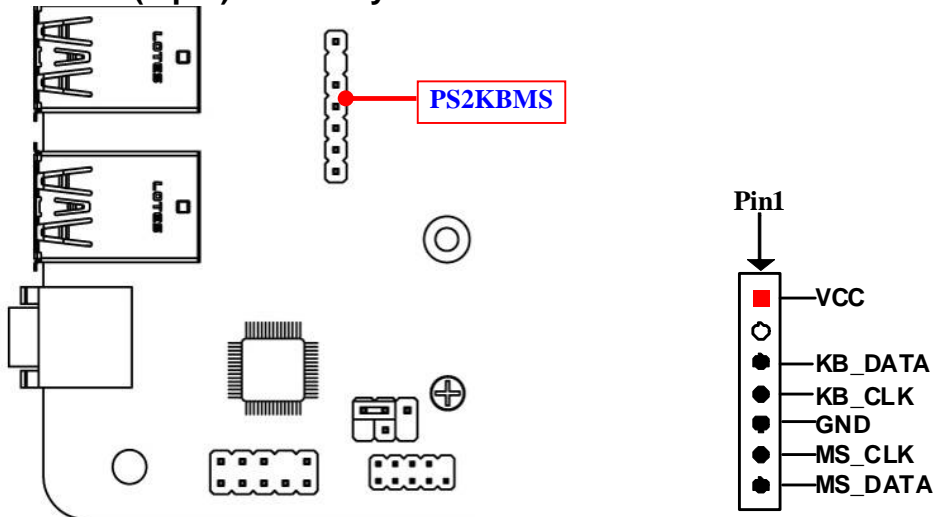
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(5) LAN1\_LED/LAN2\_LED (2-pin): LAN Activity LED Header



*\*Note: LAN2\_LED header is optional to NF695HV series only.*

(6) PS2KBMS (6-pin): PS/2 Keyboard & Mouse Header

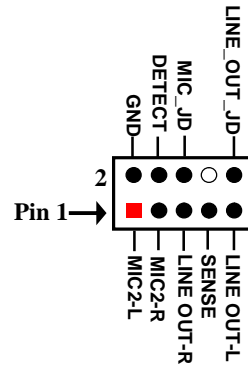
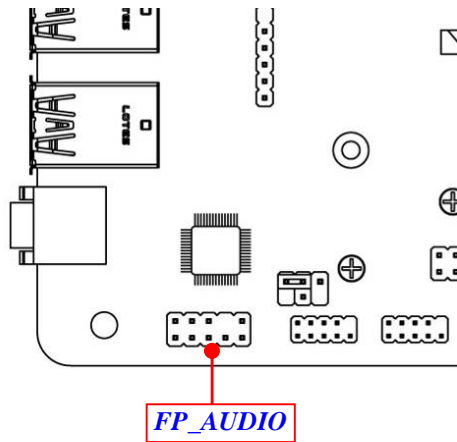


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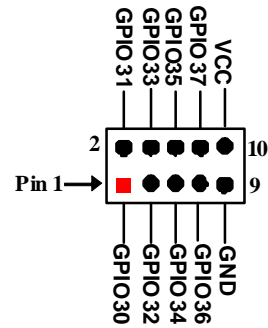
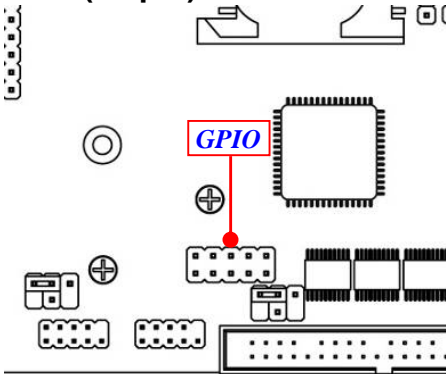
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### (7) FP\_AUDIO (9-pin): Line-Out, MIC-In Header

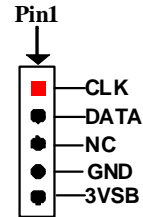
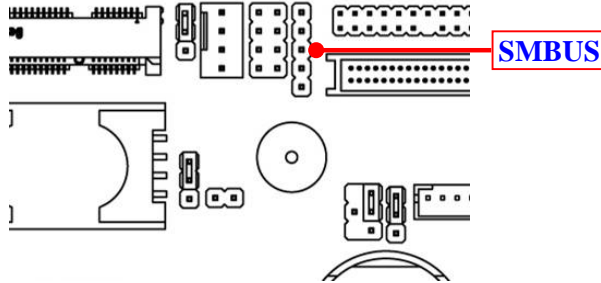
This header connects to Front Panel Line-out, MIC-In connector with cable.



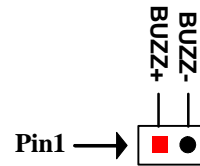
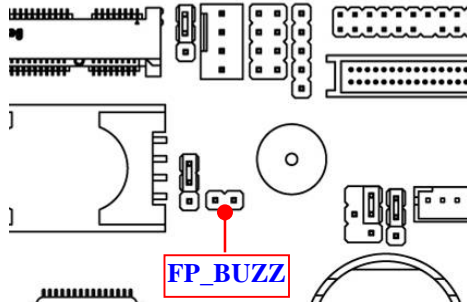
### (8) GPIO (10-pin): GPIO Header



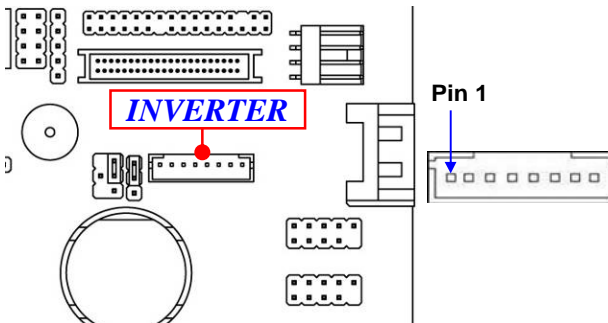
(9) SMBUS (5-pin): SMBUS Header



(10) FP\_BUZZ (2-pin): Buzzer Header



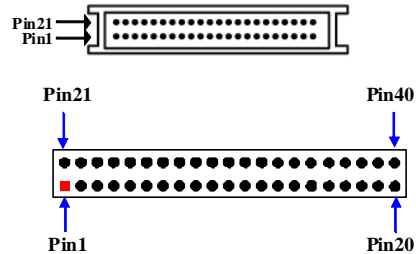
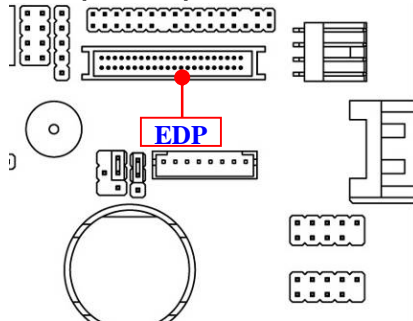
(11) INVERTER (8-pin): LVDS Inverter Connector



Pin No.	Definition
1	Backlight Enable
2	Backlight PWM
3	Back Light LED VCC
4	Back Light LED VCC
5	GND
6	GND
7	Backlight Up SW
8	Backlight Down SW

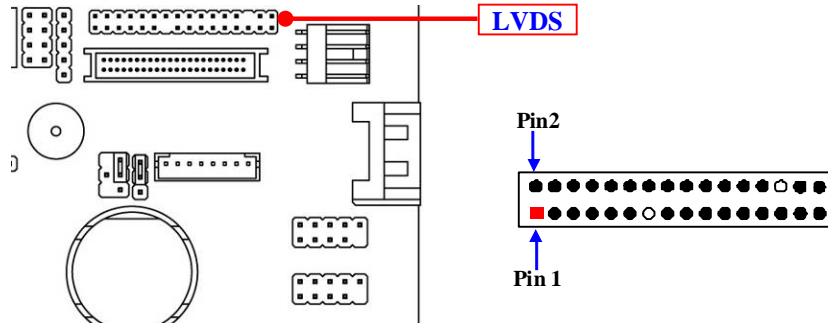
**Warning!** Find Pin-1 location of the inverter and make sure that the installation direction is correct! Otherwise serious harm will occur to the board/display panel!!

(12) \*EDP (40-Pin): EDP wafer



Pin NO.	Pin Define	Pin NO.	Pin Define
Pin 1	XX	Pin 21	XX
Pin 2	GND	Pin 22	XX
Pin 3	EDP_TXN3	Pin 23	GND
Pin 4	EDP_TXP3	Pin 24	GND
Pin 5	GND	Pin 25	GND
Pin 6	EDP_TXN2	Pin 26	GND
Pin 7	EDP_TXP2	Pin 27	EDP_HPDP
Pin 8	GND	Pin 28	GND
Pin 9	EDP_TXN1	Pin 29	GND
Pin 10	EDP_TXP1	Pin 30	GND
Pin 11	GND	Pin 31	EDP_DETECT
Pin 12	EDP_TXN0	Pin 32	EDP_Backlight Enable
Pin 13	EDP_TXP0	Pin 33	EDP_Backlight PWM
Pin 14	GND	Pin 34	XX
Pin 15	EDP_AUXP	Pin 35	XX
Pin 16	EDP_AUXN	Pin 36	Backlight power
Pin 17	GND	Pin 37	Backlight power
Pin 18	LCD_VCC	Pin 38	Backlight power
Pin 19	LCD_VCC	Pin 39	Backlight power
Pin 20	LCD_VCC	Pin 40	XX

(13) \*LVDS (30-Pin): 24-bit dual channel LVDS Header



Pin NO.	Pin Define	Pin NO.	Pin Define
Pin 1	E-DATAN3	Pin 2	E-DATAP3
Pin 3	E-CLKN	Pin 4	E-CLKP
Pin 5	E-DATAN2	Pin 6	E-DATAP2
Pin 7	E-DATAN1	Pin 8	E-DATAP1
Pin 9	E-DATAN0	Pin 10	E-DATAP0
Pin 11	DDC_SDA	Pin 12	DDC_SCL
Pin 13	XX	Pin 14	GND
Pin 15	GND	Pin 16	GND
Pin 17	O-DATAP3	Pin 18	O-DATAN3
Pin 19	O-CLKP	Pin 20	O-CLKN
Pin 21	O-DATAP2	Pin 22	O-DATAN2
Pin 23	O-DATAP1	Pin 24	O-DATAN1
Pin 25	O-DATAP0	Pin 26	O-DATAN0
Pin 27	LCD_VCC	Pin 28	XX
Pin 29	LCD_VCC	Pin 30	LCD_VCC
Pin 31	GND	Pin 32	GND

**\*Note:** User can choose between LVDS and EDP display options, but **only one of them can function at the same time**. Before connecting compatible cable to corresponding header/wafer, user should go to BIOS settings→**Chipset**→ **Uncore Configuration**→ **Active LFP**, and set it as **[LVDS]** or **[EDP]** based on actual configuration.

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# Chapter 3

## Introducing BIOS

**Notice!** The BIOS options in this manual are for reference only. Different configurations may lead to difference in BIOS screen and BIOS screens in manuals are usually the first BIOS version when the board is released and may be different from your purchased motherboard. Users are welcome to download the latest BIOS version form our official website.

The BIOS is a program located on a Flash Memory on the motherboard. This program is a bridge between motherboard and operating system. When you start the computer, the BIOS program will gain control. The BIOS first operates an auto-diagnostic test called POST (power on self test) for all the necessary hardware, it detects the entire hardware device and configures the parameters of the hardware synchronization. Only when these tasks are completed done it gives up control of the computer to operating system (OS). Since the BIOS is the only channel for hardware and software to communicate, it is the key factor for system stability, and in ensuring that your system performance as its best.

### 3-1 Entering Setup

Power on the computer and by pressing <Del> immediately allows you to enter Setup. If the message disappears before your respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the “RESET” button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt> and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to

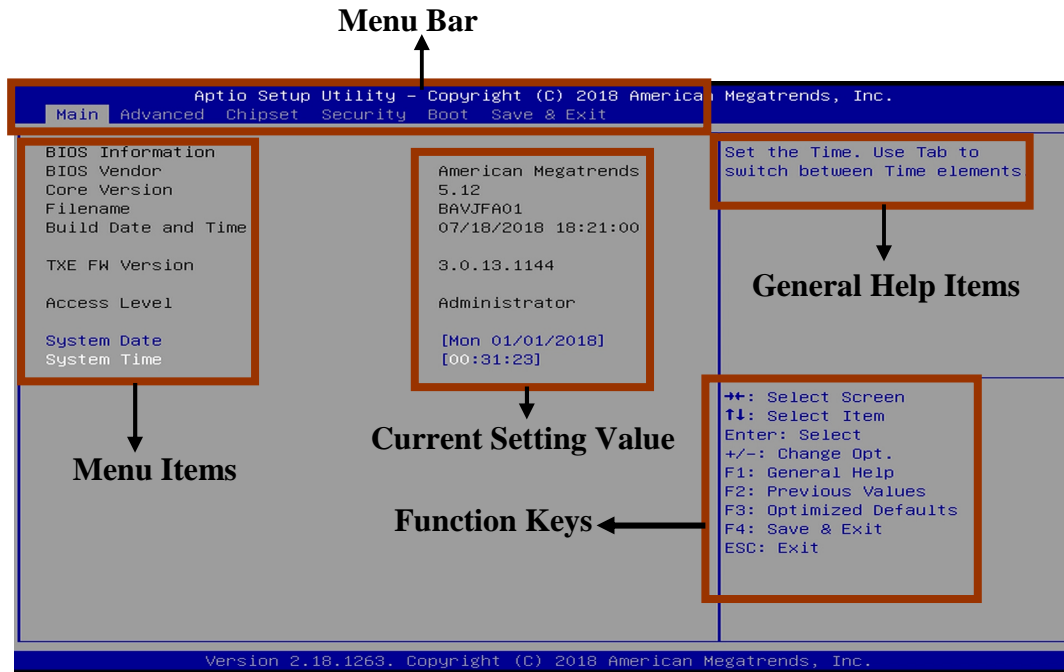
Press <Del> to enter Setup/ Press <F7> to enter Popup Menu.

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## 3-2 BIOS Menu Screen

The following diagram show a general BIOS menu screen:



## 3-3 Function Keys

In the above BIOS Setup main menu of, you can see several options. We will explain these options step by step in the following pages of this chapter, but let us first see a short description of the function keys you may use here:

- I Press → (left, right) to select screen;
- I Press - ↑ (up, down) to choose, in the main menu, the option you want to confirm or to modify.
- I Press <Enter> to select.



- 
- 
- I Press <+>/<-> keys when you want to modify the BIOS parameters for the active option.
  - I [F1]: General help.
  - I [F2]: Previous value.
  - I [F3]: Optimized defaults.
  - I [F4]: Save & Exit.
  - I Press <Esc> to quit the BIOS Setup.

## **3-4 Getting Help**

### **Main Menu**

The on-line description of the highlighted setup function is displayed at the top right corner the screen.

### **Status Page Setup Menu/Option Page Setup Menu**

Press [F1] to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window, press <Esc>.

## **3-5 Menu Bars**

**There are six menu bars on top of BIOS screen:**

<b>Main</b>	To change system basic configuration
<b>Advanced</b>	To change system advanced configuration
<b>Chipset</b>	To change chipset configuration
<b>Security</b>	Password settings
<b>Boot</b>	To change boot settings
<b>Save &amp; Exit</b>	Save setting, loading and exit options.

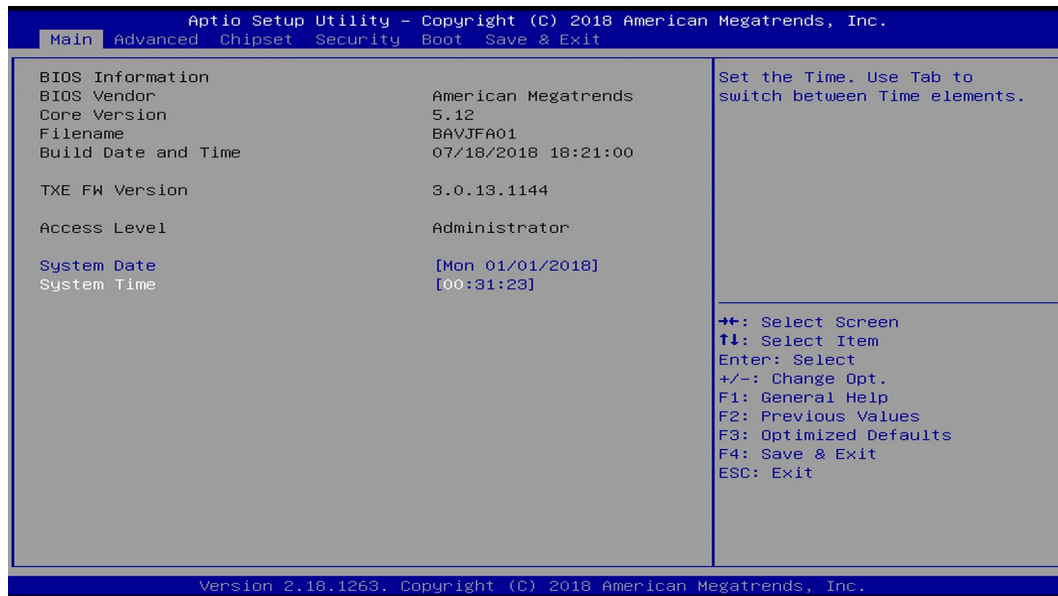
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User can press the right or left arrow key on the keyboard to switch from menu bar. The selected one is highlighted.

### 3-6 Main Menu

Main menu screen includes some basic system information. Highlight the item and then use the <+> or <-> and numerical keyboard keys to select the value you want in each item.



#### System Date

Set the date. Please use [Tab] to switch between date elements.

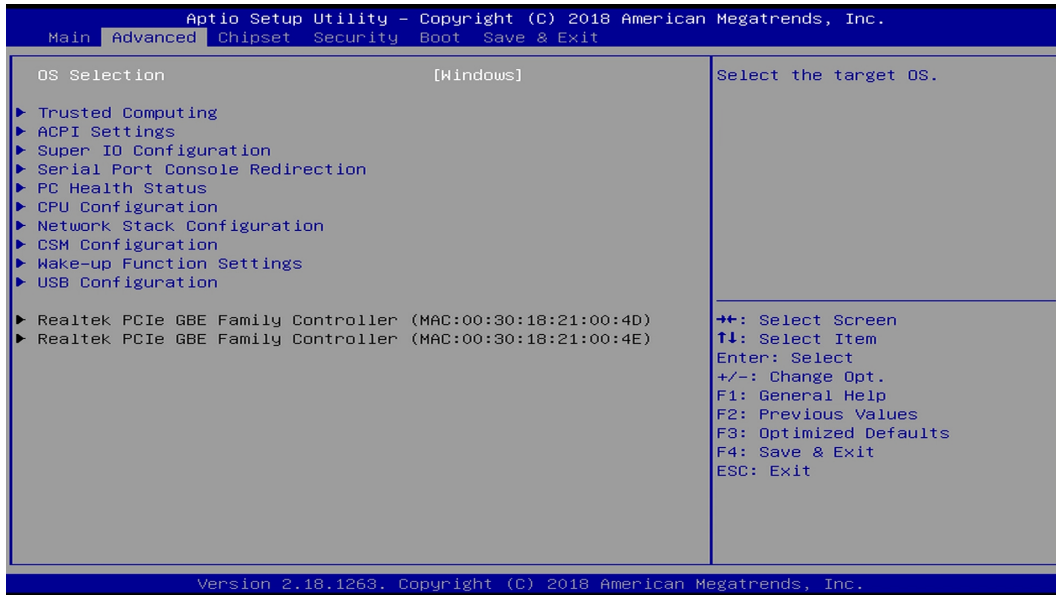
#### System Time

Set the time. Please use [Tab] to switch between time elements.

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## 3-7 Advanced Menu



### OS Selection

The optional settings: [Windows]; [Intel Linux]; [MSDOS].

**\*Note:** User need to go to this item to select the OS mode before installing corresponding OS driver, otherwise problems will occur when installing the driver.

#### 4 Trusted Computing

Press [Enter] to enable or disable 'Security Device Support'.

##### Configuration

##### **Security Device Support**

Use this item to enable or disable BIOS support for security device.

The optional settings: [Disabled]; [Enabled].

#### 4 ACPI Settings

Press [Enter] to make settings for the following sub-items:

##### ACPI Settings

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### ACPI Sleep State

Use this item to select the highest ACPI sleep state the system will enter when the suspend button is pressed.

The optional settings are: [Suspend Disabled]; [S3 (Suspend to RAM)].

#### 4 Super I/O Configuration

Press [Enter] to make settings for the following sub-items:

##### Super I/O Configuration

##### ▶ Serial Port 1 Configuration

Press [Enter] to make settings for the following items:

##### Serial Port 1 Configuration

##### Serial Port

Use this item to enable or disable serial port (COM).

The optional settings are: [Disabled]; [Enabled].

##### Change Settings

Use this item to select an optimal setting for super IO device. **Changing setting may conflict with system resources.**

The optional settings are: [Auto]; [IO=3F8h; IRQ=4]; [IO=2F8h; IRQ=3]; [IO=3E8h; IRQ=4]; [IO=2E8h; IRQ=3].

##### Transmission Mode Select (\*Optional)

The optional settings are: [RS422]; [RS232]; [RS485].

**\*Note:** *'Transmission Mode Select' is only optional for NF695HV series.*

##### Mode Speed Select (\*Optional)

The optional settings are: [RS232/RS422/RS485=250kbps]; [RS232=1Mbps, RS422/RS485=10Mbps].

**\*Note:** *'Mode Speed Select' is only optional for NF695HV series.*

##### ▶ Serial Port 2 Configuration/ Serial Port 3 Configuration / Serial Port 4 Configuration/ Serial Port 5 Configuration/ Serial Port 6 Configuration/ Serial Port 7 Configuration/ Serial Port 8 Configuration / Serial Port 9 Configuration/ Serial Port 10 Configuration (\*Optional)

Press [Enter] to make settings for the following items:

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## **Serial Port 2/3/4/5/6/7/8/9/10 Configuration**

### **Serial Port**

Use this item to enable or disable serial port (COM).

The optional settings are: [Disabled]; [Enabled].

### **Change Settings**

Use this item to select an optimal setting for Super IO Device.

The optional settings are: [Auto]; [IO=3F8h; IRQ=4]; [IO=2F8h; IRQ=3]; [IO=3E8h; IRQ=4]; [IO=2E8h; IRQ=3] for '**Serial Port 2 Configuration**'.

*\*The optional settings are: [Auto]; [IO=240h; IRQ=10]; [IO=248h; IRQ=10]; [IO=250h; IRQ=10]; [IO=258h; IRQ=10] for '**Serial Port 3 Configuration**'.*

*\*The optional settings are: [Auto]; [IO=248h; IRQ=10]; [IO=240h; IRQ=10]; [IO=250h; IRQ=10]; [IO=258h; IRQ=10] for '**Serial Port 4 Configuration**'.*

*\*The optional settings are: [Auto]; [IO=250h; IRQ=10]; [IO=240h; IRQ=10]; [IO=248h; IRQ=10]; [IO=258h; IRQ=10] for '**Serial Port 5 Configuration**'.*

*\*The optional settings are: [Auto]; [IO=258h; IRQ=10]; [IO=240h; IRQ=10]; [IO=248h; IRQ=10]; [IO=250h; IRQ=10] for '**Serial Port 6 Configuration**'.*

*\*The optional settings are: [Auto]; [IO=280h; IRQ=11]; [IO=288h; IRQ=11]; [IO=290h; IRQ=11]; [IO=298h; IRQ=11] for '**Serial Port 7 Configuration**'.*

*\*The optional settings are: [Auto]; [IO=288h; IRQ=11]; [IO=280h; IRQ=11]; [IO=290h; IRQ=11]; [IO=298h; IRQ=11] for '**Serial Port 8 Configuration**'.*

*\*The optional settings are: [Auto]; [IO=290h; IRQ=11]; [IO=280h; IRQ=11]; [IO=288h; IRQ=11]; [IO=298h; IRQ=11] for '**Serial Port 9 Configuration**'.*

*\*The optional settings are: [Auto]; [IO=298h; IRQ=11]; [IO=280h; IRQ=11]; [IO=288h; IRQ=11]; [IO=290h; IRQ=11] for '**Serial Port 10 Configuration**'.*

*\* **Note:** '**Serial Port 3~6 Configuration**' & '**Serial Port 7-10 Configuration**' are only optional for models come with **COM3-6** or **COM 7-10** serial port header block.*

### **ERP Support**

The optional settings: [Disabled]; [Enabled].

This item should be set as [**Disabled**] if you wish to have all active wake-up functions.

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### **Case Open Detect**

Use this item to detect case has already open or not, show message in POST.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], system will detect if COPEN has been short or not (refer to Page 16); if COPEN is short, system will show Case Open Message during POST.

### **WatchDog Reset Timer**

Use this item to enable or disable WDT reset function.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], the following sub-items shall appear:

#### **WatchDog Reset Timer Value**

User can select a value in the range of [10] to [255] seconds when 'WatchDog Reset Timer Unit' set as [Sec]; or in the range of [1] to [255] minutes when 'WatchDog Reset Timer Unit' set as [Min].

#### **WatchDog Reset Timer Unit**

The optional settings are: [Sec.]; [Min.].

#### **WatchDog Wake-up Timer**

This item support WDT wake-up.

The optional settings are: [Enabled]; [Disabled].

When set as [Enabled], the following sub-items shall appear:

#### **WatchDog Wake-up Timer Value**

User can select a value in the range of [10] to [4095] seconds when 'WatchDog Wake-up Timer Unit' set as [Sec]; or in the range of [1] to [4095] minutes when 'WatchDog Wake-up Timer Unit' set as [Min].

#### **WatchDog Wake-up Timer Unit**

The optional settings are: [Sec.]; [Min.].

### **ATX Power Emulate AT Power**

This item support Emulate AT power function, MB power On/Off control by power supply. Use needs to select 'AT or ATX Mode' on MB jumper at first (refer to **Page 12**, jumper ATX\_AT for ATX Mode & AT Mode Select).

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## 4 Serial Port Console Redirection

### COM1

#### Console Redirection

The optional settings: [Disabled]; [Enabled]. When set as [Enabled], the following sub-items shall appear:

#### 4 Console Redirection Settings

The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.

Press [Enter] to make settings for the following items:

##### **Terminal Type**

The optional settings: [VT100]; [VT100+]; [VT-UTF8]; [ANSI].

Emulation: [ANSI]: Extended ASCII char set; [VT100]: ASCII char set; [VT100+]: Extends VT100 to support color, function keys, etc.; [VT-UTF8]: Uses UTF8 encoding to map Unicode chars onto 1 or more bytes.

##### **Bits per second**

Use this item to select serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.

The optional settings: [9600]; [19200]; [38400]; [57600]; [115200].

##### **Data Bits**

The optional settings: [7]; [8].

##### **Parity**

A parity bit can be sent with the data bits to detect some transmission errors.

The optional settings: [None]; [Even]; [Odd]; [Mark]; [Space].

[Even]: parity bit is 0 if the num of 1's in the data bits is even; [Odd]: parity bit is 0 if num of 1's in the data bits is odd; [Mark]: parity bit is always 1; [Space]: Parity bit is always 0; [Mark] and [Space] Parity do not allow for error detection.

##### **Stop Bits**

Stop bits indicate the end of a serial data packet. (A start bit indicates the beginning). The standard setting is 1 stop bit. Communication with slow devices may require more than 1 stop bit.

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The optional settings: [1]; [2].

### **Flow Control**

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a “stop” signal can be sent to stop the data flow. Once the buffers are empty, a “start” signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

The optional settings: [None]; [Hardware RTS/CTS].

### **VT-UTF8 Combo Key Support**

Use this item to enable VT-UTF8 Combination Key Support for ANSI/VT100 terminals.

The optional settings: [Disabled]; [Enabled].

### **Recorder Mode**

With this mode enable only text will be sent. This is to capture Terminal data.

The optional settings: [Disabled]; [Enabled].

### **Resolution 100x31**

Use this item to enable or disable extended terminal resolution.

The optional settings: [Disabled]; [Enabled].

### **Legacy OS Redirection Resolution**

On Legacy OS, the Number of Rows and Columns supported redirection.

The optional settings: [80x24]; [80x25].

### **Putty KeyPad**

Use this item to select FunctionKey and KeyPad on Putty.

The optional settings: [VT100]; [Intel Linux]; [XTERMR6]; [SCO]; [ESCN]; [VT400].

### **Redirection After BIOS POST**

The optional settings are: [Always Enable]; [BootLoader].

When [Bootloader] is selected, then Legacy Console Redirection is disabled before booting to legacy OS. When [Always Enable] is selected, then Legacy Console is enabled for legacy OS. Default setting for this option is set to [Always Enable].



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**Serial Port for Out-of-Band Management/  
Windows Emergency Management Services (EMS)**

**Console Redirection**

The optional settings: [Disabled]; [Enabled]. When set as [Enabled], the following sub-items shall appear:

**4 Console Redirection Settings**

The settings specify how the host computer and the remote computer (which the user is using) will exchange data. Both computers should have the same or compatible settings.

Press [Enter] to make settings for the following items:

**Out-of-Band Mgmt Port**

The optional setting is: [COM1].

**Terminal Type**

The optional settings: [VT100]; [VT100+]; [VT-UTF8]; [ANSI].

[VT-UTF8] is the preferred terminal type for out-of-band management. The next best choice is [VT100+] and then [VT100]. See above, in Console Redirection Settings page, for more help with Terminal Type/Emulation.

**Bits per second**

Use this item to select serial port transmission speed. The speed must be matched on the other side. Long or noisy lines may require lower speeds.

The optional settings: [9600]; [19200]; [57600]; [115200].

**Flow Control**

Flow control can prevent data loss from buffer overflow. When sending data, if the receiving buffers are full, a “stop” signal can be sent to stop the data flow. Once the buffers are empty, a “start” signal can be sent to re-start the flow. Hardware flow control uses two wires to send start/stop signals.

The optional settings: [None]; [Hardware RTS/CTS]; [Software Xon/Xoff].

**Data Bits**

The default setting is: [8].

*\*This item may or may not show up, depending on different configuration.*

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### **Parity**

The default setting is: [None].

*\*This item may or may not show up, depending on different configuration.*

### **Stop Bits**

The default setting is: [1].

*\*This item may or may not show up, depending on different configuration.*

## ▶ **PC Health Status**

Press [Enter] to view current hardware health status, make further settings in 'SmartFAN Configuration'.

### ▶ **SmartFAN Configuration**

Press [Enter] to make settings for SmartFan Configuration:

#### **SmartFAN Configuration**

#### **CPUFAN Type/SYSFAN1 Type**

The optional settings are: [4-Pin]; [3-Pin].

#### **CPUFAN /SYSFAN1 Smart Mode**

The optional settings are: [Disabled]; [Enabled].

*\*When set as **[Enabled]**, user can make further settings in the following sub-items:*

#### **CPUFAN/SYSFAN1 Full-Speed Temperature**

Use this item to set CPUFAN/SYSFAN1 full speed temperature. Fan will run at full speed when above this pre-set temperature.

#### **CPUFAN/SYSFAN1 Full-Speed Duty**

Use this item to set CPUFAN/SYSFAN1 full-speed duty. Fan will run at full speed when above this pre-set duty.

#### **CPUFAN/SYSFAN1 Idle-Speed Temperature**

Use this item to set CPUFAN/SYSFAN1 idle speed temperature. Fan will run at idle speed when below this pre-set temperature.

#### **CPUFAN/SYSFAN1 Idle-Speed Duty**

Use this item to set CPUFAN/SYSFAN1 idle speed duty. Fan will run at idle speed when below this pre-set duty.

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#### 4 CPU Configuration

Press [Enter] to view current CPU configuration and make settings for the following sub-items:

##### **VT-d**

Use this item to enable or disable CPU VT-d.

The optional settings: [Enabled]; [Disabled].

##### **EIST**

Use this item to enable or disable Intel SpeedStep.

The optional settings: [Disabled]; [Enabled].

##### **C-States**

Use this item to enable or disable C-State.

The optional settings: [Disabled]; [Enabled].

##### **Enhanced C-states**

Use this item to enable or disable C1E. When enabled, CPU will switch to minimum speed when all cores enter C-state.

##### **Max Core C State Limit**

This item controls the max Core C State that cores will support.

The optional settings: [Fused Value]; [Core C10]; [Core C9] [Core C8]; [Core C 7]; [Core C 6]; [Core C1]; [Unlimited].

#### 4 Network Stack Configuration

Press [Enter] to go to '**Network Stack**' screen to make further settings.

##### **Network Stack**

The optional settings are: [Enabled]; [Disabled].

When set as [Enabled], the following sub-items shall appear:

##### **Ipv4 PXE Support**

The optional settings are: [Disabled]; [Enabled].

Use this item to enable Ipv4 PXE Boot Support. When set as [Disabled], Ipv4 boot option will not be created.

##### **Ipv6 PXE Support**

The optional settings are: [Disabled]; [Enabled].

Use this item to enable Ipv6 PXE Boot Support. When set as [Disabled], Ipv6 boot

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option will not be created.

**PXE Boot Wait Time**

Use this item to set wait time to press [ESC] key to abort the PXE boot.

**Media Detect Count**

Use this item to set number of times presence of media will be checked.

The optional settings range is from [1] to [50].

4 **CSM Configuration**

Press [Enter] to make settings for the following sub-items:

**Compatibility Support Module Configuraton**

**Boot option filter**

This item controls Legacy/UEFI ROMs priority.

The optional settings are: [UEFI and Legacy]; [Legacy only]; [UEFI only].

**Network**

This item controls the execution of UEFI and Legacy PXE OpROM.

The optional settings are: [Do not launch]; [UEFI]; [Legacy].

**Storage**

This item controls the execution of UEFI and Legacy Storage OpROM.

The optional settings are: [Do not launch]; [UEFI]; [Legacy].

**Video**

This item controls the execution of UEFI and Legacy Video OpROM.

The optional settings are: [UEFI]; [Legacy].

**Other PCI devices**

This item determines OpROM execution policy for devices other than Network, storage or video.

The optional settings are: [Do not launch]; [UEFI]; [Legacy].

4 **Wake-up Function Settings**

Press [Enter] to make settings for the following sub-items:

**Wake-up System with Fixed Time**

Use this item to enable or disable system wake on alarm event.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], system will wake on the hour/min/sec specified.

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**\*\*Note:** This function is supported when 'ERP Support' is set as [Disabled].

### **Wake-up System with Dynamic Time**

Use this item to enable or disable system wake on alarm event.

System will wake on the current time + Increase minutes.

The optional settings: [Disabled]; [Enabled].

When set as [Enabled], system will wake on the current time + increased minute(s).

### **USB/PS2 Power Gating in S3-S5**

Use this item to enable or disable USB/PS2 wake-up from S3-S5.

USB Wake-up is affected by ERP function in S4. Please disable ERP before activating this function in S4.

**\*\*Note:** This function is supported when 'ERP Support' is set as [Disabled].

## **4 USB Configuration**

Press [Enter] to make settings for the following sub-items:

### **USB Configuration**

#### **Legacy USB Support**

The optional settings are: [Enabled]; [Disabled]; [Auto].

**[Enabled]:** To enable legacy USB support.

**[Disabled]:** To keep USB devices available only for EFI specification,

**[Auto]:** To disable legacy support if no USB devices are connected.

#### **XHCI Hand-off**

This is a workaround for Oses without XHCI hand-off support. The XHCI ownership change should be claimed by XHCI driver.

The optional settings are: [Enabled]; [Disabled].

#### **USB Mass Storage Driver Support**

The optional settings are: [Disabled]; [Enabled].

### **USB Hardware Delays and Time-outs:**

#### **USB Transfer Time-out**

Use this item to set the time-out value for control, bulk, and interrupt transfers.

The optional settings are: [1 sec]; [5 sec]; [10 sec]; [20 sec].

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### **Device Reset Time-out**

Use this item to set USB mass storage device start unit command time-out.  
The optional settings are: [10 sec]; [20 sec]; [30 sec]; [40 sec].

### **Device Power-up Delay**

Use this item to set maximum time the device will take before it properly reports itself to the host controller.

The optional settings: [Auto]; [Manual].

'Auto' uses default value: for a root port it is 100 ms, for a hub port the delay is taken from hub descriptor.

Select [Manual] you can set value for the following sub-item: '**Device Power-up Delay in Seconds**'.

### **Device Power-up Delay in Seconds**

The delay range is from [1] to [40] seconds, in one second increments.

- ▶ **Realtek PCIe GBE Family Controller(MAC:XX:XX:XX:XX:XX:XX)/Realtek PCIe GBE Family Controller(MAC:XX:XX:XX:XX:XX:XX)**

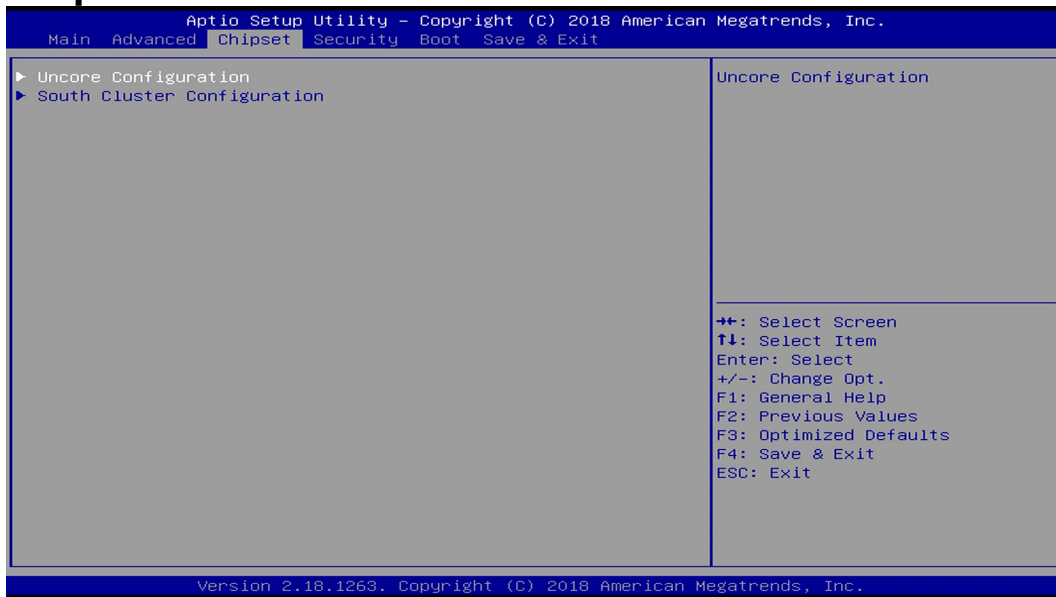
These items show current network brief information.

***\*Note:** The secondary Realtek PCIe GBE Family Controller shows up based on actual configuration of the actual product.*

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## 3-8 Chipset Menu



### ► **Uncore Configuration**

Press [Enter] to make settings for the following sub-items:

#### **GTT Size**

The optional settings are: [2MB]; [4MB]; [8MB].

#### **DVMT Pre-Allocated**

Use this item to select DVMT 5.0 pre-allocated (fixed) graphics memory size used by the internal graphics device.

The optional settings are: [64M]; [96M]; [128M]; [160M]; [192M]; [224M]; [256M]; [288M]; [320M]; [352M]; [384M]; [416M]; [448M]; [480M]; [512M].

#### **DVMT Total Gfx Mem**

Use this item to select DVMT 5.0 total graphics memory size used by the internal graphics device.

The optional settings are: [128M]; [256M]; [MAX].

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### **Active LFP**

The optional settings are: [Disabled]; [LVDS]; [eDP].

*\* When set as [LVDS], user can make further setting in the following subitems:*

### **LCD Panel Type**

Use this item to select LCD panel used by Internal Graphics Device by selecting the appropriate setup item.

The optional settings are: [800x480 1-ch 18-bit]; [800x600 1-ch 18-bit]; [800x600 1-ch 24-bit]; [1024x600 1-ch 18-bit]; [1024x768 1-ch 18-bit]; [1024x768 1-ch 24-bit]; [1280x768 1-ch 24-bit]; [1280x800 1-ch 18-bit]; [1280x800 1-ch 24-bit]; [1366x768 1-ch 18-bit]; [1366x768 1-ch 24-bit]; [1440x900 2-ch 18-bit]; [1440x900 2-ch 24-bit]; [1280x1024 2-ch 24-bit]; [1680x1050 2-ch 24-bit]; [1920x1080 2-ch 24-bit].

### **LVDS FW Protect**

The optional settings are: [Enabled]; [Disabled].

Use this item to select LCD panel used by Internal Graphics Device by selecting the appropriate setup item.

*\* When set as [LVDS] or [eDP], user can make further setting in the following subitems:*

### **GMCH BLC Control**

Use this item to select back light control settings.

The optional settings are: [PWM-Inverted]; [PWM-Normal].

### **Primary IGFX Boot Display**

Use this item to select the video device which will be activated during POST. This has no effect if external graphics present. Secondary boot display selection will appear based on your selection. VGA modes will be supported only on primary display.

*The optional settings are: [Auto]; [LFP]; [CRT]; [HDMI] for **NF695HV** series.*

*The optional settings are: [Auto]; [LFP]; [HDMI1]; [HDMI2] for **NF695L** series.*

### **Secondary IGFX Boot Display**

Use this item to select Secondary Display Device.

The optional settings are: [Disabled]; [CRT]; [HDMI] for **NF695HV** series.

The optional settings are: [Disabled]; [HDMI1]; [HDMI2] for **NF695L** series.



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## **Memory Information**

The working memory information will be on display.

### ▶ **South Cluster Configuration**

#### **4 PCI Express Configuration**

Press [Enter] to make settings for the following sub-items:

##### ***PCI Express Configuration***

##### **Peer Memory Write Enable**

The optional settings: [Disabled]; [Enabled].

##### **Compliance Mode**

The optional settings: [Disabled]; [Enabled].

##### **Onboard PCIE LAN1/ \*Onboard PCIE LAN2(Optional)**

The optional settings: [Disabled]; [Enabled].

**\*Note:** *'Onboard PCIE LAN2' shows up based on actual configuration of the actual product.*

#### **4 SATA Configuration**

Press [Enter] to make settings for the following sub-items:

##### **SATA Controller(s)**

The optional settings are: [Enabled]; [Disabled].

When set as [Enabled], the following items shall appear:

##### **SATA Mode Selection**

The default setting is: [AHCI].

Use this item to enable or disable each SATA port.

##### **SATA Port**

##### **SATA Port**

The optional settings: [Disabled]; [Enabled].

##### **M.2**

##### **M.2**

Use this item to enable or disable SATA port.

The optional settings: [Disabled]; [Enabled].

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## HD-Audio Support

Use this item to enable or disable HD-Audio Support.

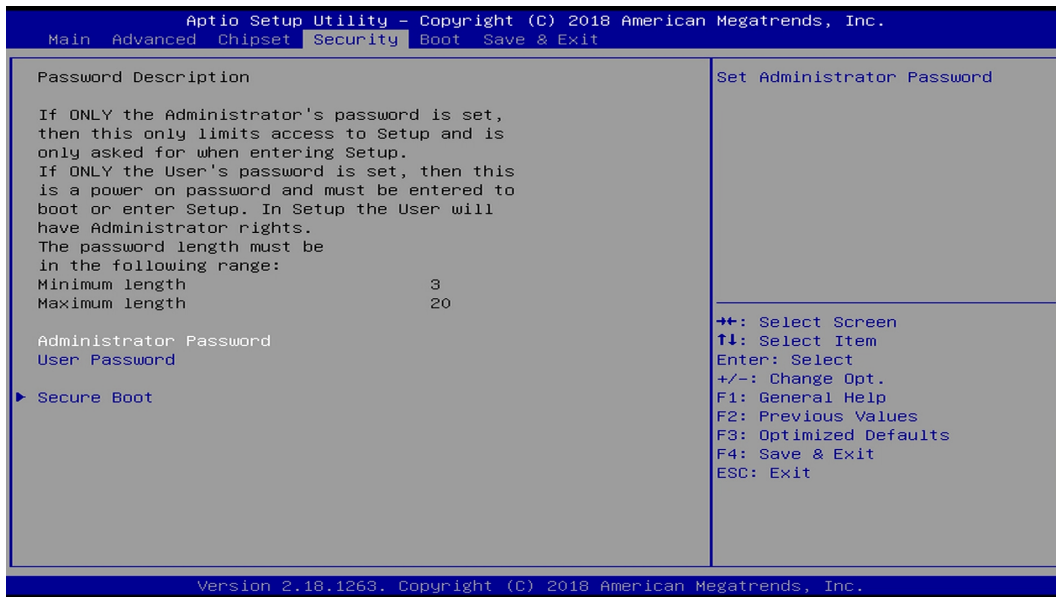
The optional settings are: [Disabled]; [Enabled].

## System State after Power Failure

Use this item to specify what state to go to when power re-applied after a power failure (G3 state).

The optional settings are: [Always On]; [Always Off]; [Former State].

## 3-9 Security Menu



Security menu allow users to change administrator password and user password settings.

### Administrator Password

If there is no password present on system, please press [Enter] to create new administrator password. If password is present on system, please press [Enter] to verify old password then to clear/change password. Press again to confirm the new

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administrator password.

### **User Password**

If there is no password present on system, please press [Enter] to create new administrator password. If password is present on system, please press [Enter] to verify old password then to clear/change password. Press again to confirm the new administrator password.

### **4 Secure Boot**

Press [Enter] to make customized secure settings:

#### **Secure Boot Control**

The optional settings are: [Disabled]; [Enabled].

Secure Boot can be enabled if 1. System running in user mode with enrolled Platform Key (PK); 2. CSM function is disabled.

#### **Secure Boot Mode**

The optional settings are: [Standard]; [Custom].

Set UEFI Secure Boot Mode to Standard mode or Custom mode. This change is effective after save. After reset, this mode will return to Standard mode.

*\*When set as [Custom], user can make further settings in 'Key Management'.*

### **4 Key Management**

This item enables experienced users to modify Secure Boot variables, which includes the following items:

#### **Provision Factory Default Keys**

This item is for user to install factory default secure boot keys when system is in Setup Mode.

The optional settings are: [Disabled]; [Enabled].

#### **4 Enroll All Factory Default Keys**

This item forces system to User Mode-install all Factory Default keys.

*\*This item shows up when 'Provision Factory Default Keys' is set as [Enabled].*

#### **4 Delete All Factory Default Keys**

This item forces system to Setup Mode-clear all Secure Boot Variables.

*\*This item shows up when 'Provision Factory Default Keys' is set as*

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[Disabled].

#### **4 Save all Secure Boot Variables**

This item will save NVRAM content of all Secure Boot variables to the files (WFI\_SIGNATURE\_LIST data format) in root folder on a target file system device.

#### **4 Platform Key (PK)/Key Exchange Keys/Authorized Signature/Forbidden Signature/ Authorized TimeStamps/OS Recovery Signatures**

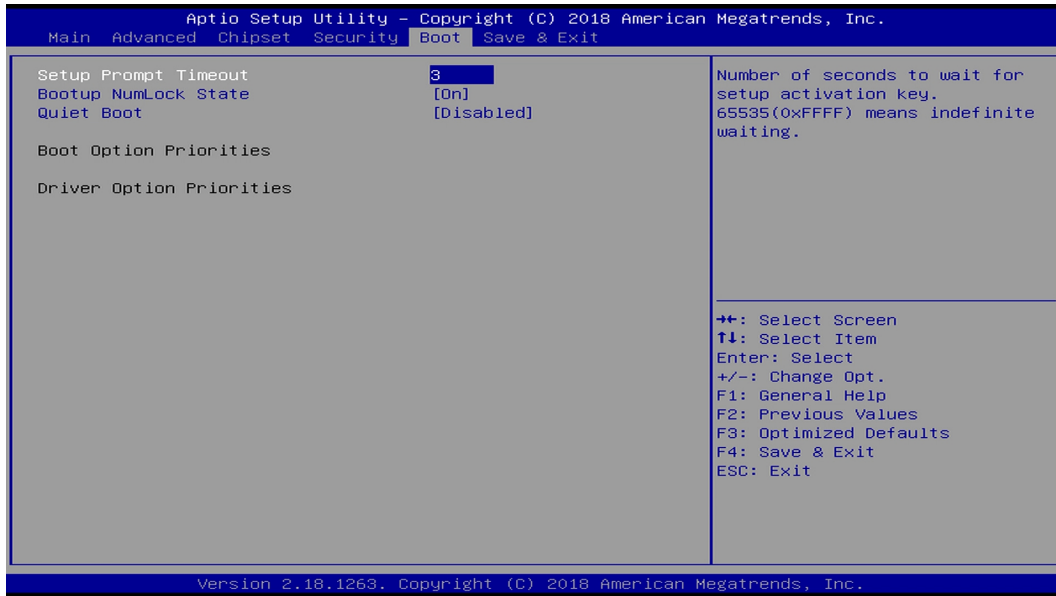
Use this item to enroll Factory Defaults or load the keys from a file with:

1. Public Key Certificate in:
  - a) EFI\_SIGNATURE\_LIST
  - b) EFI\_CERT\_X509 (DER encoded)
  - c) EFI\_CERT\_RSA2048 (bin)
  - d) EFI\_CERT\_SHA256 (bin)
2. Authenticated UEFI Variable

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## 3-10 Boot Menu



### **Setup Prompt Timeout**

Use this item to set number of seconds to wait for setup activation key.

### **Bootup Numlock State**

Use this item to select keyboard numlock state.

The optional settings are: [On]; [Off].

### **Quiet Boot**

The optional settings are: [Disabled]; [Enabled].

### **Boot Option Priorities**

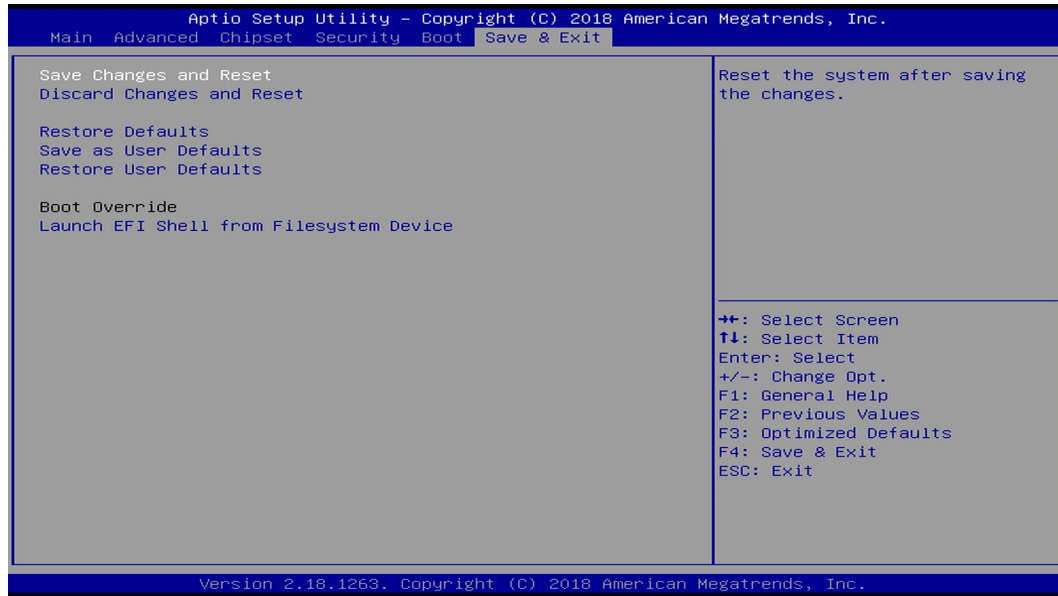
#### **Boot Option #1/ Boot Option #2...**

Use this item to decide system boot order from available options.

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## 3-11 Save & Exit Menu



### **Save Changes and Reset**

This item allows user to reset the system after saving the changes.

### **Discard Changes and Reset**

This item allows user to reset the system without saving any changes.

### **Restore Defaults**

Use this item to restore /load default values for all the setup options.

### **Save as User Defaults**

Use this item to save the changes done so far as user defaults.

### **Restore User Defaults**

Use this item to restore defaults to all the setup options.

### **Boot Override**

The available options here are dynamically updated and make system boot to any

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boot option selected.

**Launch EFI Shell from filesystem device**

Use this item to launch EFI shell application (shell.efi) from one of the available filesystem device.