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CUSTOMER . PTC

SAMPLE CODE . NSC1602LRS-JWA-H

MASS PRODUCTION CODE . NPC1602LRS-JWA-H

SAMPLE VERSION . 01

SPECIFICATIONS EDITION . 004

DRAWING NO. (Ver.) JLMD-NPC1602LRS-JWA-H_003

PACKAGING NO. (Ver.) : JPKG-NPC1602LRS-JWA-H_001

Customer Approved

Date:

POWERTIP 2015.05.26

JS RD APPROVED

Approved	Checked	Designer
閆偉	劉進	周志仙

Preliminary specification for design input

Specification for sample approval

POWERTIP TECH. CORP.

Headquarters: No.8, 6th Road, Taichung Industrial Park,

Taichung, Taiwan

台中市 407 工業區六路 8號

TEL: 886-4-2355-8168

FAX: 886-4-2355-8166

E-mail: sales@powertip.com.tw

Http://www.powertip.com.tw



History of Version

Date (mm / dd / yyyy)	Ver.	Edi.	Description	Page	Design by
08/09/2013	01	001	New Drawing	-	周志仙
11/28/2013	01	002	New Sample	-	周志仙
06/24/2014	01	003	Update Sample Specification	-	周志仙
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Total: 30 Pages



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Note: For detailed information please refer to IC data sheet: SITRONIX---ST7066U-0A



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	16*2 Characters
LCD Type	STN Gray , Positive , Transflective
Driver Condition	LCD Module: 1/16 Duty , 1/5 Bias
Viewing Direction	6 O'clock
Weight	32.2g
Interface	6800-series 8-bit parallel
Driver IC	ST7066U
	THIS PRODUCT CONFORMS THE ROHS OF PTC
ROHS	Detail information please refer website:
	http://www.powertip.com.tw/news.php?area_id_view=1085560481/

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	85.0 (L) * 30.0 (W) *12.7(H)	mm
Viewing Area	66.0 (L) * 16.0 (W)	mm
Active Area	56.2 (L) * 11.5 (W)	mm
Character Size	2.95mm * 5.55mm	mm
Character Pitch	3.55mm * 5.95mm	mm

Note: For detailed information please refer to LCM drawing





1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	-	-0.3	7.0	V
LCD Driver Supply Voltage	VLCD	-	VDD -10.0	VDD +0.3	V
Input Voltage	Vin	-	-0.3	V _{DD} +0.3	V
Operating Temperature	Тор	-	-20	70	$^{\circ}\mathbb{C}$
Storage Temperature	T _{ST}	-	-30	80	$^{\circ}\mathbb{C}$
Storage Humidity	H _D	Ta<60 ℃	-	90	%RH

1.4 DC Electrical Characteristics

Ta = 25°℃

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V_{DD}		4.5	5.0	5.5	V
"H" Input Voltage	ViH	-	0.7 Vdd	-	Vdd	٧
"L" Input Voltage	VIL		-0.3	-	0.6	٧
"H" Output Voltage	Vон	IOH=-0.1mA	3.9	-	Vdd	٧
"L" Output Voltage	Vol	IOL=0.1mA	-	-	0.4	٧
Supply Current	I _{DD}	V _{DD} = 5.0 V ,Vop= 4.5V *1	-	2.0	3.0	mA
	Vop	-20 ℃	4.4	4.6	4.8	
LCM Driver Voltage		25 ℃	4.3	4.5	4.7	V
	*2	70 ℃	3.8	4.0	4.2	

NOTE: *1 The Maximum current display

*2 The VOP test point is (VDD –V0)



1.5 Optical Characteristics

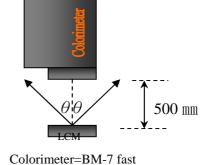
LCD Panel : 1/16 Duty , 1/5 Bias , $V_{LCD} = 4.5V$, Ta =25°C

Item		Symbol	Conditions	Min.	Тур.	Max.	Unit	Reference
Response Time	Rise	tr		1	80	120	ms	Note 2
Response fille	Fall	tf	-	ı	150	225		Note 2
	Rear	θ+	C <u>></u> 2.0	ı	40	-		
Viewing angle	Front	θ-		1	40	-	Deg	Note 1
range	Left	θL		-	45	-		
	Right	θR		1	45	-		
Contrast Ra	tio	С	-	-	5	-	-	Note 3
Average Bright (with LCD)		IV		30	35	-	cd/m ²	
Wavelength (with LCD)		λр	IF=100 mA	568	571	574	nm	Note 4
Uniformity *	1	∆B		70	-	_	%	

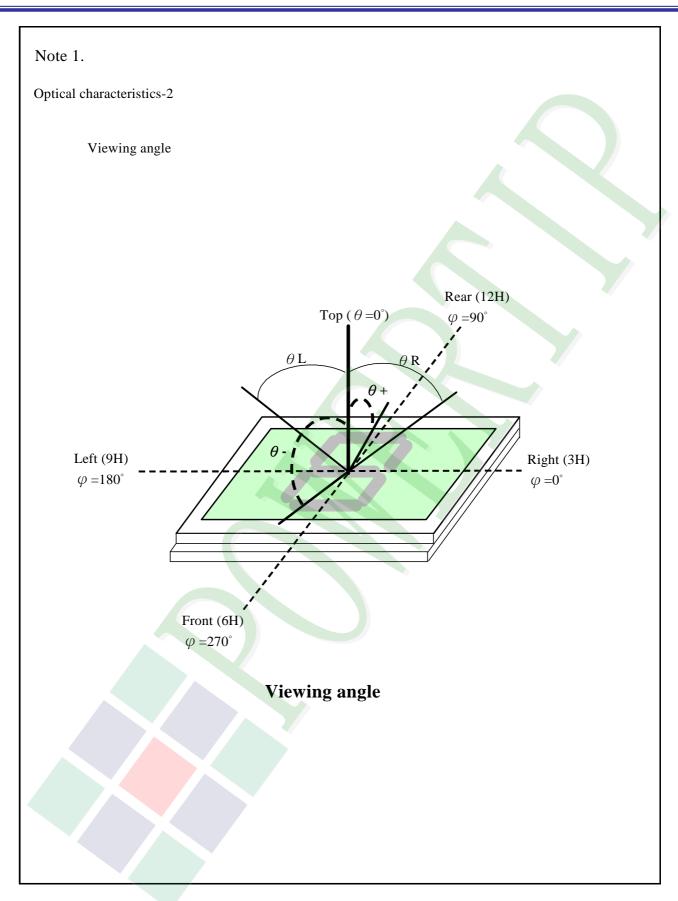
Note 4

- 1 : △B=B(min) / B(max) * 100%
- 2: Measurement Condition for Optical Characteristics:
 - a : Environment: 25°C±5°C / 60±20%R.H , no wind , dark room below 10 Lux at typical lamp current and typical operating frequency.
 - b : Measurement Distance: 500 \pm 50 mm \rightarrow (θ = 0°)
 - c: Equipment: TOPCON BM-7 fast, (field 1°), after 10 minutes operation.
 - d: The uncertainty of the C.I.E coordinate measurement ±0.01, Average Brightness ± 4%

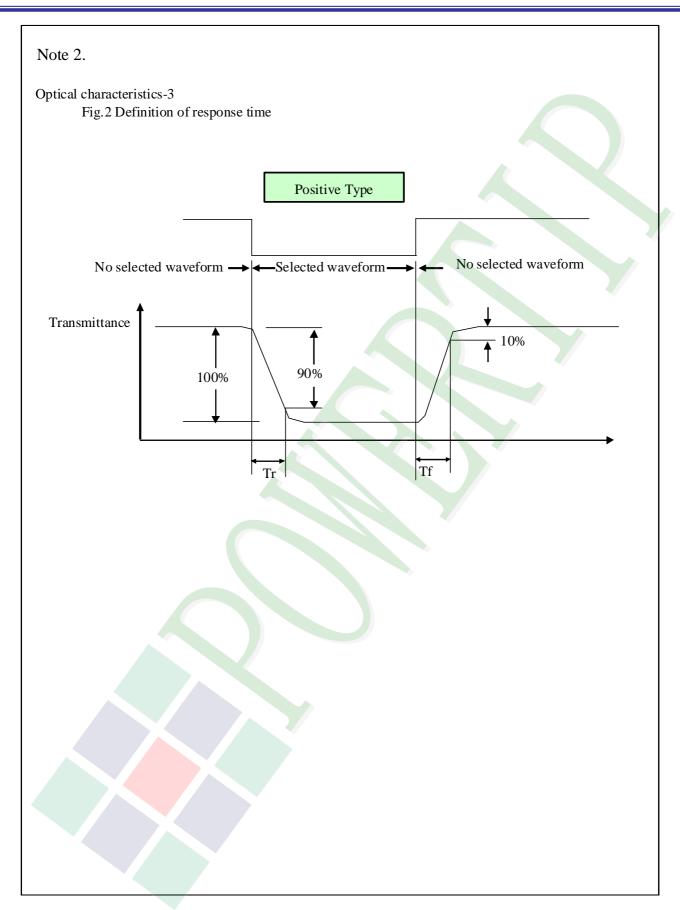














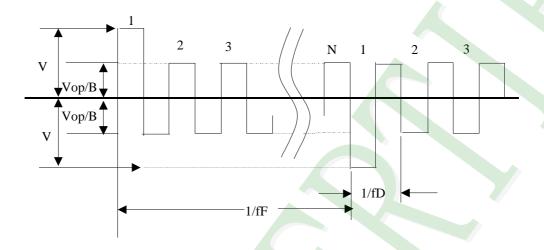
Electrical characteristics-2

※2 Drive waveform

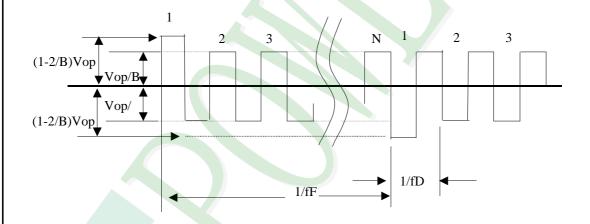
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



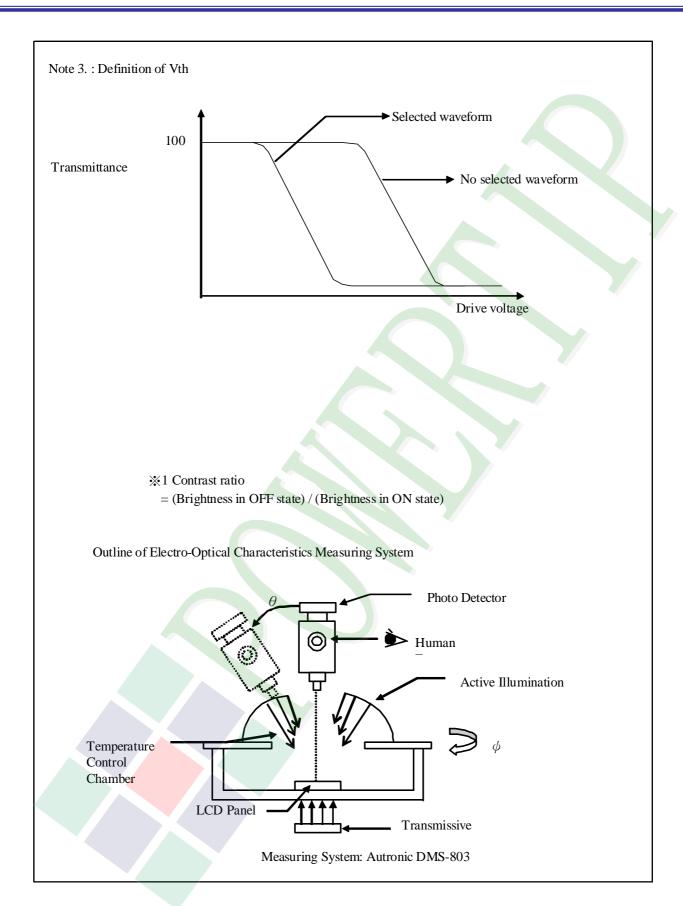
(2) Non-Selected wave form



Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2 = 1 period







1.6 Backlight Characteristics

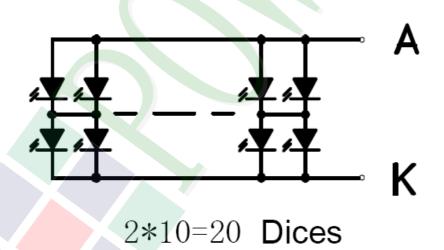
Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°ℂ	-	150	mA
Reverse Voltage	VR	Ta =25°ℂ	-	8	V
Power Dissipation	PD	Ta =25°ℂ	-	660	mW

Electrical / Optical Characteristics

Item	Symbol	Conditions	Min.	Тур.	Max.	Unit	
Forward Voltage	VF		4.0	4.2	4.4	V	
Average Brightness (without LCD)	IV	IF= 100 mA	165	190	\\\\-\\\\-\\\\\-\\\\\\\\\\\\\\\\\\\\\\	cd/m ²	
Wavelength (Without LCD)	λр		569	572	575	nm	
Color	Yellow/Green						

Internal Circuit Diagram:





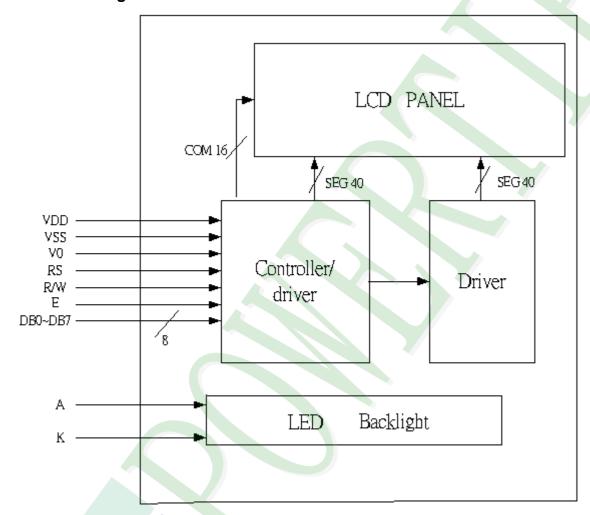
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram



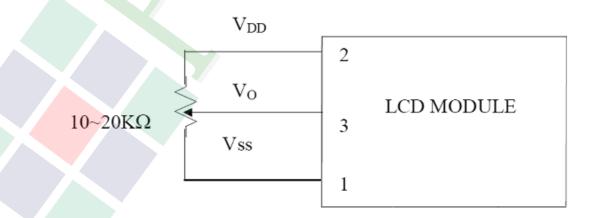


2.2 Interface Pin Description

Pin No.	Symbol	Signal Description
1	Vss	Power Supply (Vss=0)
2	V_{DD}	Power Supply (5V)
3	Vo	Operating voltage for LCD
4	RS	Register Selection input High = Data register Low = Instruction register (for write) Busy flag address counter (for read)
5	R/W	Read/Write signal input is used to select the read/write mode High = Read mode, Low = Write mode
6	E	Start enable signal to read or write the data
7	DB0	Four low and which is directional three atots data has lines. Here for
8	DB1	Four low order bi-directional three-state data bus lines. Use for data transfer between the MPU and the LCD module.
9	DB2	These four are not used during 4-bit operation.
10	DB3	These four are not used during 4-bit operation.
11	DB4	
12	DB5	Four high order bi-directional three-state data bus lines. Used for data transfer between the MPU and the LCD module.
13	DB6	DB7 can be used as a busy flag.
14	DB7	DDI Gail DC asca as a basy liag.

2.2.1 Application Notes

Contrast Adjust





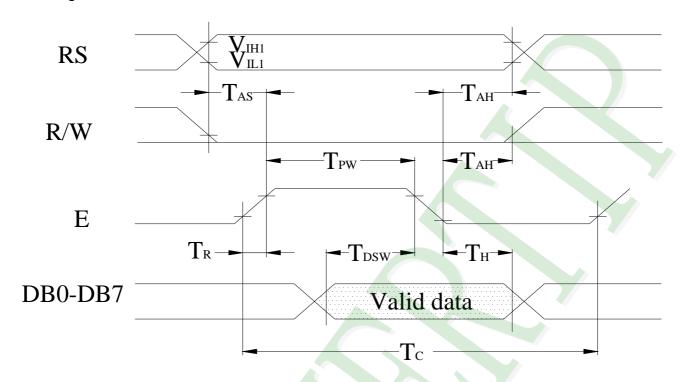
2.2.2 Refer Initial code

```
void initial()
{
    delay(40);
    write_com(0x01);
    delay(5);
    write_com(0x38);
    delay(5);
    write_com(0x0c);
    delay(5);
    write_com(0x06);
    delay(5);
}
```

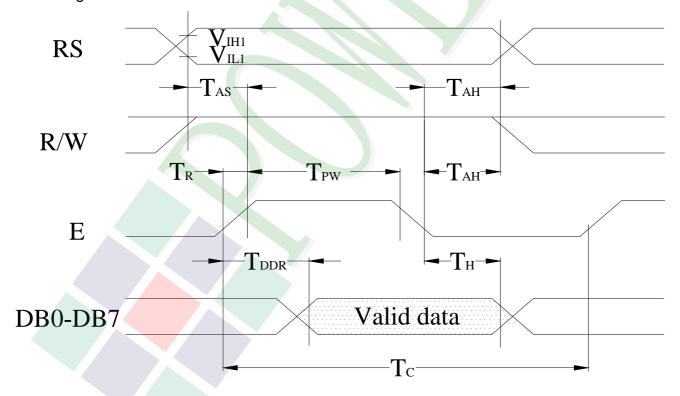


2.3 Timing Characteristics

· Writing data from MPU to ST7066U



· Reading data from ST7066U to MPU





· Write Mode (Writing data from MPU to ST7066U)

 $(VDD = 5V,Ta=25^{\circ}C)$

Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	ı		ns
T _{PW}	Enable Pulse Width	Pin E	140	-		ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
Tas	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
Тан	Address Hold Time	Pins :RS,RW,E	10	-	1	ns
T _{DSW}	Data Setup Time	Pins:DB0~DB7	40		-	ns
Тн	Data Hold Time	Pins:DB0~DB7	10	-	1	ns

- Read Mode (Reading data from ST7066U to MPU)

 $(VDD = 5V,Ta=25^{\circ}C)$

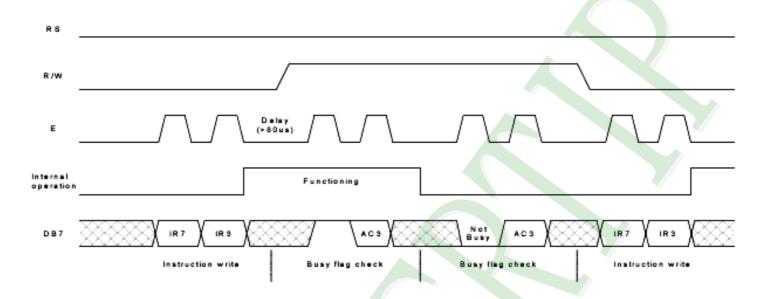
Symbol	Characteristics	Test Condition	Min.	Тур.	Max.	Unit
Tc	Enable Cycle Time	Pin E	1200	1	-	ns
T _{PW}	Enable Pulse Width	Pin E	140	-	-	ns
T _R , T _F	Enable Rise / Fall Time	Pin E	-	-	25	ns
Tas	Address Setup Time	Pins: RS , RW,E	0	-	-	ns
Тан	Address Hold Time	Pins :RS,RW,E	10	-	-	ns
T_DDR	Data Setup Time	Pins:DB0~DB7		-	100	ns
Тн	Data Hold Time	Pins:DB0~DB7	10	-	-	ns





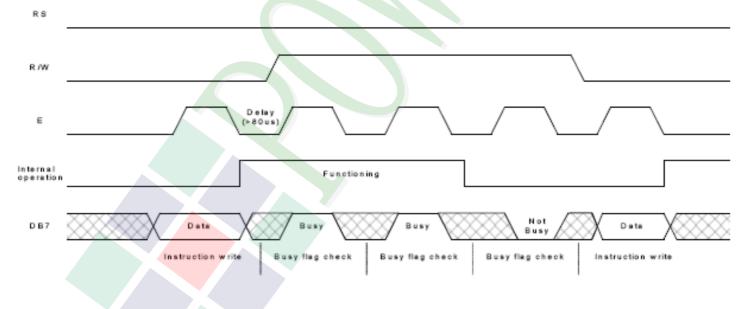
For 4-bit interface date, only four bus lines (DB4 to DB7) are used for transfer

Example of busy flag check timing sequence



For 8-bit interface date, all eight bus lines (DB0 to DB7) are used

Example of busy flag check timing sequence





2.4 Display Command

				l	Instru	ction	Code	Э				Description
Instructions		R/	DB	DB	DB	DB	DB	DB	DB	DB	Description	Time
	RS	W	7	6	5	4	3	2	1	0		(270KHz)
			-			•		_	-		Write "20H" to DDRAM, and set	
Clear	0	0	0	0	0	0	0	0	0	1	DDRAM address to "00H" from	1.52ms
Display										-	AC.	
											Set DDRAM address to "00H"	
											from AC and return cursor to it's	
Return	0	0	0	0	0	0	0	0	1	×	original position if shifted.	1.52ms
Home		J									The contents of DDRAM	1.021110
											are not changed.	
											Sets cursor move direction and	
Entry Mode											specifies display shift. These	
Set	0	0	0	0	0	0	0	1	I/D	S	operations are performed	37 μ s
Set												
Dioploy								4			during data write and read .	
Display ON/OFF	0	0	0	0	0	0	1		C	В	D=1 : entire display on C=1 : cursor on	270
ON/OFF	U	U	U	U	U	U	1	D	С	Б		37 μ s
											B=1 : cursor position on	
Cursor or											Set cursor moving and display	
Display	0	0	0	0	0	1	S/C	R/L	×	×	shift control bit, and the	37 µs
Shift											the direction, without changing	
											of DDRAM data.	
Function						5 .					DL: interface data is 8/4 bits	07
Set	0	0	0	0	1	DL	N	F	×	×	NL: number of line is 2/1	37 µs
											F: font size is 5×11/5×8	
Set					AC	AC	AC	AC	AC	AC	Set CGRAM address	
CGRAM	0	0	0	1	5	4	3	2	1	0	in address counter.	37 µs
Address								_	•		in dudi ees eedinen	
Set				AC	AC	AC	AC	AC	AC	AC	Set DDRAM address	
DDRAM	0	0	1	6	5	4	3	2	1	0	in address counter.	37 μ s
Address								_		U	in address counter.	
											Whether during internal	
Read Busy			В	AC	AC	AC	AC	AC	AC	AC	operation or not can be	
Flag and	0	1	F	6	5	4	3	2	1	0	known by reading BF.	0 µs
Address			1	U	3	7	3	_	'	U	The contents of address	
											counter can also be read.	



Write Data to RAM	1	0	D 7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	37µs
Read Data from RAM	1	1	D 7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	37µs

Note:

Be sure the ST7066U is not in the busy state (BF=0) before sending an instruction from the MPU to the ST7066.

If an instruction is sent without checking the busy flag, the time between the first instruction and next instruction will take much longer than the instruction time itself.

Before checking BF, be sure to wait at least 80us.. Do not keep "E" always "High" for checking BF Refer to Instruction Table for the list of each instruction execution time.



2.5 Character Pattern

NO.7066-0A

<u> 10.7</u>																
67-64 63-60	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	11 11
0000	용종															
0001	(2)															
0010	(3)															
0011	4)															
0 100	(5)															
0101	(6)															
0 110	(7)															
0 11 1	(8)															
1000	(1)															
1001	(2)															
1010	(3)															
1011	(4)															
1100	(5)															
1101	(6)															
1110	(7)															
1111	(8)															

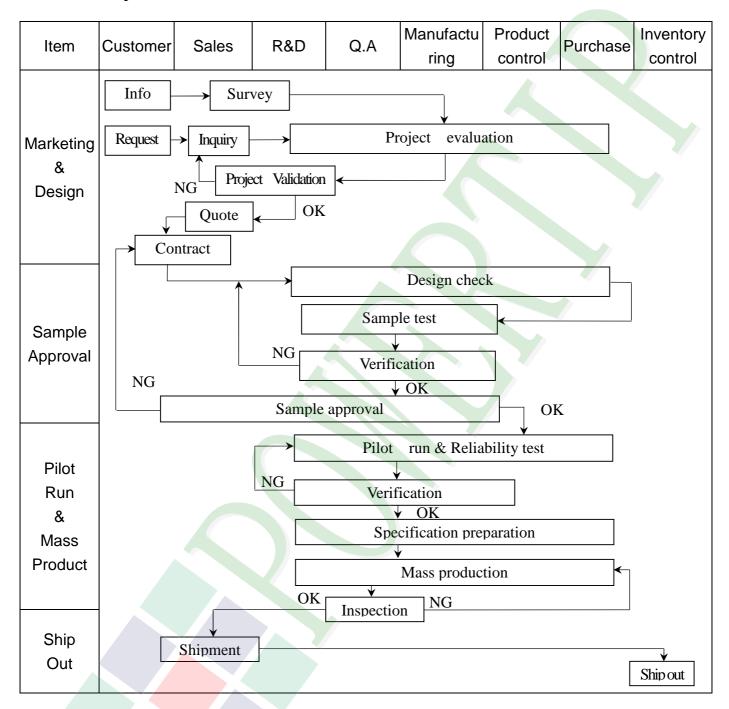
2.6 JUMPER(Setting different use)

J4

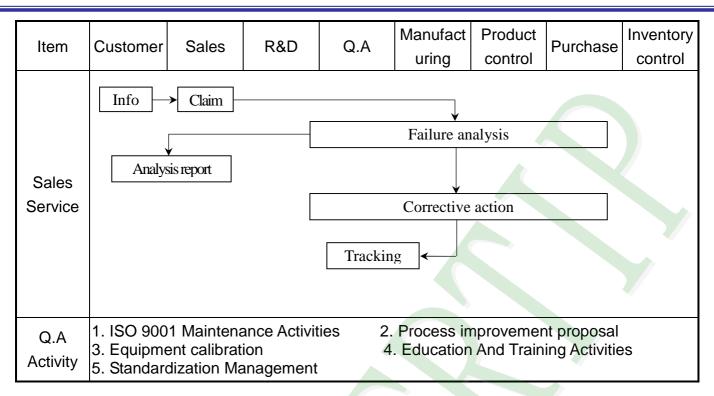


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

- ◆Scope: The document shall be applied to LCD Module for Monotype and Color STN(Ver. B01).
- ◆Inspection Standard: MIL-STD-105E Table Normal Inspection Single Sampling Level Ⅱ.
- ◆Equipment : Gauge · MIL-STD · Powertip Tester · Sample
- ◆Defect Level: Major Defect AQL: 0.4; Minor Defect: AQL: 1.5.
- **♦**OUT Going Defect Level : Sampling .
- **♦**Manner of appearance test :
 - (1). The test be under 20W×2 fluorescent light 'and distance of view must be at 30 cm.
 - (2). Standard of inspection: (Unit: mm)
 - (3). The test direction is base on about around 45° of vertical line. (Fig. 1)
 - (4). Definition of area . (Fig. 2)

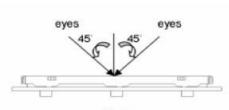


Fig.1

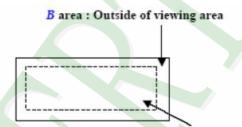


Fig. 2 A area: viewing area

♦ Specification:

NO	Item	Criterion	Level
		1. 1 The part number is inconsistent with work order of Production.	Major
01	Product condition	1. 2 Mixed production types.	Major
		1. 3 Assembled in inverse direction.	Major
02	Quantity	2. 1 The quantity is inconsistent with work order of production.	Major
03	Outline dimension	3. 1 Product dimension and structure must conform to Structure diagram.	Major
		4. 1 Missing line character and icon.	Major
		4, 2 No function or no display.	Major
04	Electrical Testing	4, 3 Output data is error.	Major
		4, 4 LCD viewing angle defect.	Major
		4. 5 Current consumption exceeds product specifications.	Major



♦Specification For Monotype and Color STN:

NO	Item	c	riteri	on			Level		
	Black or white dot \ scratch \ contamination	 5. 1 Round type: 5. 1. 1 display only: • White and black spots on display ≤ 0, 30 mm, no more than 4 white or black spots present. • Densely spaced: NO more than two spots or lines within 3 mm. 							
		5. 1. 2 Non-display :							
	Round type	Dimension (diameter : Φ)	Acceptance (Q'ty)						
		$\Phi \leq 0.10$	A area B area Accept no dense						
	→ <u>x</u>	$0.10 < \Phi \le 0.20$	3						
05	—			2		Ignore	Minor		
	-	$0.20 < \Phi \leq 0.30$							
	$\Phi = (x+y)/2$	Total quantity		4					
		5. 1. 3 Line type:							
	Linetone	Dimension	Acceptance (Q'ty)						
	Line type	Length (L) Width (W)		A area		B area			
	√ w	W ≦ (nse				
	→ L +	$L \le 3.0$ $0.03 < W \le 0$	0. 05 Ignore						
	2	$L \le 2.5$ $0.05 < W \le 0.$							
		W >0	. 075	As	roun	d type			
		Dimension (diameter : Φ)		Acceptan	ce (Q				
		$\Phi \leq 0.20$		A area	+	B area			
	D. 1		A	3	\dashv				
06	Polarizer Bubble	$0.20 < \Phi \le 0.50$			\dashv	-	Minor		
	Dubble	$0.50 < \Phi \le 1.00$	2		Ignore				
		$\Phi > 1.00$	0						
		Total quantity		4					



♦Specification For Monotype and Color STN:

NO	Item	Criterion		Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass a: I	The width of crack. terminal length LCD side length	
		7. 1 General glass chip: 7. 1. 1 Chip on panel surface and crack	between panels:	
		Z Z	Y	
07	The crack of glass	SP Y	SP [NG]	Minor
		[OK]	Y	
		z.		
		X Y	z	
		≦ a Crack can't enter viewing area	≤1/2 t	
		≤ a Crack can't exceed the half of SP width.	1/2 t < Z ≤2 t	



◆Specification For Monotype and Color STN:

NO	Item	Criterion	Level				
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass 7. 1. 2 Corner crack:					
		X Y Z					
		≤1/5 a Crack can't enter viewing area Z ≤ 1/2 t					
07	The crack of	$\leq 1/5$ a Crack can't exceed the half of SP width. 1/2 t < Z ≤ 2 t	Minor				
01	glass	7.2 Protrusion over terminal:	Minor				
		7.2.1 Chip on electrode pad:					
		X X Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z Z					
		W X					
		X Y Z					
		Front \leq a \leq 1/2 W \leq t					
		Back Neglect					



◆Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		Symbols: X: The length of crack Z: The thickness of crack t: The thickness of glass Y: The width of crack W: terminal length a: LCD side length	
		7. 2. 2 Non-conductive portion:	
07	The crack of glass	$\begin{array}{c cccc} X & Y & Z \\ & \leq 1/3 \text{ a} & \leq W & \leq t \end{array}$	Minor
		⊙ If the chipped area touches the ITO terminal, over 2/3 of	
		the ITO must remain and be inspected according to electrode	
		terminal specifications. 7. 2. 3 Glass remain :	
		$\begin{array}{c cccc} X & Y & Z \\ & \leq a & \leq 1/3 \text{ W} & \leq t \end{array}$	



♦Specification For Monotype and Color STN:

NO	Item	Criterion	Level
		8. 1 Backlight can't work normally.	Major
08	Backlight elements	8. 2 Backlight doesn't light or color is wrong.	Major
		8. 3 Illumination source flickers when lit.	Major
		9. 1 Pin type must match type in specification sheet.	Major
		9. 2 No short circuits in components on PCB or FPC.	Major
09	General appearance	9. 3 Product packaging must the same as specified on packaging specification sheet.	Minor
		9. 4 The folding and peeled off in polarizer are not acceptable.	Minor
		9. 5 The PCB or FPC between B/L assembled distance (PCB or FPC) is ≤1.5 mm.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO.	TEST ITEM	TEST CO	ONDITION				
1	High Temperature Storage Test	Keep in +80 ±2°C 96 hrs Surrounding temperature, then sto	orage at normal condition 4hrs.				
2	Low Temperature Storage Test	Keep in -30 ±2°C 96 hrs Surrounding temperature, then storage at normal condition 4hrs.					
3	High Temperature / High Humidity Storage Test	Keep in +60 °C / 90% R.H duration for 96 hrs Surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer)					
4	Temperature Cycling Storage Test	$-30^{\circ} \bigcirc \rightarrow +25^{\circ} \bigcirc \rightarrow +80^{\circ} \bigcirc \rightarrow +25^{\circ} \bigcirc$ (30mins) (5mins) (30mins) (5mins)					
5	ESD Test	Air Discharge: Apply 2 KV with 5 times Discharge for each polarity +/- 1. Temperature ambiance: 15°C ~35°C 2. Humidity relative: 30% ~60% 3. Energy Storage Capacitance(Cs+Cd): 150pF±10% 4. Discharge Resistance(Rd): 330 Ω±10% 5. Discharge, mode of operation: Single Discharge (time between successive discharges at least 1 sec) (Tolerance if the output voltage indication: ±5%)					
6	Vibration Test (Packaged)	 Sine wave 10~55 Hz frequency (1 min/sweep) The amplitude of vibration :1.5 mm Each direction (X \ Y \ Z) duration for 2 Hrs 					
7	Drop Test (Packaged)	Packing Weight (Kg	122 76 61 46				



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module, be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.
- 5.2.8 To control temperature and time of soldering is 320±10°C and 3-5 sec.
- 5.2.9 To avoid liquid (include organic solvent) stained on LCM.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25°C ±5°C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

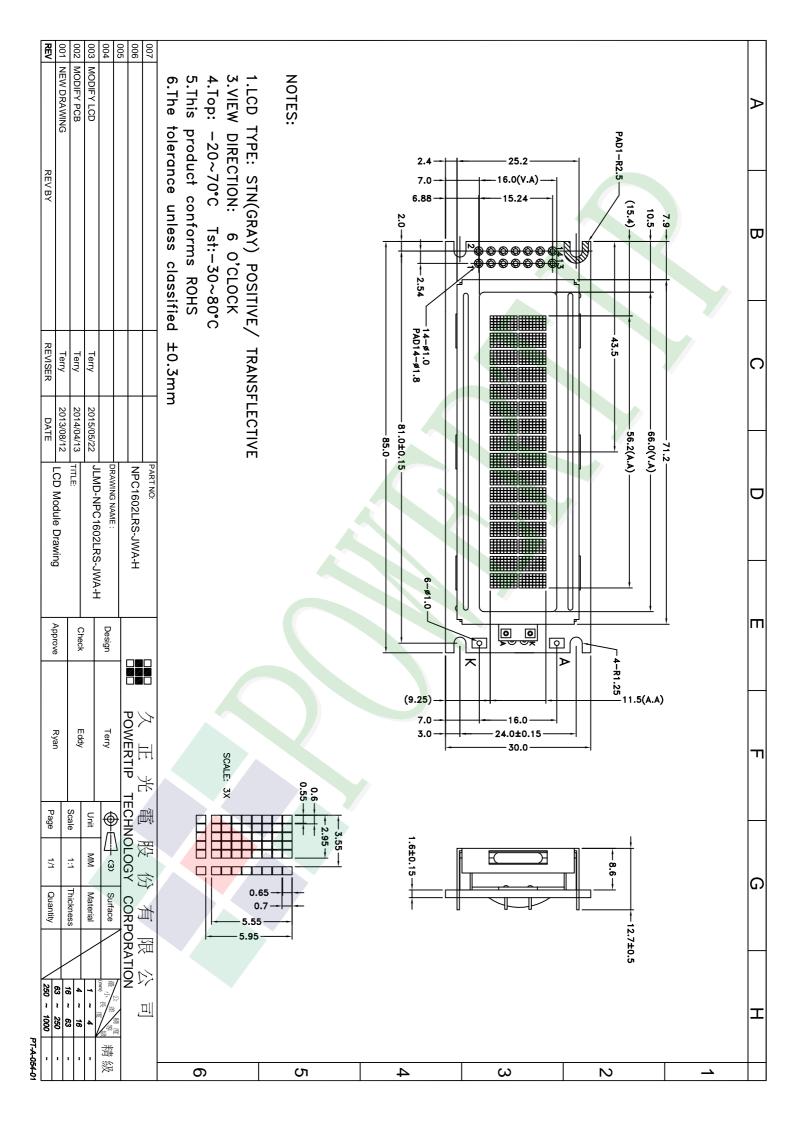
5.4 TERMS OF WARRANTY

5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.



Approve Check Contact Ver.001 LCM包裝規格書 Documents NO. JPKG-NPC1602LRS-JWA-H LCM Packaging Specifications Ryan Eddy Terry 1.包裝材料規格表 (Packaging Material): (per carton) Dimensions (mm) No. Item Model 1Pcs Weight Total Weight Quantity 1 成品 (LCM) NPC1602LRS-JWA-H 85.0 X 30.0 X 12.7 0.0322 468 15.0696 2 靜電袋(1)Antistatic Bag BAG100100ARABA 100 X 100 0.0011 468 0.5148 3 A1-1隔板(3)A1-1 Partition 295 X 47 X 3 0.0078 168 1.3104 BX29500047BZBA 4 B1-1隔板(4)B1-1 Partition 245 X 47 X 3 0.312 BX24500047BZBA 0.0065 48 5 氣泡紙(5)Bubble Sheet BAG280240BWABA 280 X 240 0.006 24 0.144 6 C1內盒(6)Product Box 12. BX31025555AABA 310 X 255 X 55 0.13 1.56 7 外紙箱(7)Carton 527 X 325 X 360 0.83 0.83 BX52732536CCBA 8 9 2.一整箱總重量 (Total LCD Weight in carton): 19.74 Kg±10% 3. 單箱數量規格表 (Packaging Specifications and Quantity): (1)Quantity Of Spacer: A1-1隔板 X 14 , B1-1隔板 X (2)Total LCM quantity in carton: quantity per box x no of boxes 12 468 (5) 氣泡紙 Bubble Sheet (1)靜電袋+(2)氣泡袋+LCM Antistatic Bag+Bubble Bag+LCM (4) B1-1隔板 B1-1 Partition (3) A1-1隔板 À1-1 Partition 11 (5) 氣泡紙 **Bubble Sheet** ₩ (7)外紙箱 Carton (6) C1內盒 Product Box 特 記 事 項 (REMARK) 4. Label Specifications: 5. LCM排放示意圖(前後間隔不放置): 5. LCM placed as figure showing: 依廠內標準作業 (First and last slot should be empty)

🥅 模組(LCM) X 1pcs.