# **NCE N-Channel Super Trench Power MOSFET**

### **Description**

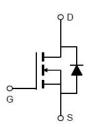
The NCEP15T14D uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{\text{DS(ON)}}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

#### **General Features**

- $V_{DS}$  =150V, $I_D$  =140A  $R_{DS(ON)}$ =5.6m $\Omega$  , typical @  $V_{GS}$ =10V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

## **Application**

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification



Schematic diagram



TO-263-2L top view

100% UIS TESTED!

100% ΔVds TESTED!

### **Package Marking and Ordering Information**

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCEP15T14D	NCEP15T14D	TO-263-2L	-	-	-

## Absolute Maximum Ratings (T<sub>c</sub>=25℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	150	V
Gate-Source Voltage	V <sub>G</sub> s	±20	V
Drain Current-Continuous	I <sub>D</sub>	140	Α
Drain Current-Continuous(Tc=100°C)	I <sub>D</sub> (100°C)	100	Α
Pulsed Drain Current	I <sub>DM</sub>	560	Α
Maximum Power Dissipation	P <sub>D</sub>	320	W
Derating factor		2.1	W/℃
Single pulse avalanche energy (Note 1)	E <sub>AS</sub>	1296	mJ
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	$^{\circ}$

#### **Thermal Characteristic**

Thermal Resistance, Junction-to-Case	Rejc	0.47	°C/W
		•	



# Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

BV <sub>DSS</sub> I <sub>DSS</sub> I <sub>GSS</sub>	V <sub>GS</sub> =0V I <sub>D</sub> =250µA V <sub>DS</sub> =150V,V <sub>GS</sub> =0V V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	150	-	_	
IDSS	V <sub>DS</sub> =150V,V <sub>GS</sub> =0V		-		
	<u> </u>	-		-	V
I <sub>GSS</sub>	\/=+20\/\/=0\/		-	1	μA
	VGS-IZOV, VDS-UV	-	-	±100	nA
V <sub>GS(th)</sub>	$V_{DS}=V_{GS},I_{D}=250\mu A$	2.0	3.0	4.0	V
R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =70A	-	5.6	6.4	mΩ
R <sub>G</sub>		-	4.5	-	Ω
<b>g</b> FS	V <sub>DS</sub> =10V,I <sub>D</sub> =70A	70	-	-	S
C <sub>lss</sub>	V <sub>DS</sub> =75V,V <sub>GS</sub> =0V,	-	6000	-	PF
Coss		-	690	-	PF
C <sub>rss</sub>	F=1.UMHZ	-	24	-	PF
t <sub>d(on)</sub>		-	26	-	nS
tr	$V_{DD}$ =75 $V$ , $I_D$ =70 $A$	-	36	-	nS
t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =4.7 $\Omega$	-	47	-	nS
t <sub>f</sub>		-	15	-	nS
Qg	\\ 75\\\ 70A	-	80	-	nC
Qgs	•	-	32	-	nC
Q <sub>gd</sub>	V <sub>GS</sub> =1UV	-	22	-	nC
V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>F</sub> = I <sub>S</sub>	-		1.2	V
Is		-	-	140	Α
t <sub>rr</sub>	T <sub>J</sub> = 25°C, I <sub>F</sub> = I <sub>S</sub>	-	146	-	nS
Qrr	di/dt = 100A/μs	-	485	-	nC
	RDS(ON) RG GFS  Clss Coss Crss  td(on) tr td(off) tf Qg Qgs Qgs Qgd  VSD Is trr	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

#### Notes:

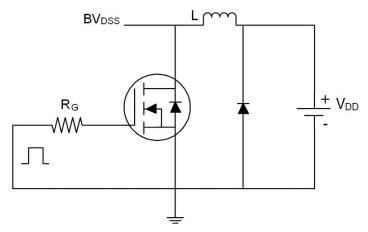
<sup>1.</sup> EAS condition : Tj=25  $^{\circ}\mathrm{C}$  ,V\_DD=50V,V\_G=10V,L=0.5mH,Rg=25 $\Omega$ 

<sup>2.</sup> Guaranteed by design, not subject to production

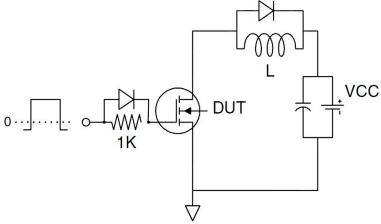
<sup>3.</sup> These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsin k, assuming a maximum junction temperature of TJ(MAX)=175° C. The SOA curve provides a single pulse rating.

## **Test Circuit**

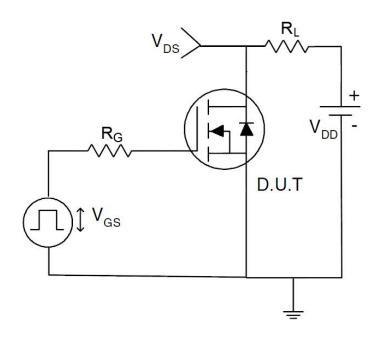
## 1) E<sub>AS</sub> test Circuit



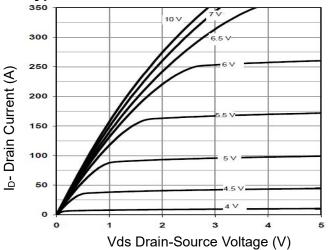
## 2) Gate charge test Circuit



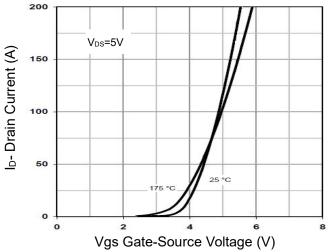
## 3) Switch Time Test Circuit







**Figure 1 Output Characteristics** 



**Figure 2 Transfer Characteristics** 

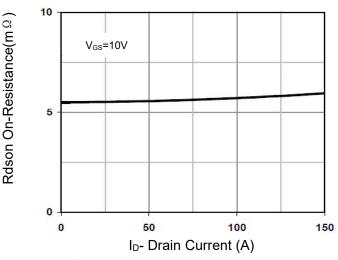


Figure 3 Rdson- Drain Current

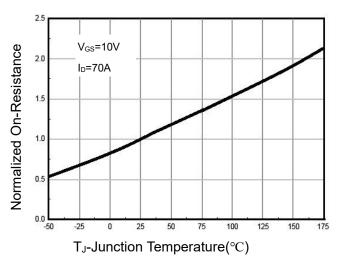


Figure 4 Rdson-JunctionTemperature

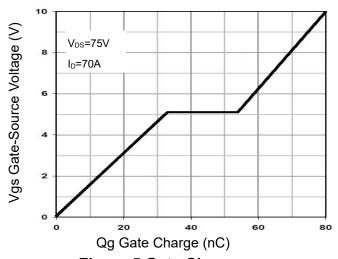


Figure 5 Gate Charge

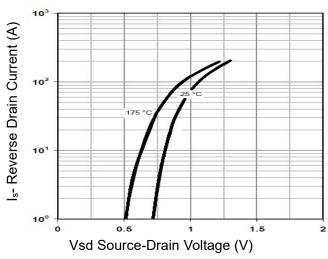
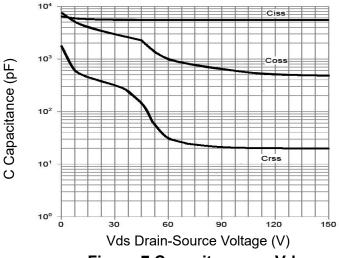


Figure 6 Source- Drain Diode Forward



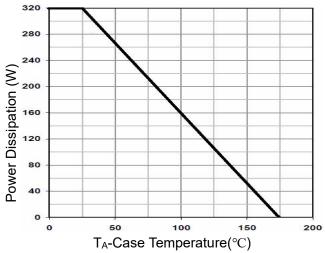
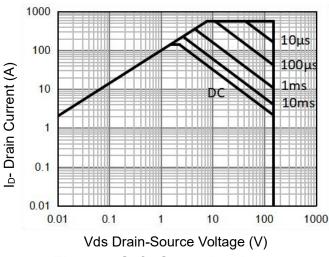


Figure 7 Capacitance vs Vds

Figure 9 Power De-rating



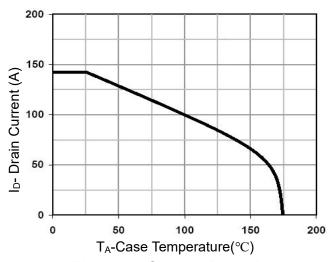
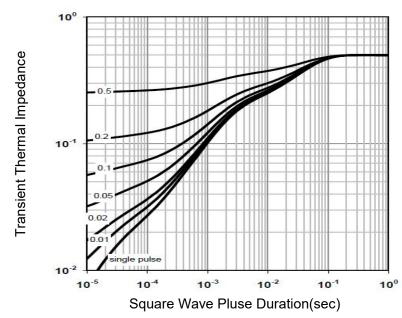


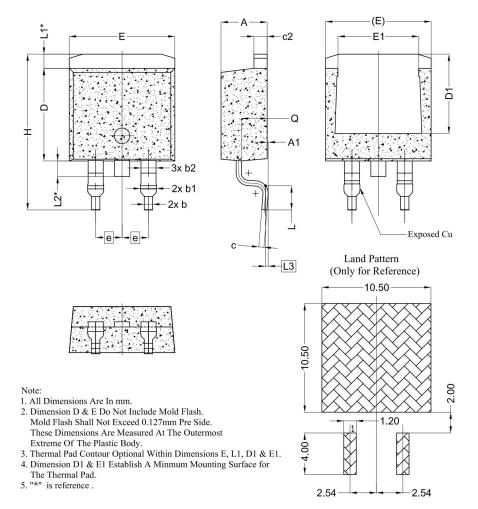
Figure 8 Safe Operation Area(Note3)

Figure 10 Current De-rating



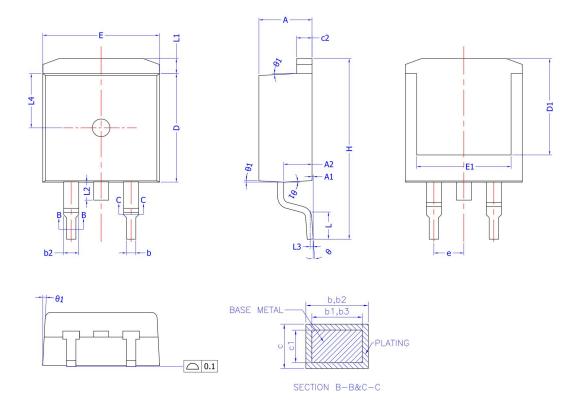
**Figure 11 Normalized Maximum Transient Thermal Impedance** 

# TO-263-2L(G) Package Information



SYMBOL	DIMENSIONS			
STIVIBUL	MIN.	NOM.	MAX.	
А	4.24	4.44	4.64	
A1	0.00	0.10	0.25	
b	0.70	0.80	0.90	
b1	1.20	1.55	1.75	
b2	1.20	1.45	1.70	
С	0.40	0.50	0.60	
c2	1.15	1.27	1.40	
D	8.82	8.92	9.02	
D1	6.86	7.65	_	
E	9.96	10.16	10.36	
E1	6.89	7.77	7.89	
е	2.54 BSC			
Н	14.61	15.00	15.88	
L	1.78	2.32	2.79	
L1	1.36 REF.			
L2	1.50 REF.			
L3	0.25 BSC			
Q	2.30	2.48	2.70	

# TO-263-2L(P) Package Information



# COMMON DIMENSIONS (UNITS OF MEASURE =MILLIMETER)

SYMBOL	MIN	NOM	MAX
Α	4.40	4.50	4.60
A1	0	0.10	0.25
A2	2.20	2.40	2.60
b	0.76		0.89
b1	0.75	0.80	0.85
b2	1.23		1.37
b3	1.22	1.27	1.32
С	0.47		0.60
c1	0.46	0.51	0.56
c2	1.25	1.30	1.35
D	9.10	9.20	9.30
D1	8.00		
E	9.80	9.90	10.00
E1	7.80		
е	2.		
Н	14.90	15.30	15.70
L	2.00	2.30	2.60
L1	1.17	1.27	1.40
L2	(1 <del>-3-4</del> )	C	1.75
L3	0.25BSC 4.60 REF		
L4			
θ	0°		8°
θ1	1°	3°	5°

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