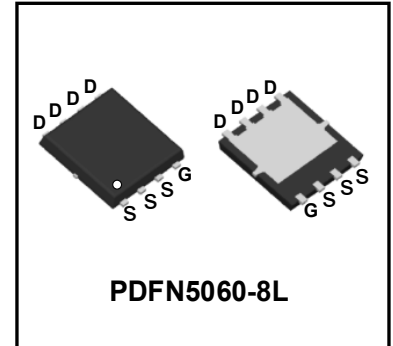


60V N-Channel Enhancement Mode Power MOSFET

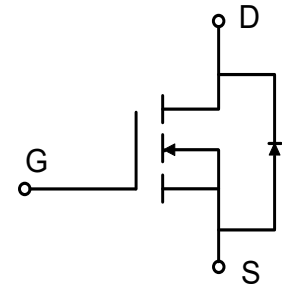
Description

WMB098N06LG2 uses Wayon's 2nd generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.



Features

- $V_{DS} = 60V$, $I_D = 57A$
 $R_{DS(on)} < 9.8m\Omega$ @ $V_{GS} = 10V$
 $R_{DS(on)} < 15m\Omega$ @ $V_{GS} = 4.5V$
- Green Device Available
- Low Gate Charge
- 100% EAS Guaranteed



Applications

- Synchronous Rectification
- DC/DC Converter

Absolute Maximum Ratings ($T_A = 25^\circ C$, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	$T_C = 25^\circ C$	57
		$T_C = 100^\circ C$	36
Pulsed Drain Current ¹	I_{DM}	228	A
Single Pulse Avalanche Energy ²	EAS	26.5	mJ
Total Power Dissipation	P_D	56.8	W
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient ³	$R_{\theta JA}$	69	$^\circ C/W$
Thermal Resistance from Junction-to-Case	$R_{\theta JC}$	2.2	$^\circ C/W$

Electrical Characteristics (T_J= 25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	60	-	-	V
Gate-Body Leakage current	I _{GSS}	V _{DS} = 0V, V _{GS} = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60V, V _{GS} = 0V	-	-	1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.8	2.5	V
Drain-Source on-Resistance ⁴	R _{DS(on)}	V _{GS} = 10V, I _D = 12A	-	7.8	9.8	mΩ
		V _{GS} = 4.5V, I _D = 8A		11.8	15	
Dynamic Characteristics⁵						
Input Capacitance	C _{iss}	V _{DS} = 30V, V _{GS} = 0V, f = 1MHz	-	937	-	pF
Output Capacitance	C _{oss}		-	428	-	
Reverse Transfer Capacitance	C _{rss}		-	31	-	
Gate Resistance	R _g	f = 1MHz	-	1.7	-	Ω
Switching Characteristics⁵						
Total Gate Charge	Q _g	V _{GS} = 10V, V _{DS} = 30V, I _D = 12A	-	17.9	-	nC
Gate-Source Charge	Q _{gs}		-	6.7	-	
Gate-Drain Charge	Q _{gd}		-	3.1	-	
Turn-on Delay Time	t _{d(on)}	V _{GS} = 10V, V _{DD} = 30V, R _G = 3Ω, I _D = 12A	-	6.9	-	ns
Rise Time	t _r		-	51	-	
Turn-off Delay Time	t _{d(off)}		-	17.3	-	
Fall Time	t _f		-	7.6	-	
Body Diode Reverse Recovery Time	t _{rr}	I _F = 12A, dI _F /dt = 100A/μs	-	50	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	49	-	nC
Drain-Source Body Diode Characteristics						
Diode Forward Voltage ⁴	V _{SD}	I _S = 1A, V _{GS} = 0V	-	-	1.2	V
Continuous Source Current	T _C =25°C	I _S	-	-	57	A

Notes:

1. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C.
2. The EAS data shows Max. rating . The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=23A.
3. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test.

Typical Characteristics

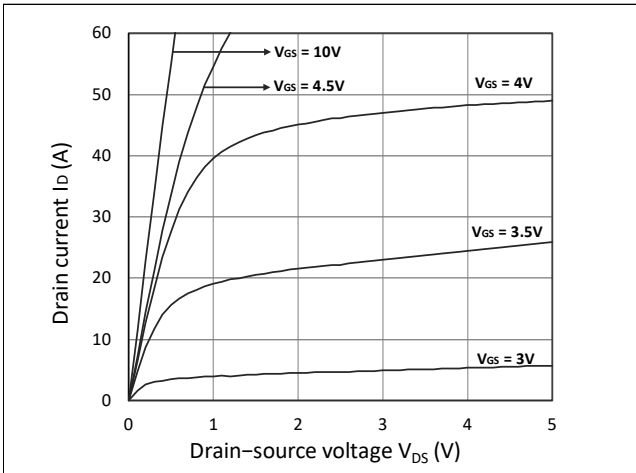


Figure 1. Output Characteristics

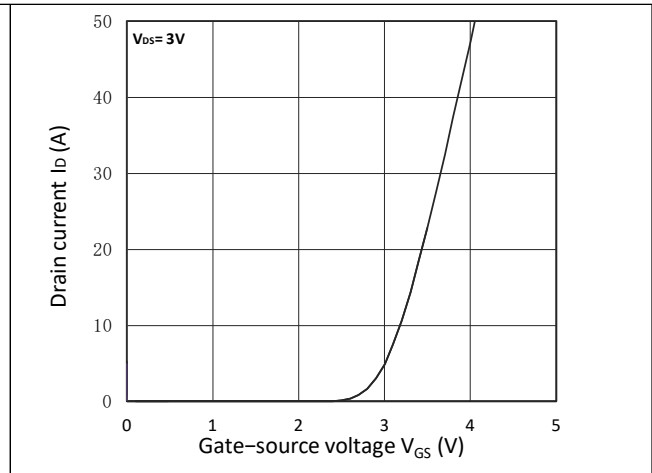


Figure 2. Transfer Characteristics

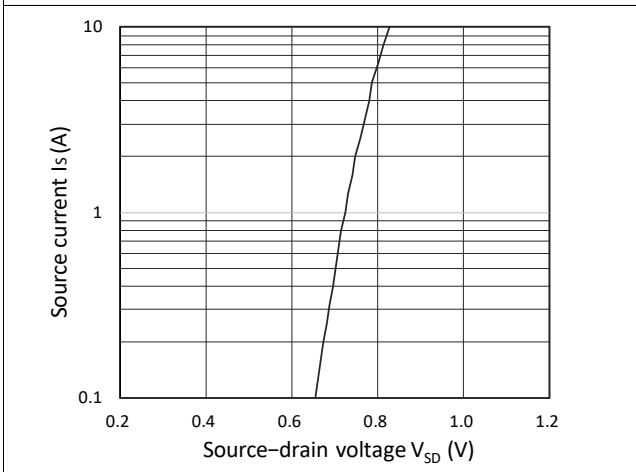


Figure 3. Forward Characteristics of Reverse

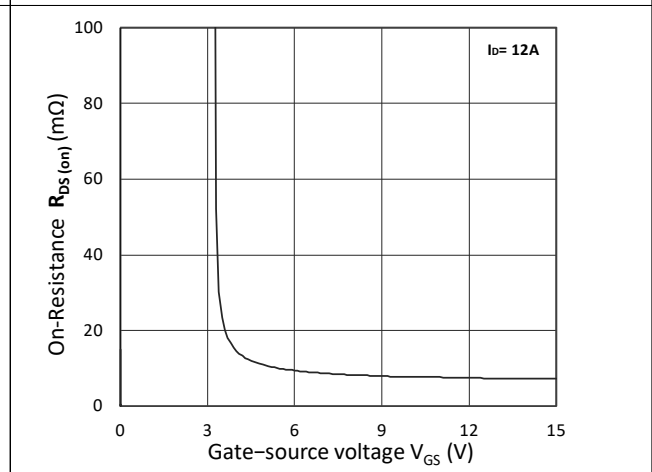


Figure 4. $R_{DS(ON)}$ vs. V_{GS}

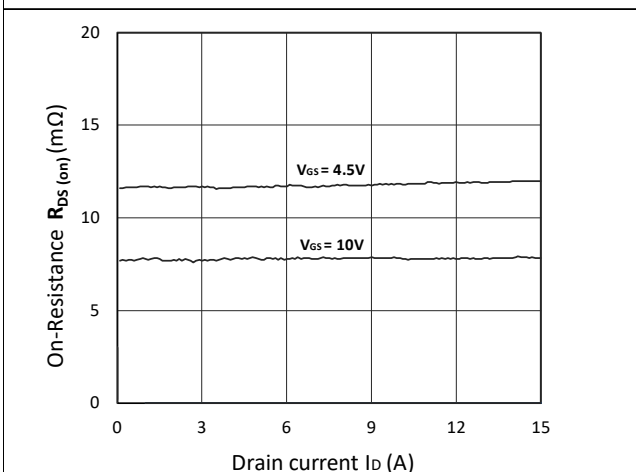


Figure 5. $R_{DS(ON)}$ vs. I_D

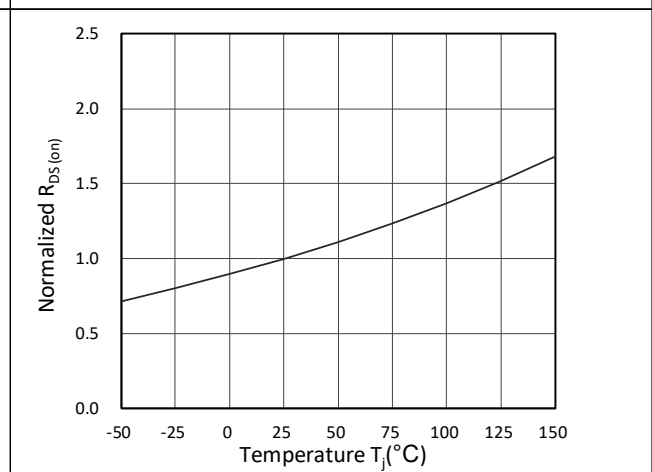


Figure 6. Normalized $R_{DS(ON)}$ vs. Temperature

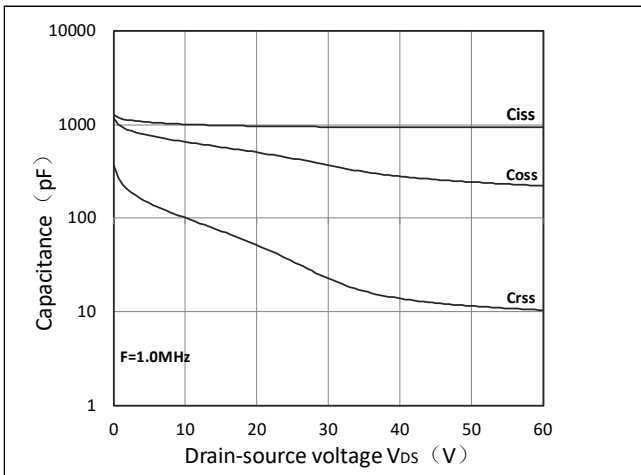


Figure 7. Capacitance Characteristics

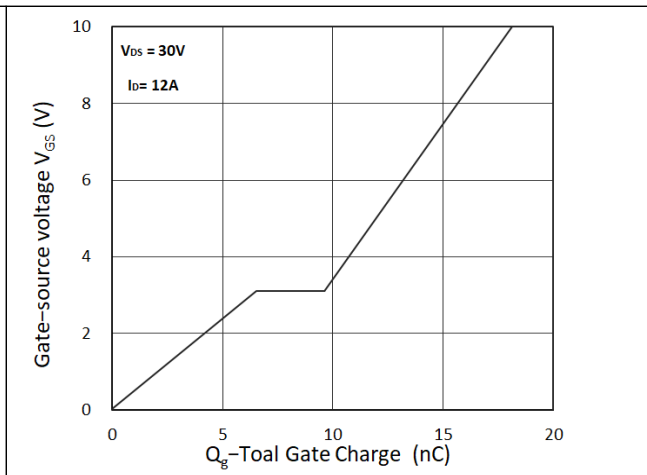


Figure 8. Gate Charge Characteristics

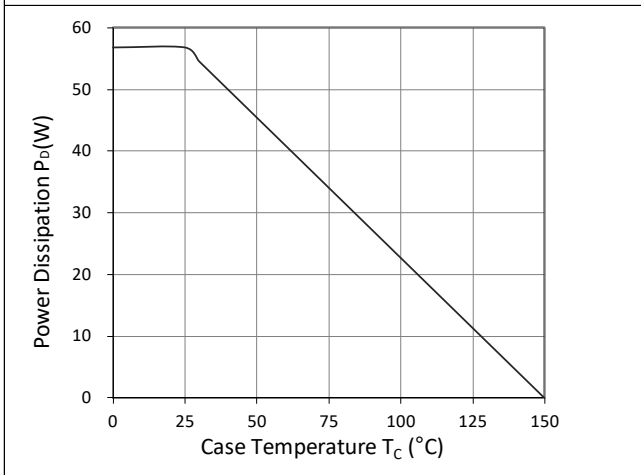


Figure 9. Power Dissipation

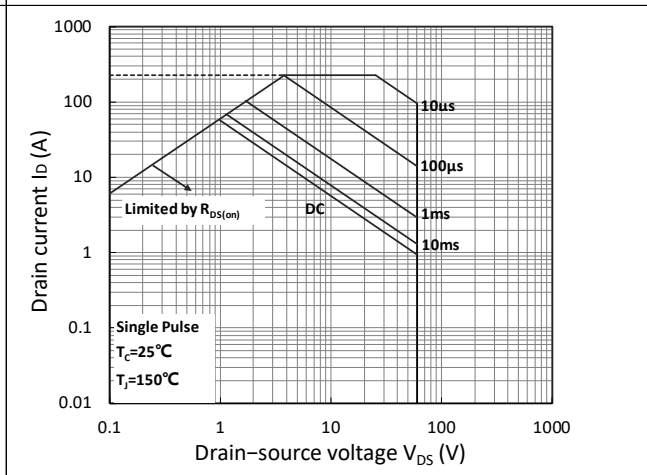


Figure 10. Safe Operating Area

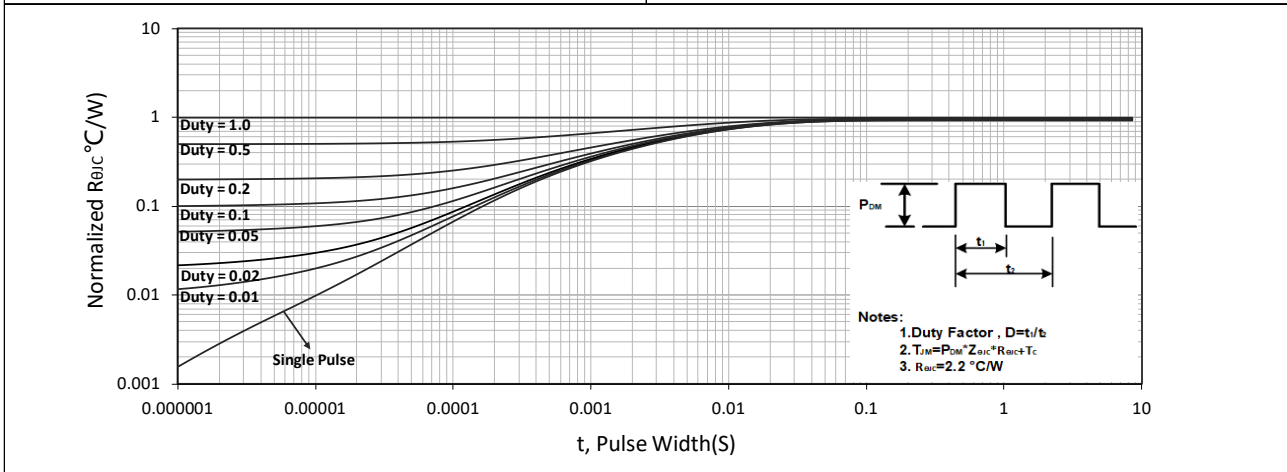


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit

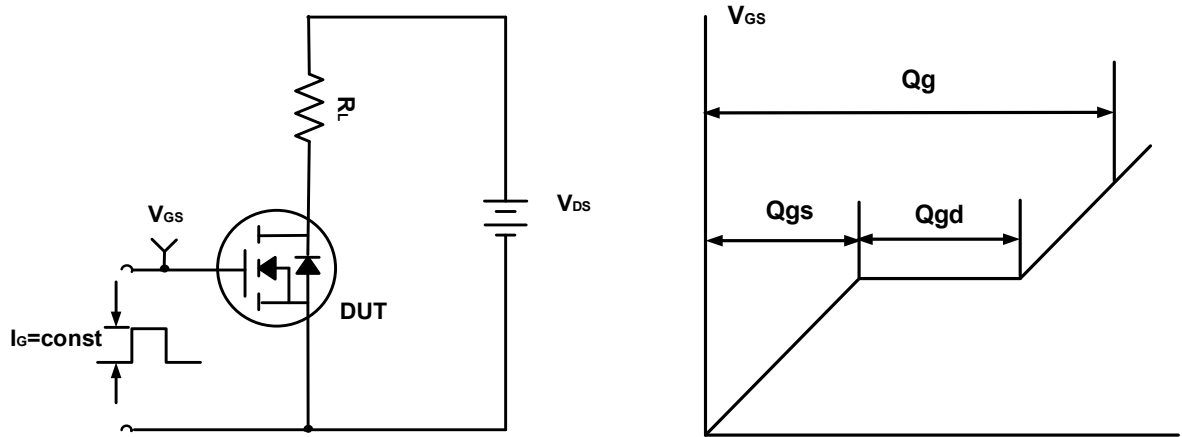


Figure A. Gate Charge Test Circuit & Waveforms

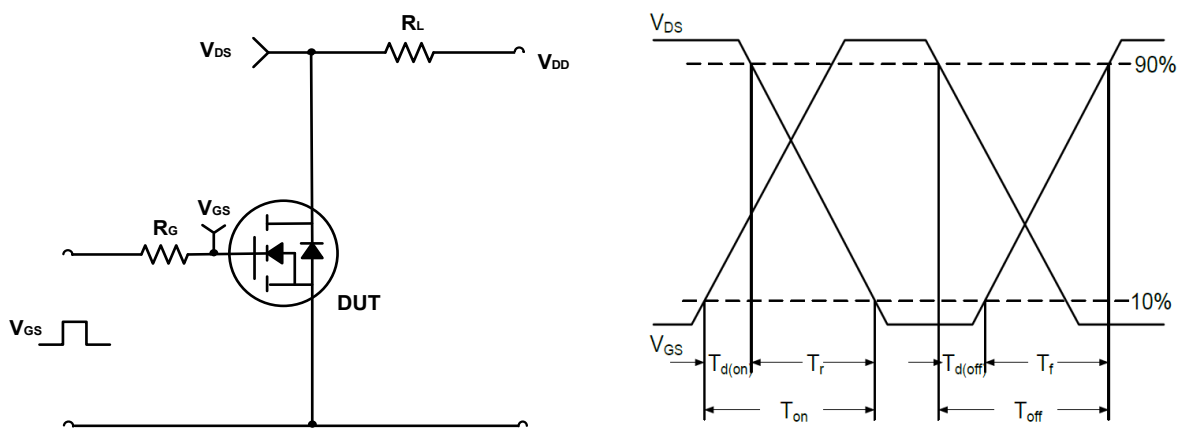


Figure B. Switching Test Circuit & Waveforms

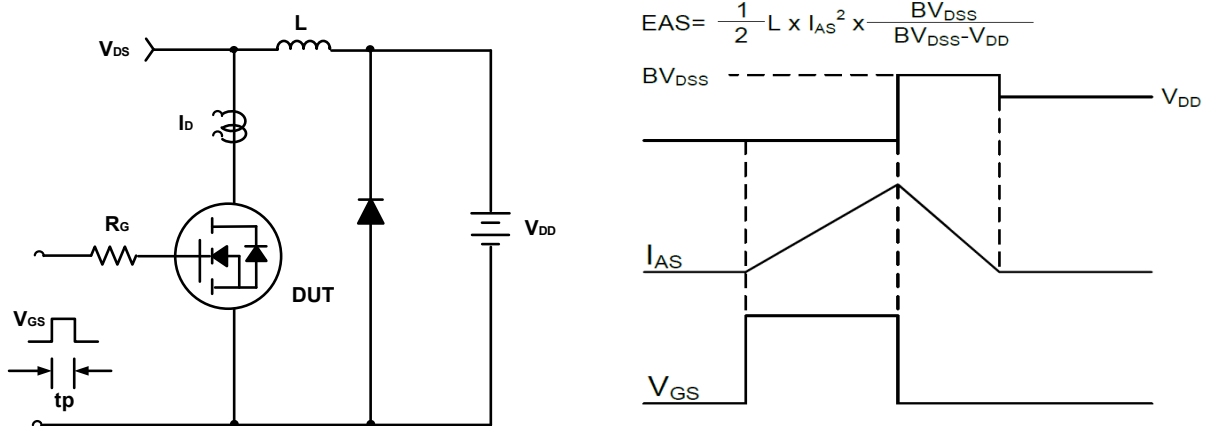
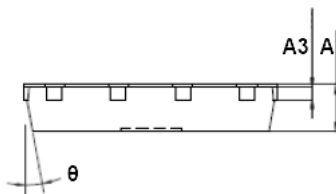
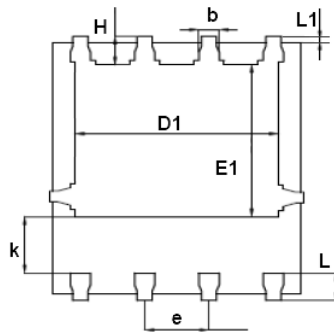
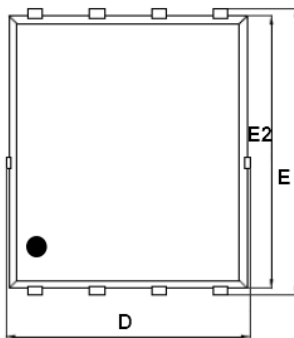


Figure C. Unclamped Inductive Switching Circuit & Waveforms

Mechanical Dimensions for PDFN5060-8L



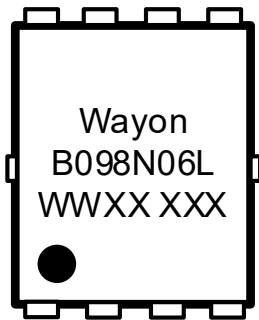
COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	0.90	1.20
A3	0.15	0.35
D	4.80	5.40
E	5.90	6.35
D1	3.61	4.31
E1	3.30	3.92
E2	5.50	6.06
k	1.10	-
b	0.30	0.51
e	1.27BSC	
L	0.38	0.71
L1	0.05	0.36
H	0.38	0.71
θ	0°	12°

Ordering Information

Part	Package	Marking	Packing method
WMB098N06LG2	PDFN5060-8L	B098N06L	Tape and Reel

Marking Information



B098N06L = Device code

WWXX XXX= Date code

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