

SIGC81T60SNC

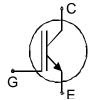
IGBT Chip in NPT-technology

FEATURES:

- 600V NPT technology
- 100µm chip
- short circuit prove
- positive temperature coefficient
- · easy paralleling

This chip is used for:

• IGBT-Modules



Applications:

• drives

Chip Type	V _{CE}	I _{Cn}	Die Size	Package	Ordering Code	
SIGC81T60SNC	600V	100A	8.99 x 8.99 mm ²	sawn on foil	Q67050-A4164-	
SIGCOTTOUSING	0007	TOUA	0.33 x 0.33 11111	Sawii Oii ioii	A003	

MECHANICAL PARAMETER:

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Raster size	8.99 x 8.99	mm²		
Area total / active	80.82 / 72.6			
Emitter pad size	8x(1.77x2.82)			
Gate pad size	0.78 x 1.51			
Thickness	100	μm		
Wafer size	150	mm		
Flat position	90	deg		
Max.possible chips per wafer	169			
Passivation frontside	Photoimide			
Emitter metallization	3200 nm Al Si 1%			
Collector metallization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding			
Die bond	electrically conductive glue or solder			
Wire bond	AI, ≤500μm			
Reject Ink Dot Size	Ø 0.65mm ; max 1.2mm			
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C			



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MAXIMUM RATINGS:

Parameter	Symbol	Value	Unit
Collector-emitter voltage, T _j =25 °C	V _{CE}	600	V
DC collector current, limited by T _{jmax}	I _C	1)	А
Pulsed collector current, t _p limited by T _{jmax}	I _{cpuls}	300	Α
Gate emitter voltage	V _{GE}	±20	V
Operating junction and storage temperature	T_j , T_{stg}	-55 + 150	°C

¹⁾ depending on thermal properties of assembly

STATIC CHARACTERISTICS (tested on chip), T_i =25 °C, unless otherwise specified:

Parameter	Symbol	Conditions	Value			Unit
Tarameter		Conditions	min.	typ.	max.	0
Collector-emitter breakdown voltage	V _{(BR)CES}	V_{GE} =0V, I_{C} =4mA	600			
Collector-emitter saturation voltage	V _{CE(sat)}	V _{GE} =15V, I _C =100A	1.7	2.1	2.5	V
Gate-emitter threshold voltage	V _{GE(th)}	I _C =1.5mA, V _{GE} =V _{CE}	3	4	5	
Zero gate voltage collector current	I _{CES}	V _{CE} =600V, V _{GE} =0V			7	μA
Gate-emitter leakage current	I _{GES}	V _{CE} =0V, V _{GE} =30V			300	nA

DYNAMIC CHARACTERISTICS (tested at component):

Parameter	Symbol	Conditions	Value			Unit
raiailletei			min.	typ.	max.	Oill
Input capacitance	Ciss	V _{CE} =25V	-	5430	6500	pF
Output capacitance	Coss	V _{GE} =0V	-	508	610	
Reverse transfer capacitance	Crss	f=1MHz	-	312	373	

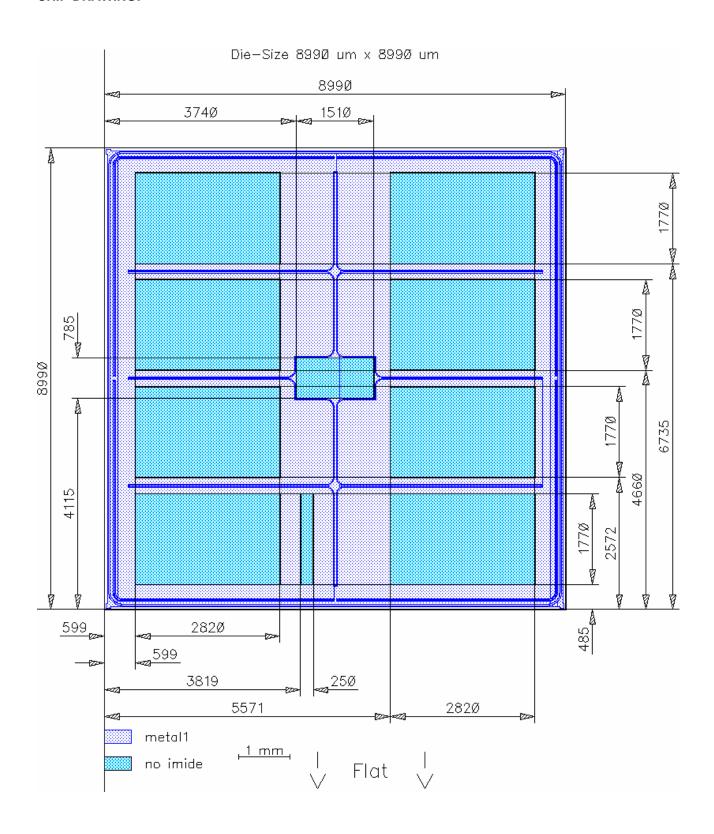
SWITCHING CHARACTERISTICS (tested at component), Inductive Load:

Parameter	Symbol	Conditions ²⁾	Value			Unit
			min.	typ.	max.	
Turn-on delay time	$t_{d(on)}$	$T_{\rm j} = 150^{\circ} \text{C}$	1	65	91	ns
Rise time	t_{r}	V _{CC} =400V I _C =100A	ı	50	70	
Turn-off delay time	$t_{d(off)}$	V_{GE} =+15/0V R_{G} =3.3 Ω	-	450	630	
Fall time	t_{f}	, 10 - 0 : 022	-	90	126	

²⁾ switching conditions different to 600V Standard IGBT 2, under comparable switching conditions 40% faster turnoff than Standard IGBT 2. Values also influenced by parasitic L- and C- in measurement and package.



CHIP DRAWING:





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FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the device data sheet

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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