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B3 EV101WUM-N80 Product Specification Rev.P0

BUYER	
SUPPLIER	HEFEI BOE Optoelectronics Technology CO., LTD
FG-Code	EV101WUM-N80

ITEM BUYER SIGNATURE DATE	ITEM SUPPLIER SIGNATURE DATE
	Prepared
	Reviewed
	Approved

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REVISION HISTORY ($$)preliminary specification ()Final specification						
REV. ECN No.	DESCRIPTION	DESCRIPTION OF CHANGES				PREPARED
PO	Initial Release			2024-01-12	2	刘贵阳

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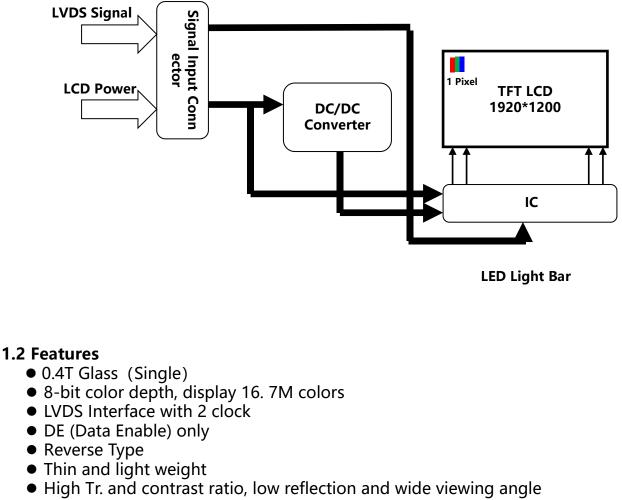
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1.0 GENERAL DESCRIPTION

1.1 Introduction

EV101WUM-N80 is a color active matrix TFT LCD module using amorphous silico n TFT 's (Thin Film Transistors) as an active switching devices. This module has a 10.1 inch diagonally measured active area with WUXGA resolutions (1920(H)x120 0(V)). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in verti cal stripe and this module can display 16.7M colors.



- RoHS compliant
- Landscape usage support

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1.3 Application ● HMI					
1.4 General Spe The following	cification s are general specifi	cations at the	EV101WUM-N80		

<Table 1. LCD Module Specifications>

Parameter	Specification	Unit	Remarks
Active Area	216.8064(H)x135.504(V)	mm	
Number Of Pixels	1920(H)x1200(V)	pixels	
Pixel Pitch	0.11292(H)x0.11292(V)	mm	
Pixel Arrangement	RGB Vertical stripe		
Display Mode	Normally black		
Display Colors	16.7M	colors	6+FRC
Surface Treatment	НС		
Contrast Ratio	Тур. 1000		
Viewing Angle(CR>10)	Тур. 80/80/80	deg.	
Response Time	Typ. 30 , Max. 35	ms	
Color Gamut	NTSC 72% typ.		
Brightness	1000 typ.	cd/m2	
Brightness Uniformity	85 typ. 75 min.		
Power Consumption	1.08	watt	
Outline Dimension	228.56(H)*149.06(V)*4.3 (B)	mm	
Weight	252 (Estimate value)	gram	
Back-light	Down edge side, one LED Light ba r		

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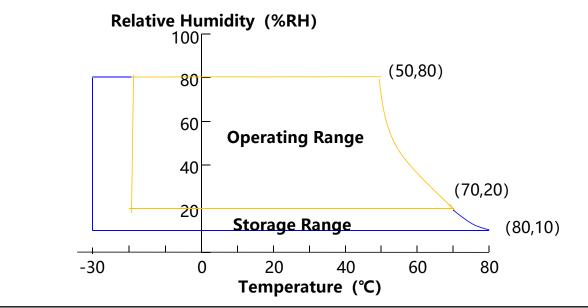
2.0 ABSOLUTE MAXIMUM RATINGS

The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

Parameter	Symbol	Min.	Max.	Unit	Remark
Dower Supply Voltage	VDD	VSS-0.3	5	V	Ta = 25 °C
Power Supply Voltage	VBLU	VSS-0.3	26.7	V	Ta = 25 °C
Operating Temperature	T _{OP}	0	+50	٦°	
Operating Temperature	T _{SUR}	-	+65	C°	
Storage Temperature	Τ _{st}	-20	+60	C°	Note 1
Operating Ambient Humidit y	Нор	10	80	%RH	
Storage Humidity	Hst	10	80	%RH	

< Table 2. Absolute Maximum Ratings>

Note : 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water. T_{SUR} : Panel surface Temperature is measured at 50°C Dry Condition



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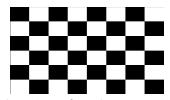
3.0 ELECTRICAL SPECIFICATIONS

3.1 TFT LCD Module

< Table 3. LCD Module Electrical specifications > $[Ta = 25 \pm 2 \ ^{\circ}C]$

			Values			
Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes
Power Supply Voltage	VDD	3.0	3.3	3.6	V	
Power Supply Ripple Voltage	VRP	-	-	300	mV	Note 1
Power Supply Current	IDD	-	330	520	mA	
Rush current	IRUSH	-	-	2	А	Note 2
Differential Input High T hreshold Voltage	VLVTH		-	+100	mV	
Differential Input Low Threshold Voltage	VLVTL	-100	-	-	mV	
Input Differential Voltage	VID	200	400	600	mV	
Common Input Voltage	VLVC	0.6	1.2	1.6	V	
BLU Supply Voltage	V_{LED}	-	27	28	V	
BLU Supply Current	I _{LED}	-	240	-	mA	
	P _D	-	1.08	1.7	W	
Power Consumption	P _{LED}	-	-	6.72	W	Note 1
	P _{total}	-	-	8.32	W	

Notes : 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for VDD=3.3V, Frame rate f_v =60Hz and Clock frequency = 77.56MHz. Test Pattern of power supply current a) Typ : Mosaic 8 x 6 Pattern(L0/L255) b) Max : H 1 Line



2. The duration of rush current is about 2ms and rising time of Power Input is 1ms(min)

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4.0 INTERFACE CONNECTION

4.1 Interface Input Signal & Power :
 - LVDS Connector : FH34SRJ-45S-0.5SH(50) (HRS) or equivalent
 < Table5. Pin Assignment for LCM Connector >

Pin No.	Symbol	Description	I/O
1	VLED-	LED Cathode	Р
2	VLED-	LED Cathode	Р
3	VLED+	LED Anode	Р
4	VLED+	LED Anode	Р
5	NC	NC	-
6	GND	GROUND	Р
7	ELV3P	EVEN LVDS Positive data signal (+)	I
8	ELV3N	EVEN LVDS Negative data signal (-)	I
9	GND	GROUND	Р
10	ELV2P	EVEN LVDS Positive data signal (+)	I
11	ELV2N	EVEN LVDS Negative data signal (-)	I
12	GND	GROUND	Р
13	ELVCLKP	EVEN LVDS Positive CLK signal (+)	I
14	ELVCLKN	EVEN LVDS Negative CLK signal (-)	I
15	GND	GROUND	Р
16	ELV1P	EVEN LVDS Positive data signal (+)	I
17	ELV1N	EVEN LVDS Negative data signal (-)	
18	GND	GROUND	Р
19	ELVOP	EVEN LVDS Positive data signal (+)	
20	ELVON	EVEN LVDS Negative data signal (-)	I

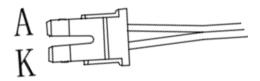
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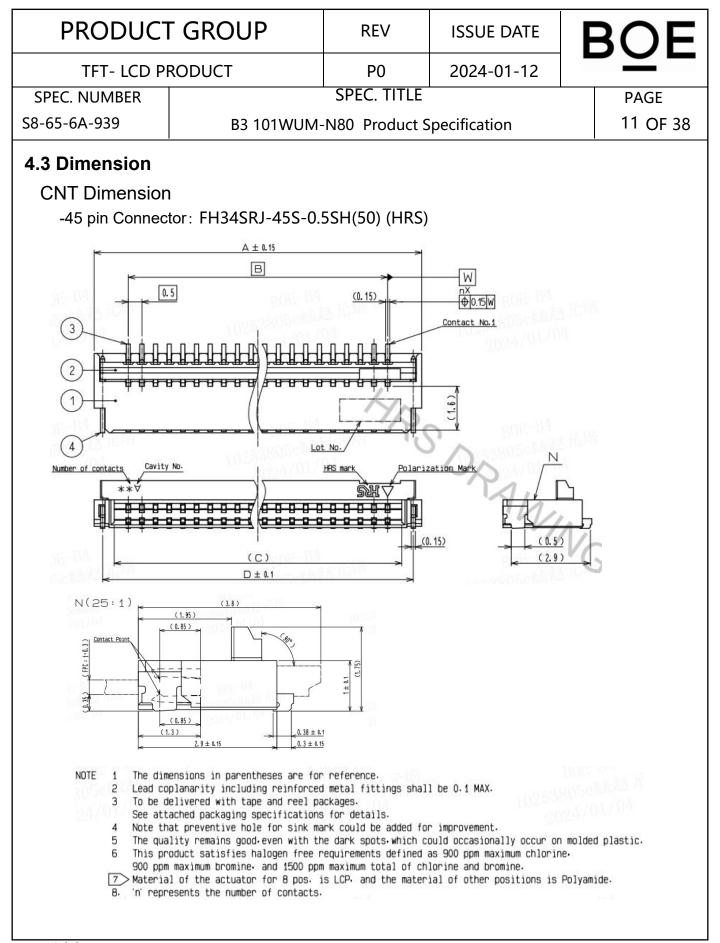
Pin No.	Symbol	Description	I/O
21	GND	GROUND	Р
22	OLV3P	Odd LVDS Positive data signal (+)	I
23	OLV3N	Odd LVDS Negative data signal (-)	I
24	GND	GROUND	Р
25	OLV2P	Odd LVDS Positive data signal (+)	I
26	OLV2N	Odd LVDS Negative data signal (-)	I
27	GND	GROUND	Р
28	OLVCLKP	Odd LVDS Positive CLK signal (+)	I
29	OLVCLKN	Odd LVDS Negative CLK signal (-)	I
30	GND	GROUND	Р
31	OLV1P	Odd LVDS Positive data signal (+)	I
32	OLV1N	Odd LVDS Negative data signal (-)	I
33	GND	GROUND	Р
34	OLV0P	Odd LVDS Positive data signal (+)	I
35	OLV0N	Odd LVDS Negative data signal (-)	I
36	GND	GROUND	Р
37	NC	Reserved for BOE use	I
38	NC	Reserved for BOE use	I
39	NC	Reserved for BOE use	Р
40	NC	Not Connection	I
41	VDDIN		Р
42	VDDIN]	Р
43	VDDIN	Power supply VDDIN=3.3V (Typ.)	Р
44	VDDIN		Р
45	VDDIN		Р

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4.2 Pin assignment for LED Bar Connector : JST BHSR-02VS-1 or equivalent

	< Table6. Pin assignment for LED Bar >							
Pin No Symbol Description Remarks								
1	VLED+	Channel 2 Current Feedback	A					
2	VLED-	LED power supply	К					





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4.3 Dimension

CNT Dimension

-45 pin Connector: FH34SRJ-45S-0.5SH(50) (HRS)

PART NUMBER	PART NUMBER CODE NUMBER	NUMBER OF	DIMENSION OF CONNECTOR, FPC,PCB MOUNTING PATTERN AND STENCIL					DIMENSION OF DRAWING FOR PACKING						
8h	BOIEana	CONTACTS	Α	в	С	D	Е	F	G	н	J	к	L	м
FH34SRJ-4S-0.5SH(50)	CL580-1238-7-50	4	4	1.5	2.53	3.38	3.1	3.9	2.5	16	7.5	72	17.4	21.4
FH34SRJ-5S-0.5SH(50)	CL580-1264-7-50	5	4.5	2	3.03	3.88	3.6	4.4	3	16	7.5	-	17.4	21.4
FH34SRJ-6S-0.5SH(50)	CL580-1236-1-50	6	5	2.5	3.53	4.38	4.1	4.9	3.5	16	7.5	-	17.4	21.4
FH34SRJ-7S-0.5SH(50)	CL580-1200-0-50	~7	5.5	3	4.03	4.88	4.6	5.4	4	16	7.5	-	17.4	21.4
FH34SRJ-8S-0.5SH(50)	CL580-1231-8-50	Os	6	3.5	4.53	5.38	5.1	5.9	4.5	16	7.5	-	17.4	21.4
FH34SRJ-9S-0.5SH(50)	CL580-1262-1-50	9	6.5	4	5.03	5.88	5.6	6.4	5	16	7.5	-	17.4	21.4
FH34SRJ-10S-0.5SH(50)	CL580-1251-5-50	10	A	4.5	5.53	6.38	6.1	6.9	5.5	16	7.5		17.4	21.4
FH34SRJ-11S-0.5SH(50)	CL580-1258-4-50	11	7.5	5	6.03	6.88	6.6	7.4	6	16	7.5	-	17.4	21.4
FH34SRJ-12S-0.5SH(50)	CL580-1253-0-50	12	8	5.5	6.53	7.38	7.1	7.9	6.5	24	11.5	-	25.4	29.4
FH34SRJ-14S-0.5SH(50)	CL580-1252-8-50	14	9	6.5	7.53	8.38	8.1	8.9	7.5	24	11.5	-	25.4	29.4
FH34SRJ-16S-0.5SH(50)	CL580-1259-7-50	16	10	7.5	8.57	9.38	9.1	9.9	8.5	24	11.5	-	25.4	29.4
FH34SRJ-18S-0.5SH(50)	CL580-1248-0-50	18	11	8.5	9.57	10.38	10.1	10.9	9.5	24	11.5	-	25.4	29.4
FH34SRJ-20S-0.5SH(50)	CL580-1256-9-50	20	12	9.5	10.57	11.38	11.1	11.9	10.5	24	11.5	-	25.4	29.4
FH34SRJ-22S-0.5SH(50)	CL580-1254-3-50	22	13	10.5	11.57	12.38	12.1	12.9	11.5	24	11.5	-	25.4	29.4
FH34SRJ-24S-0.5SH(50)	CL580-1255-6-50	24	14	11.5	12.57	13.38	13.1	13.9	12.5	24	11.5	-	25.4	29.4
FH34SRJ-26S-0.5SH(50)	CL580-1247-8-50	26	15	12.5	13.57	14.38	14,1	14.9	13.5	24	11.5	-	25.4	29.4
FH34SRJ-30S-0.5SH(50)	CL580-1232-0-50	30	17	14.5	15.57	16.38	16,1	16.9	15.5	32	14.2	28.4	33.4	37.4
FH34SRJ-32S-0.5SH(50)	CL580-1257-1-50	32	18	15.5	16.53	17.38	17.1	17.9	16.5	32	14.2	28.4	33.4	37.4
FH34SRJ-34S-0.5SH(50)	CL580-1261-9-50	34	19	16.5	17.53	18.38	18.1	18.9	17.5	32	14.2	28.4	33.4	37.4
FH34SRJ-40S-0.5SH(50)	CL580-1260-6-50	40	22	19.5	20.53	21.38	21.1	21.9	20.5	44	20.2	40.4	45.4	49.4
FH34SRJ-45S-0.5SH(50)	CL580-1265-0-50	45	24.5	22	23.03	23.88	23.6	24.4	23	44	20.2	40.4	45.4	49.4
FH34SRJ-50S-0.5SH(50)	CL580-1266-2-50	50	27	24.5	25.53	26.38	26.1	26.9	25.5	44	20.2	40.4	45.4	49.4

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4.4 LVDS Interface

- LVDS Receiver : Timing Controller (LVDS Rx merged) / LVDS Data : Pixel Data < Table 7. LVDS Input Data Mapping >

	D. (N	8-bit LVDS Type				
Channel No.	Data No.	VESA				
	Bit-0	G0				
	Bit-1	R5				
	Bit-2	R4				
0	Bit-3	R3				
	Bit-4	R2				
	Bit-5	R1				
	Bit-6	R0				
	Bit-0	B1				
	Bit-1	B0				
	Bit-2	G5				
1	Bit-3	G4				
	Bit-4	G3				
	Bit-5	G2				
	Bit-6	G1				
	Bit-0	DE				
	Bit-1	VS				
	Bit-2	HS				
2	Bit-3	B5				
	Bit-4	B4				
	Bit-5	B3				
	Bit-6	B2				
	Bit-0	-				
	Bit-1	B7				
	Bit-2	B6				
3	Bit-3	G7				
	Bit-4	G6				
	Bit-5	R7				
	Bit-6	R6				

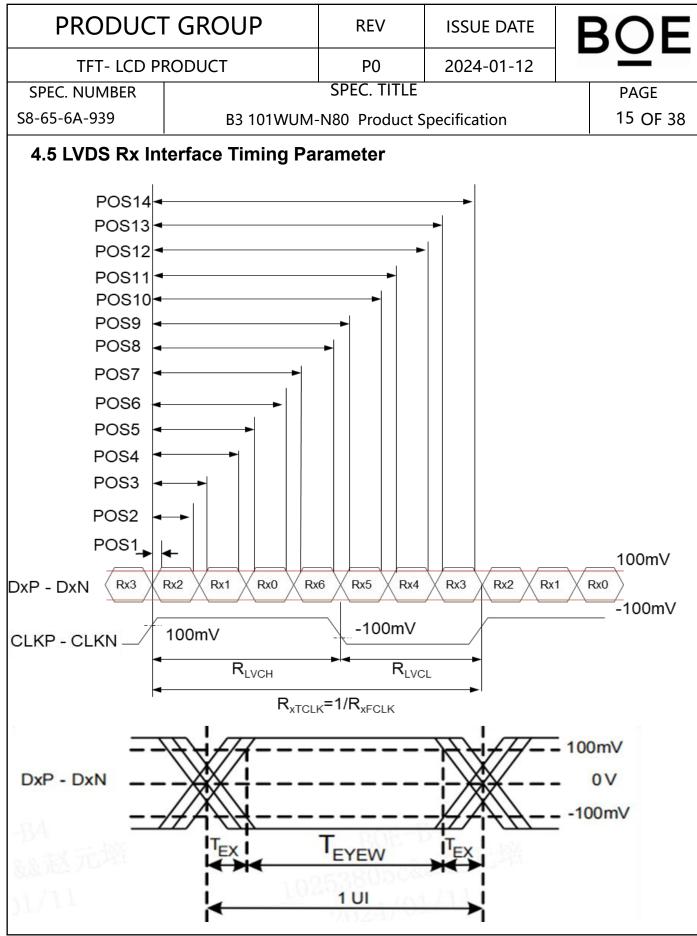
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4.5 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 8.

Parameter	Symbol	Min	Тур	Ma x	Unit	Notes
Clock Frequency	R _{xFCLK}	20	-	100	MHz	
Clock Period	R _{xtclk}	10	-	50	ns	
1 Data bit time	UI	-	1/7	-	R _{xtclk}	
Clock high time	T _{LVCH}		4			
Clock low time	T _{LVCL}		3			
Position 1	T _{POS1}	-0.2 5	-	0.25		
Position 2	T _{POS2}	0.75	-	1.25		
Position 3	T _{POS3}	0.75	1	1.25		
Position 4	T _{POS4}	1.75	-	2.25		
Position 5	T _{POS5}	1.75	2	2.25		
Position 6	T _{POS6}	2.75	-	3.25		
Position 7	T _{POS7}	2.75	3	3.25	UI	
Position 8	T _{POS8}	3.75	-	4.25		
Position 9	T _{POS9}	3.75	4	4.25		
Position 10	T _{POS10}	4.75	-	5.25		
Position 11	T _{POS11}	4.75	5	5.25		
Position 12	T _{POS12}	5.75	-	6.25		
Position 13	T _{POS13}	5.75	6	6.25		
Position 14	T _{POS14}	6.75	-	7.25		
Input eye width	T _{EYEW}	0.5	-	-		
Input eye border	T _{EX}	-	-	0.25		

<Table 8. LVDS Rx Interface Timing Specification>



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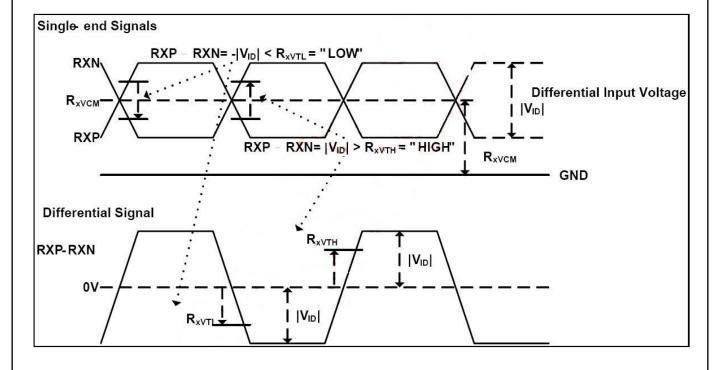
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4.6 DC Specification

- LVDS Receiver Differential Input (DC Characteristics)

Parameter	Symbol	Min	Тур	Max	Unit	Notes
Differential Input High Thre shold Voltage	VTH	-	-	+100	mV	VCM=1.2V
Differential Input Low Threshold Voltage	VTL	-100	-	-	mV	VCIVI-1.2V
Differential Input Common Mode Voltage	VCM	0.6	1.2	1.6	V	
Differential Input Voltage	VID	200	400	600	mV	





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5.0 SIGNAL TIMING SPECIFICATION

5.1 Timing Parameters (DE only mode)

< Table10. Timing Parameter >

ltem	Symbol		Value					
nem	Symbol		Min.	Тур.	Max.	Unit		
DCLK	DCLK Frequency	Fdclk	74.5	77.56	85	MHz		
	Horizontal display area	Thd		960		DCLK		
Horizont al	HSYNC period time	Th	989	1040	1248	DCLK		
	Horizontal Blank		29	80	288	DCLK		
	Vertical display area	^{ay} Tvd 1200						
Vertical	VSYNC period time	Tv	1243	1243	1560	Н		
Vertical	Vertical Blank		43	43	360	Н		
	Frequency	fV	-	60	-	Hz		

Note

1. DE Only Mode, While operation, DE signal should be have the same cycle. The input of HSYNC & VSYNC sig nal does not have an effect on normal operation.

2. Best operation clock frequency is 77.56Mhz.

3. Frequency] = [H Total] * [V Total] * [vertical Frame rate] H Total, V Total and Frame rate]should operate within the range between Frequency_Min and Frequency_Max 4. Except Best operation clock frequency, FOS(Flicker & Brightness & Crosstalk, Etc.) are not guaranteed.

5. Main frequency Max is 85Mhz MHz without spread spectrum

Symbol	Parameter	Condition	Min	Тур	Max	Unit
F _{LVMOD}	Modulating frequency of input cl ock during SSC	-	1	-	100	KHz
F _{LVDEV}	Maximum deviation of input cloc k frequency during SSC	F=70MHz	-3	-	+3	%

< Table 11. LVDS Input SSCG>

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5.3 Input Signals, Basic Display Colors and Gray Scale of Colors

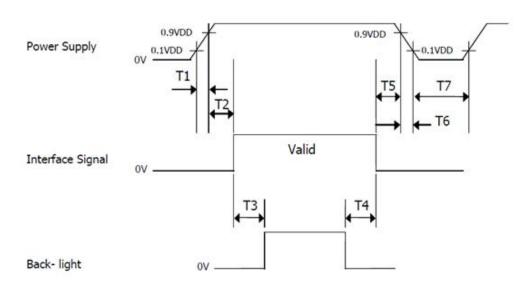
	0							,		Inp			-	-											
Color & G	ray Scale			R	ed	Da	ta					Gr	eer	ו D	ata					В	lue	Da	Ita		
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	Β7	B6	B5	Β4	Β3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\bigtriangleup	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	\bigtriangleup				,	1								1								1			
of Red	\bigtriangledown					ļ								ļ					-			Ļ			
	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\bigtriangledown	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\bigtriangleup	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of Green	\bigtriangleup				,	<u> </u>								<u> </u>								<u>^</u>			
	\bigtriangledown		_		<u> </u>	-	_	_	_				, ,						-	-		-		<u> </u>	
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	\bigtriangledown	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
-	\triangle					<u> </u>								<u> </u>								<u>1</u>			
of Blue				0			0			_	0	0						4	4	4	4		4		4
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0 0	0 0		0 0	0 0	0	0 1	1 0	0 0	0	0 0	0 0	0 0	0	0 1	1 0	0 0	0 0	0	0 0	0 0	0 0	0	0
Gray Scale	Darker	0	U	U	U		U		U	U	U	U	U		U		U	U	U	0	U	<u> ∪</u> ↑	U		U
of White	\sim	┣─																				<u> </u>			
		1	4	4	4	4	4	0	4	4	4	4	4	4	4	0	4	4	4	4	4	1	4		4
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
		1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

< Table 12. Input Signal and Display Color Table >

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5.4 Power Sequence

To prevent a latch-up or DC operation of the Open Cell, the power on/off sequence shall b e as shown in below



< Table 13. Sequence Table >	>
------------------------------	---

Parameter		Units			
Parameter	Min.	Тур.	Max.	Onits	
T1	0.5	-	10	(ms)	
T2	0	-	50	(ms)	
Т3	200	-	-	(ms)	
T4	200	-	-	(ms)	
T5	0	-	50	(ms)	
Т6	0	-	10	(ms)	
T7	500	-	-	(ms)	

Note 1: Even though T1 is over the specified value, there is no problem if the rush current is within Spec.

* When the power supply for LCD (VDD) is off, be sure to pull down the valid and invalid data to 0V.

Note 3: The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.

Note 4: T5 should be measured after the Module has been fully discharged between power off and on period Note 5: If the on time of signals (Interface signal and user control signals) precedes the on time of Power (VLCD), it w ill be happened abnormal display. When T6 is NC status, T6 doesn' t need to be measured

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6.0 OPTICAL SPECIFICATIONS

6.1 Overview

The test of optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25±2°C) with the equipment of Luminance meter system (Gonio meter system and TOPCON BM-5) and test unit shall be located at an approximate dista nce 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0°. We refer to $\theta \emptyset = 0$ (= θ 3) as the 3 o' clock direction (the "right"), $\theta \emptyset = 90$ (= θ 12) as the 12 O' clock direction ("upward"), $\theta \emptyset = 180$ (= θ 9) as the 9 O' clock direction ("left") and $\theta \emptyset = 27$ 0(= θ 6) as the 6 O' clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed.

6.2 Optical Specifications

< Table15. Optical Table >

ltem	Symbol	Condition	Min	Тур.	Мах	Unit	Note
Brightness Center	Lv	θ=0°	800	1000		cd/m2	Note 1
Brightness Uniformity	△Bp		75	85		%	Note 2
	θL		70	80			
Viewing Angle	θ_{R}	C > 10	70	80		dog	Note 2
Viewing Angle	Ψτ	Cr≥10	55	65		deg	Note 3
	ΨΒ		65	75			
Contrast Ratio	Cr	θ=0°	600	800		-	Note 4
Response Time	Tr+Tf	FF=0°	-	30	35	ms	Note 5
	Rx		0.616	0.646	0.676		
	Ry		0.322	0.352	0.382	-	
	Gx		0.294	0.324	0.354		
Color Coordinate of	Gy	θ=0°	0.605	0.635	0.665		Note 6
CIE1931	Bx	0=0	0.120	0.150	0.180	-	Note 6
	Ву		0.054	0.084	0.114		
	Wx		0.290	0.320	0.350		
	Wy		0.310	0.340	0.370		
NTSC Ratio	NTSC	CIE1931	67	72		%	Note 7
Gray inversion angle				6点钟			Note 8
Gamma Scale			2.0	2.2	2.4		

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Note1:Luminance measurement

The test condition is at ILED=240mA and measured on the surface of LCD module at 25°C.

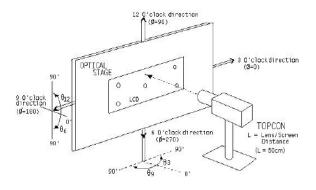
•The data are measured after LEDs are lighted on for more than 5 minutes and LCM displays are fully white. The brightness is the center of the LCD. Measurement equipment CS2000 or similar equipments (Field of view:1deg,Distance:50cm)

•Measuring surroundings: Dark room.

•Measuring temperature: Ta=25°C.

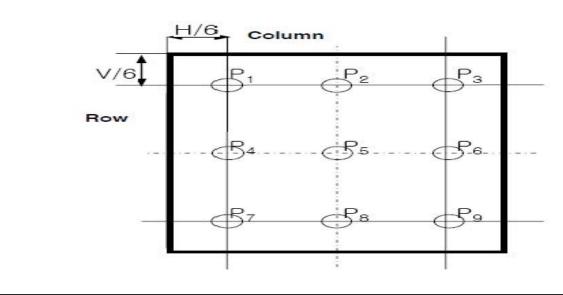
•Adjust operating voltage to get optimum contrast at the center of the display.

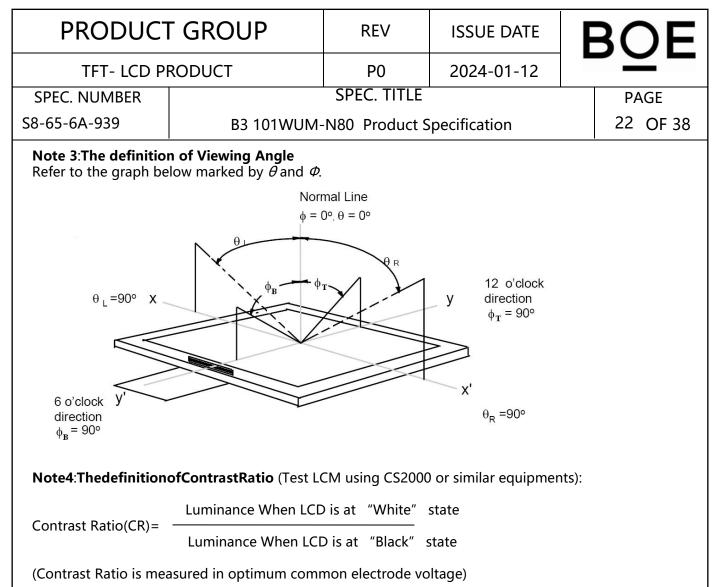
•Measured value at the center point of LCD panel must be after more than 5 minutes while backlight turning on.



Note2:Uniformity

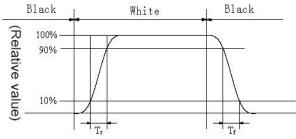
- •The test condition is at ILED=240mA and measured on the surface of LCD module at 25°C.
- Measurement equipment:CS2000 or similar equipments
- •The luminance uniformity is calculated by using following formula:
- ●△Bp = Bp (Min.) / Bp (Max.)×100 (%)
- •Bp (Max.) = Maximum brightness in 9 measured spots
- •Bp (Min.) = Minimum brightness in 9 measured spots.





Note5: Definition of Response time. (Test LCD using DMS501 or similar equipments):

The output sign also photo detector are measured when the input sign also are changed from "black" to "white" (Voltage falling time) and from "white" to "black" (Voltage rising time), respectively .Th e response time is defined as the time interval between the 10% and 90% of amplitudes . Refer to figu res below.

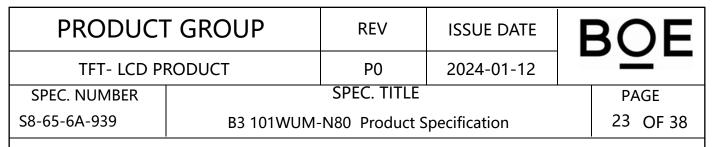




Response time of gray to gray:

Response time Tg is the average time required for display transition by switching the input signal as below table and is based on Frame rate fV = 60Hz to optimize. Each time in below table is defined as Figure 2 and shall be measured by switching the signals for "any level of gray (bright) "and any levelof gray (dark)

5. Theoutputsignalsofphotodetectoraremeasured when the inputsignal sarechanged from "Lx" to "Ly", x, y= [0, 7]. The response time is defined as the time interval between the 10% and 90% of amplitudes. The result of the test can be noted as below:

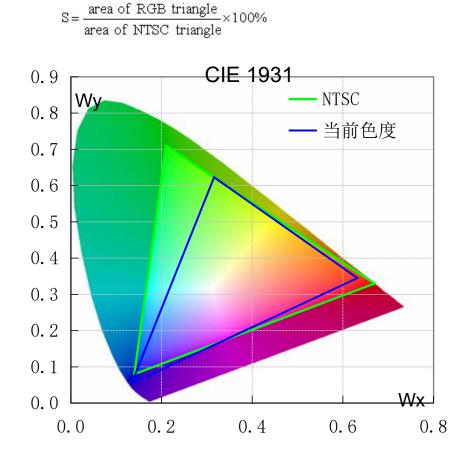


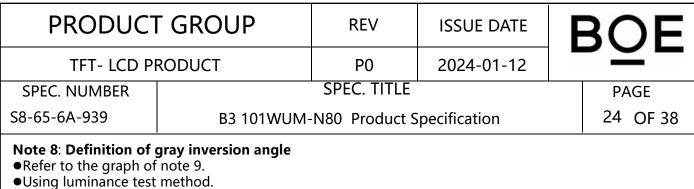
Note 6: Color Coordinates of CIE 1931

The test condition is at ILED=240mA and measured on the surface of LCD module at 25°C. Measurement equipment: CS2000 or similar equipments

The Color Coordinate (CIE 1931) is the measurement of the center of the display shown in below figure.

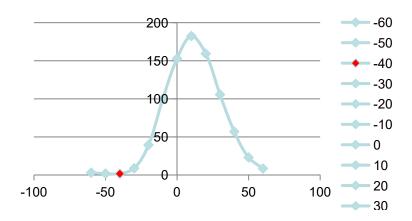
Note 7: Definition of Color of CIE Coordinate and NTSC Ratio.





•Test pattern : 128 gray

•If the viewing direction is 12 o' clock , then test the luminance while $\theta = -60^{\circ}, \theta = -50^{\circ}, \theta = -40^{\circ}, \theta = -30^{\circ}, \theta = -20^{\circ}, \theta = -10^{\circ}, \theta = 0^{\circ}, \theta = 10^{\circ}, \theta = 20^{\circ}, \theta = 30^{\circ}, \theta = -40^{\circ}, \theta = -50^{\circ}, \theta = -50^{\circ}, \theta = -60^{\circ}$. The luminance test as figure below:



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7.0 MECHANICAL CHARACTERISTICS

7.1 Dimensional Requirements

As shown in Table 13.

Parameter	Specification	Unit
Dimensional outline	228.56 (H)*149.06(V)*4.3 (Body)	mm
Weight	252 (Estimate value)	gram
Active area	216.8064(H)x135.504(V)	mm
Pixel pitch	112.92(H)x112.92(V)	um
Number of pixels	$1920(H) \times 1200(V)(1 \text{ pixel} = R + G + B \text{ dots})$	pixels
Back-light	Down edge side 2-LED Light bar Type	

< Table 13. Dimensional Parameters >

7.2 Mounting

See FIGURE 5. (shown in Appendix)

7.3 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

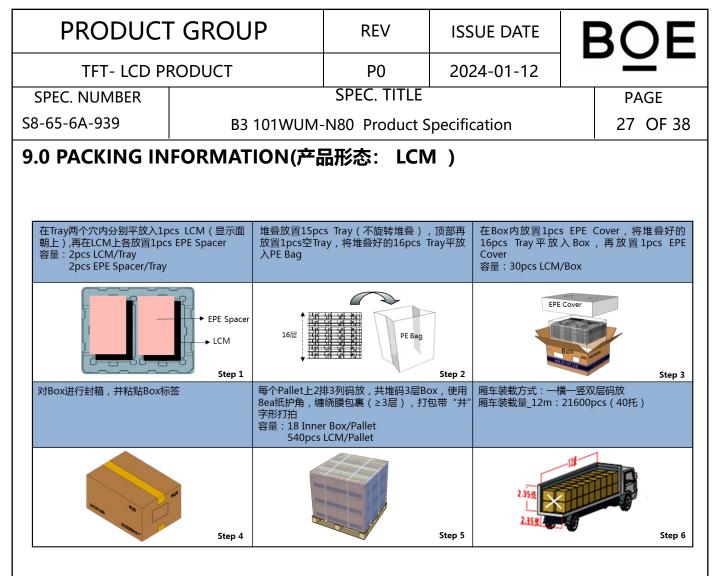
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8.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

<Table 12. Reliability Test Parameters >

No	Test Items	Conditions
1	High temperature storage test	80°C , 240hr
2	Low temperature storage test	-30℃ , 240hr
3	High temperature & high humidity (operation test)	50℃,80%RH,240hr
4	Low temperature operation test	-20℃ , 240hr
5	High temperature operation test	70℃,240hr
6	Thermal Shock Test	-30°C~80°C , 1hr/cycle , 100cycle
7	ESD	150pF,330Ω , +/-4KV Contact , +/-8K V Air , 100Points
8	Packing VIB	1.47G , 1-200hz , X , Y , ±Z , 30min/A xis

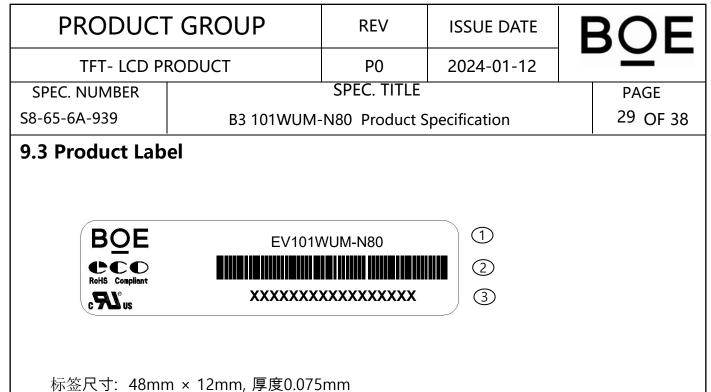


9.1 Packing Note(产品形态: LCM)

- Box Dimension: 495mm(W) x 395mm(D) x 313mm(H)
- Package Quantity in one Box: 30pcs

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Code	Х	х	х	х	х	х	х	Х	Х	Х	Х	Х	Х
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- 1. FG-CODE: EV101WUM-N80-DHP0
- 2. MDL ID 条形码
- 3. MDL ID

BOE MDL ID rule

序列号	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
代码	х	Х	Х	Х	х	х	Х	Х	х	х	Х	х	Х	х	Х	Х	х
描述	GE	BN	等 级	В3	左	F	月	FG Code后四位		序列	号 (流	水码36	位, 去	除I,O,C),U)		

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10.0 Handling & Cautions

Please pay attention to the followings when you use this TFT LCD Module.

10.1 Mounting Precautions

- Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- You must mount a module using specified mounting holes (Details refer to the drawings).
- You should consider the mounting structure so that uneven force (ex. Twisted stress, Concentrated stress) is not applied to the module. And the case on which a module

is

mounted should have sufficient strength so that external force is not transmitted directly to the module.

- Do not apply mechanical stress or static pressure on module; Abnormal display cause by pressing some parts of module during assembly process, do not belong to product failure, the press should be agreed by two sides.
- Determine the optimum mounting angle, refer to the viewing angle range in the specification for each model.
- Do not apply mechanical stress or static pressure on module , and avoid impact, vibration and falling.
- Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- Protection film for polarizer on the module should be slowly peeled off before display.
- Be careful to prevent water & chemicals contact the module surface.
- You should adopt radiation structure to satisfy the temperature specification.
- Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

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- When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane & alcohol is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene , because they cause chemical damage to the polarizer.
- Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- This module has its circuitry PCBs on the rear side and Driver IC, should be handled carefully in order not to be stressed.
- Avoid impose stress on PCB and Driver IC during assembly process ,Do not drawing, bending, COF package & wire.
- Do not disassemble the module.

10.2 Operating Precautions

- Do not connect or disconnect the cable to/from the Module at the "Power On" Condition.
- When the module is operating, do not lose CLK, ENAB signals. If any one of these si gnals is lost, the module would be damaged.
- Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- Do not allow to adjust the adjustable resistance or switch.
- The electrochemical reaction caused by DC voltage will lead to LCD module degrad ation, so DC drive should be avoided.
- The LCD modules use C-MOS LSI drivers, so customers are recommended that any u nused input terminal would be connected to Vdd or Vss, do not input any signals be fore power is turn on, and ground you body, work/assembly area, assembly equipme nt to protect against static electricity.
- Do not exceed the absolute maximum rating value. (supply voltage variation, input v oltage variation, variation in part contents and environmental temperature, and so o n) Otherwise the Module may be damaged.
- Module has high frequency circuits. Sufficient suppression to the electromagnetic int erference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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- The cables should be as short as possible between System Board and PCB interface.
- Connectors are precision devices to transmit electrical signals, and operators should plug in parallel.
- Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- For long-term lighting products, it is recommended to shut down periodically.
- If the product is used for a long time under the condition of 7*24 hrs, it is strongly recommended to contact BOE for filed application engineering advice.
- Long time and large angle forward use or unconventional use , It is strongly recommended to contact BOE for filed application engineering advice.
- Products exposed to low temperature environment for a long time, need to carry out necessary protection , low temperature environment is usually refrigerators , vending machine Etc...

10.3 Electrostatic Discharge Precautions

- Avoid the use work clothing made of synthetic fibers. We recommend cotton clothin g or other conductivity-treated fibers.
- Since a module is composed of electronic circuits, it is not strong to electrostatic dis charge. Make certain that treatment persons are connected to ground through wrist band etc.
- Do not close to static electricity to avoid product damage.
- Do not touch interface pin directly.

10.4 Precautions for Strong Light Exposure

• Do not leave the module operation or storage in Strong light . Strong light exposure causes degradation of polarizer and color filter.

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10.5 Precautions for Storage

A. Atmosphere Requirement

ITEM	UNIT	MIN	ТҮР	МАХ	
Storage Temperature	(°C)	5	25	40	
Storage Humidity	(%rH)	40	50	75	
Storage Life	6 months				
Storage Condition	 The storage room should be equipped with a dark and good ventilation facility. Prevent products from being exposed to the direct sunlight, moisture and water. The product need to keep away from organic solvent and corrosive gas. Be careful for condensation at sudden temperature change. Storage condition is guaranteed under packing conditions. 				

B. Package Requirement

- The product should be placed in a sealed polythene bag.
- Product Should be placed on the pallet, Which is away from the floor, Be cautions not to pile the product up.
- The polarizer surface should not come in contact with any other object. It is recommended that they be stored in the container in which they were shipped.
- As the original protective film, do not use the adhesive protective film to avoid change of Pol color and characteristic.

10.6 Precautions for protection film

- Remove the protective film slowly, keeping the removing direction approximate 30degree not vertical from panel surface, If possible, under ESD control device like ion blower, and the humidity of working room should be kept over 50%RH to reduce the risk of static charge.
- People who peeled off the protection film should wear anti-static strap and grounded well.

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10.7 Appropriate Condition for Commercial Display

-Generally large-sized LCD modules are designed for consumer applications . Accordingly, long-term display like in Commercial Display application, can cause uneven display including image sticking. To optimize module's lifetime and function, several operating usages are required.

1. Normal operating condition

- Temperature: 20±15°C
- Operating Ambient Humidity : 55±20%
- Display pattern: dynamic pattern (Real display)
- Well-ventilated place is recommended to set up Commercial Display system
- 2. Special operating condition
 - a. Ambient condition
 - Well-ventilated place is recommended to set up Commercial Display system.
 - b. Power and screen save
 - Periodical power-off or screen save is needed after long-term display.

c. As the low temperature, the response time is greatly delayed. As the high temperatur es (higher than the operating temperature) the LCD module may turn black screen. The above phenomenon cannot explain the failure of the display. When the temperature re turns to the normal operating temperature, the LCD module will return to normal displ ay.

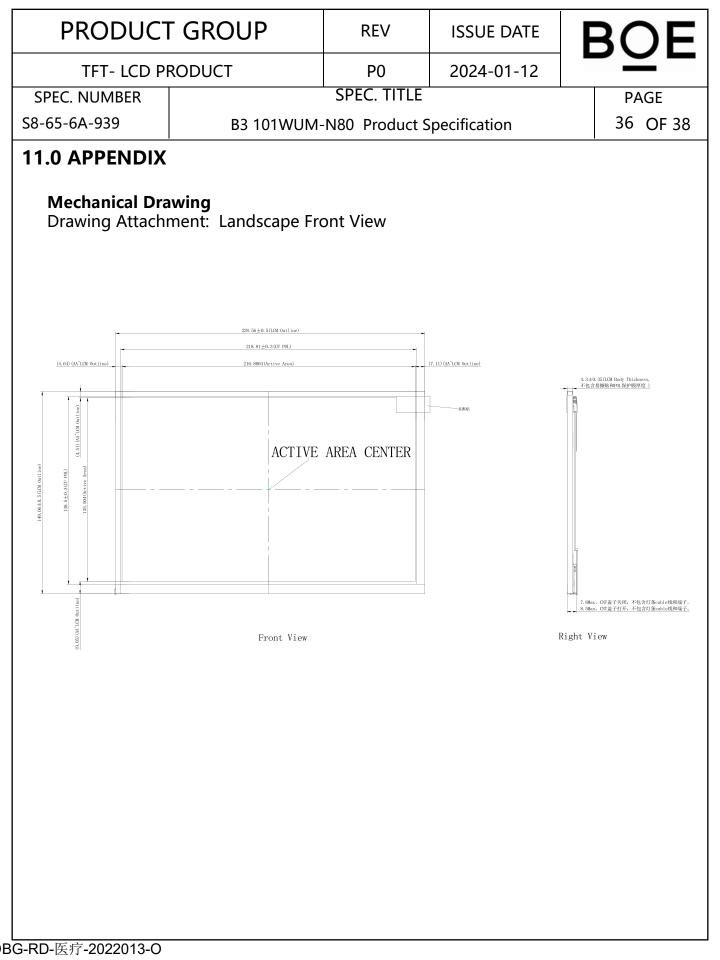
d. When expose to drastic fluctuation of temperature (hot to cold or cold to hot) ,the LCD module may be affected; Specifically, drastic temperature fluctuation from cold to hot ,produces dew on the LCD module 's surface which may affect the operation of th e polarizer and LCD module .

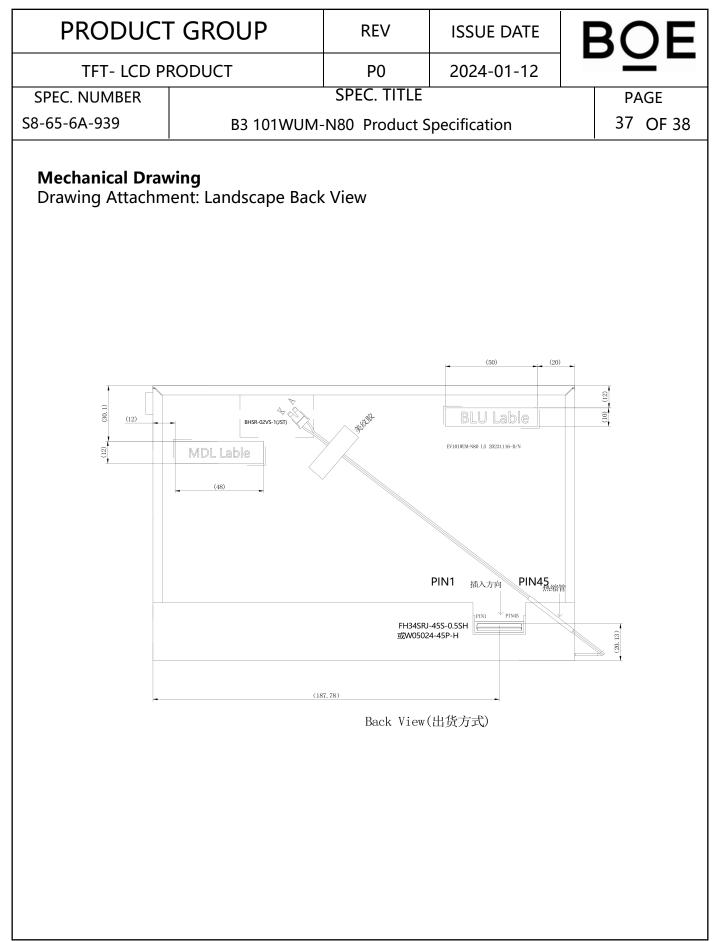
e. Do not exceed the absolute maximum rating value. (supply voltage variation, input v oltage variation, variation in part contents and environmental temperature, and so on) Otherwise the Module may be damaged.

f. Product reliability and functions are only guaranteed when the product is used under right operation usages. If product will be used in extreme conditions such as high tem perature, high humidity, high altitude, highlight, special display images, running time, lo ng time operation, outdoor operation, etc. It is strongly recommended to contact BOE for filed application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, ba nks, stock market and controlling systems.

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 Operating usages to protect against image sticking due to long-term static display. a. Suitable operating time: under 20 hours a day. b. Static information display recommended to use with moving image. Cycling display between 5 minutes' information(static) display and 10 seconds' moving image. c. Background and character (image) color change Use different colors for background and character, respectively. Change colors themselves periodically. d. Avoid combination of background and character with large different luminance. Abnormal condition just means conditions except normal condition. Black image or moving image is strongly recommended as a screen save Lifetime in this spec. is guaranteed only when Commercial Display is used according to operating usages. 							
10.8 Other Precautions							
 A. LC Leak If the liquid crystal material leaks from the panel, it is recommended to wash the LC with acetone or ethanol and then burn it. 							

- If the liquid crystal material leaks from the panel, it should be kept away from the ey es or mouth. In case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- If LC in mouth, mouth need to be washed, drink plenty of water to induce vomiting and follow medical advice.
- If LC touch eyes, eyes need to be washed with running water at least 15 minutes.
 B. Rework
- When returning the module for repair or etc., Please pack the module not to be bro ken. We recommend to use the original shipping packages.





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	PIN1	PIN45	220±5	

Back View(cable线长度)