Features

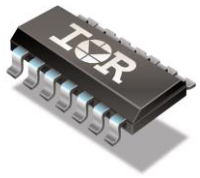
- Integrated 600 V full-bridge gate driver
- CT, RT programmable oscillator
- 15.6 V Zener clamp on V_{CC}
- Micropower startup
- Logic level latched shutdown pin
- Non-latched shutdown on CT pin (1/6th V_{CC})
- Internal bootstrap FETs
- Excellent latch immunity on all inputs & outputs
- ESD protection on all pins
- 14-lead SOIC or PDIP package
- 0.5 or 1.0μs (typ.) internal dead time
- RoHS compliant

Product Summary

Topology	Full-bridge
V_{OFFSET}	600 V
I_{o+} & I_{o-} (typical)	180 mA & 260 mA
Deadtime (typical)	1.0 μs (IRS2453D) 0.5 μs (IRS24531D)

Package Options

14 Lead PDIP
IRS2453DPbF



14 Lead SOIC
(Narrow Body)
IRS2453(1)DSPbF

Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
IRS2453D(S)	PDIP14	Tube/Bulk	25	IRS2453DPBF
	SOIC14N	Tube/Bulk	55	IRS2453DSPBF
		Tape and Reel	2500	IRS2453DSTRPBF
IRS24531DS	SOIC14N	Tube/Bulk	55	IRS24531DSPBF
		Tape and Reel	2500	IRS24531DSTRPBF

 © 2016 International Rectifier

Table of Contents	Page
Ordering Information	1
Description	3
Typical Connection Diagram	3
Qualification Information	4
Absolute Maximum Ratings	5
Recommended Operating Conditions	6
Recommended Component Values	6
Electrical Characteristics	7
Functional Block Diagram	9
Input / Output Pin Equivalent Circuit Diagram	10
Lead Definitions	11
Lead Assignments	11
Application Information and Additional Details	12
Package Details	15
Tape and Reel Details	16
Part Marking Information	17

 © 2016 International Rectifier